# TOSHIBA

# MICROWAVE SEMICONDUCTOR TECHNICAL DATA

# MICROWAVE POWER MMIC AMPLIFIER

# JS9766-AS (CHIP FORM)

#### **FEATURES**

■ High Output Power

P1dB=25.0dBm

■ 50 Ω Input / Output Broadband Matched

@ f=21.2-23.6GHz.

High Power Gain

G1dB=14dB

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

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|------------------------------------|--------|------|------------|--|
| CHARACTERISTICS                    | SYMBOL | UNIT | RATINGS    |  |
| DRAIN SUPPLY VOLTAGE               | VDD    | v    | 11         |  |
| GATE SUPPLY VOLTAGE                | VGG    | V    | -5         |  |
| INPUT POWER                        | Pin    | dBm  | 16         |  |
| FLANGE TEMPERATURE                 | Tf     | °C   | -30 - +80  |  |
| STORAGE TEMPERATURE                | Tstg   | °C   | -65+ - 175 |  |

RF PERFORMANCE SPECIFICATIONS (Ta=25 °C)

| CHARACTERISTICS        | SYMBOL  | CONDITION  | UNIT | MIN.         | TYP.     | MAX. |
|------------------------|---------|------------|------|--------------|----------|------|
| Operaing Frequency     | f       |            | GHz  | 21.2         | <u> </u> | 23.6 |
| Output Power at 1dB    | P1dB    |            | dBm  | 23.0         | 25.0     | _    |
| Gain Compression Point |         | VDD=6V     |      |              |          |      |
| Power Gain at 1dB      | G1dB    | VGG ≈-0.3V | dB   | 12.0         | 14.0     | _    |
| Gain Compression Point |         | IDD RFoff  |      |              |          |      |
| Drain Current          | IDD     | ≈500mA     | mA   | <del>-</del> | 510      | 700  |
| VSWRin(small signal)   | VSWRin  |            |      | _            | 1.7      | 1.9  |
| VSWRout(small signal)  | VSWRout |            |      |              | 1.7      | 1.9  |

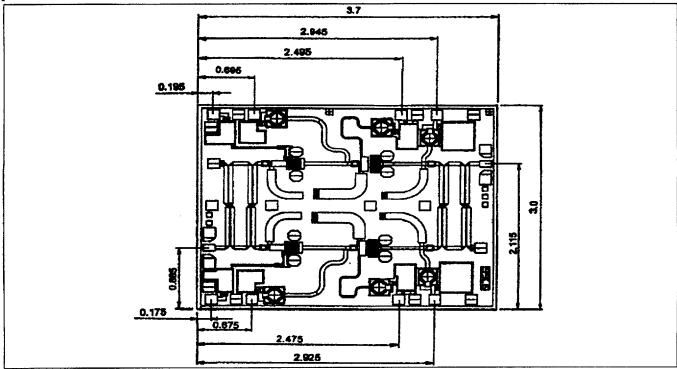
TOSHIBA CORPORATION

May 1998

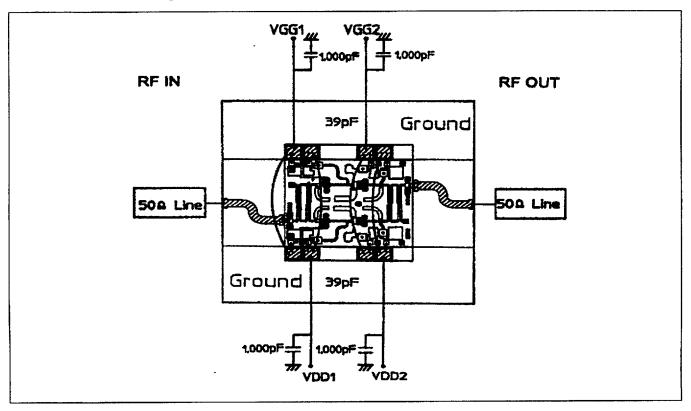
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The information contained herein may be changed without prior notice. It is therefore advisable to contact TOSHIBA before proceeding with design of equipment incorporating this product.

Chip Outline



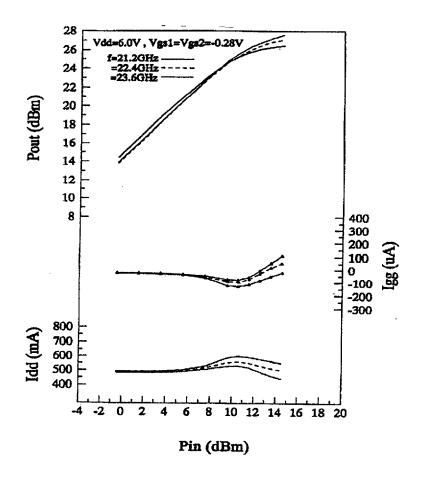
# **Recommended Bias Configuration**



#### **Assembly Precautions For Chip**

The operations must be performed in a clean and dry environment. The chips must be kept in the same environment when they are not used. All test instruments, assembly machines, benches, tweezers and operators should be grounded to avoid damage due to electronic discharge. Careful attention must be paid in handling chips with tweezers because GaAs is more brittle than Si.

#### **Typical RF Performance**



# Typical RF Performance(Cont'd)

