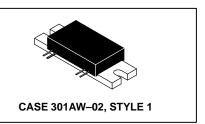
The RF MOSFET Line **RF Power Field Effect Amplifier**N-Channel Enhancement-Mode Lateral MOSFET

MHW1910-1

Specified 26 Volts, 1930–1990 MHz, Class AB Characteristics
 Output Power = 14 Watts CW Typ
 Power Gain = 26 dB Typ @ 10 Watts
 Efficiency = 34% Min @ 10 Watts

10 W, 1930-1990 MHz RF POWER AMPLIFIER

- 50 Ω Input/Output System
- Designed for GSM Linearity Requirements



MAXIMUM RATINGS

Rating	Symbol	Symbol Value	
DC Supply Voltage	٧S	28	Vdc
DC Bias Voltage	V _{bias}	28	Vdc
RF Input Power	P _{in}	21	dBm
RF Output Power	Pout	20	W
Operating Case Temperature Range	T _C	– 10 to +90	°C
Storage Temperature Range	T _{stg}	- 30 to +100	°C

ELECTRICAL CHARACTERISTICS (T_C = +25°C, V_S = 26 Vdc; V_{bias} = 5 Vdc; 50 Ω system, unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Frequency Range	BW	1930	_	1990	MHz
Quiescent Current (Pin = 0 mW)	I _{DQ}	100	_	150	mA
Bias Current	I _{bias}	_	_	2	mA
Output Power at 1 dB Compression	P _{1dB}	10	14	_	W
Power Gain (P _{out} = 10 W)	GP	24	26	28	dB
Efficiency (P _{Out} = 10 W)	η	34	_	_	%
Input VSWR	VSWRin	_	_	1.8:1	_
Harmonics at 2f ₀	H ₂	_	_	- 35	dBc
Harmonics at 3f ₀	Нз	_	_	- 45	dBc
Reverse IMD; P _{out} = 10 W; Preverse = -40 dBc (F1 = F0 ±200 kHz @ -40 dBc)	IMD _r	_	_	- 50	dBc
Load Mismatch Stress Load VSWR = 5:1, All Phase Angles	Ψ	No Degradation in Output Power			
Stability (P _{OUt} = 10 mW to 10 W, V _S ≤ 26 Vdc) Load VSWR = 5:1, All Phase Angles	_	All Spurious Outputs More Than 60 dB Below Desired Signal			

 $NOTE - \underline{\textbf{CAUTION}} - MOS$ devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.



EXTREME CASE ELECTRICAL CHARACTERISTICS (T_C = -10 to +85°C, V_S = 23.5 to 26 Vdc, V_{bias} = 3 to 26 Vdc, 50 9 system, unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Frequency Range	BW	1930	_	1990	MHz
Quiescent Current (Pin = 0 mW)	I _{DQ}	100	_	160	mA
Bias Current	I _{bias}	_	_	2	mA
Output Power at 1 dB Compression	P _{1dB}	8	_	_	W
Power Gain Variation for a Given Part (Pout = 10 W)	GP	_	5	6.5	dB
Efficiency (Pout = 10 W)	η	32	_	_	%
Input VSWR	VSWR _{in}	_	_	2:1	_
Harmonics at 2f ₀	H ₂	_	_	- 35	dBc
Harmonics at 3f ₀	Н3	_	_	– 45	dBc
Reverse IMD; P _{out} = 10 W; Preverse = -40 dBc (F1 = F0 ±200 kHz @ -40 dBc)	IMD _r	_	_	- 46	dBc
Load Mismatch Stress Load VSWR = 5:1, All Phase Angles	Ψ	No Degradation in Output Power			
Stability (P _{OUt} = 10 mW to 10 W, V _S ≤ 26 Vdc) Load VSWR = 5:1, All Phase Angles	_	All Spurious Outputs More Than 60 dB Below Desired Signal			

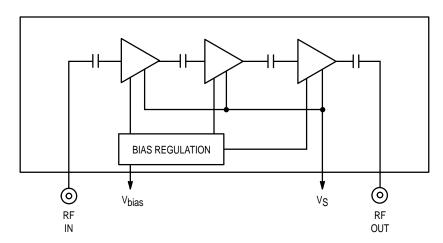


Figure 1. Internal Diagram

TYPICAL CHARACTERISTICS

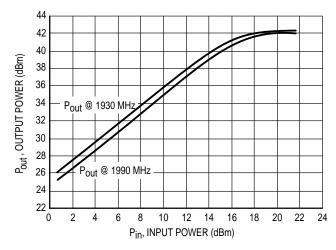


Figure 2. Output Power versus Input Power

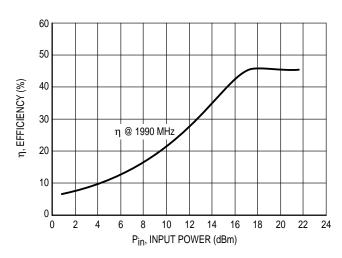


Figure 3. Efficiency versus Input Power

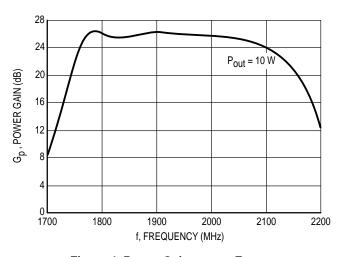


Figure 4. Power Gain versus Frequency

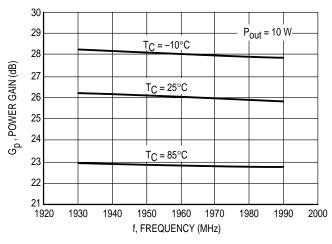


Figure 5. Gain versus Frequency

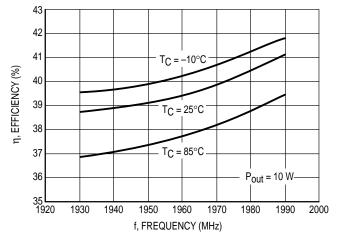


Figure 6. Efficiency versus Frequency

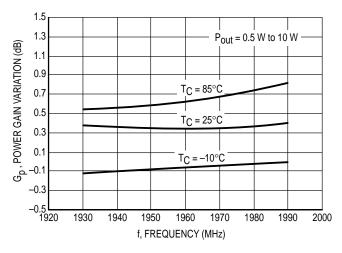
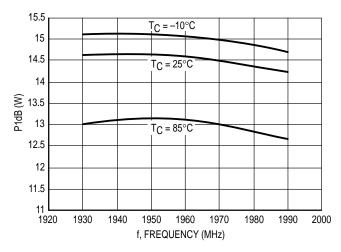


Figure 7. Power Gain Variation versus Frequency

TYPICAL CHARACTERISTICS



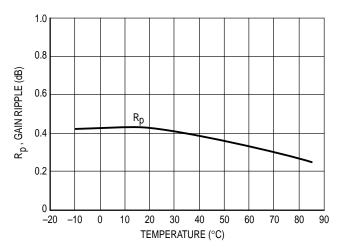


Figure 8. P1dB versus Frequency

Figure 9. Gain Ripple versus Temperature

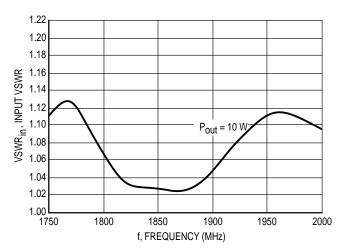
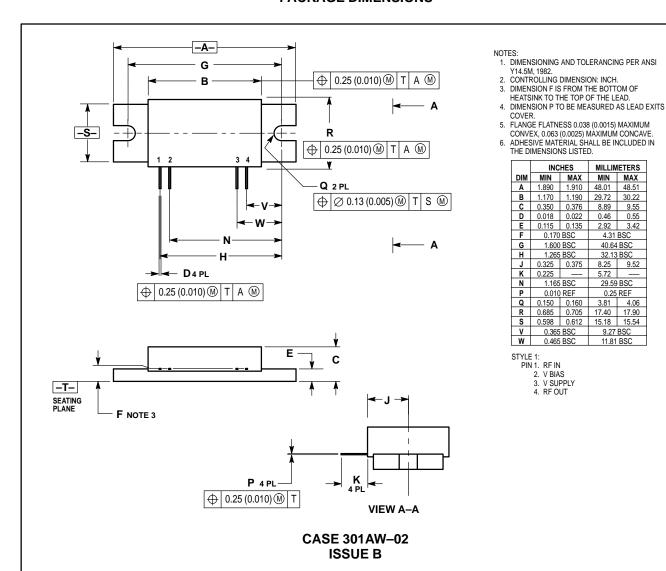


Figure 10. Input VSWR

PACKAGE DIMENSIONS



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MILLIMETERS

MIN MAX

0.46 0.55

4.31 BS

40.64 BSC

32.13 BSC

29.59 BSC

0.25 REF

3.81 4.06 17.40 17.90

15.18 15.54

9.27 BSC

8.25 9.52

48.51

48.01

29.72 30.22

8.89 9.55

2.92 3.42

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