

HFP10N60

600V N-Channel MOSFET

FEATURES

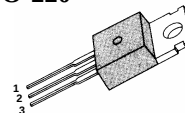
- ❑ Originative New Design
- ❑ Superior Avalanche Rugged Technology
- ❑ Robust Gate Oxide Technology
- ❑ Very Low Intrinsic Capacitances
- ❑ Excellent Switching Characteristics
- ❑ Unrivalled Gate Charge : 15 nC (Typ.)
- ❑ Extended Safe Operating Area
- ❑ Lower $R_{DS(ON)}$: 0.64 Ω (Typ.) @ $V_{GS}=10V$

$$BV_{DSS} = 600 V$$

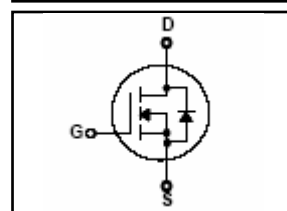
$$R_{DS(on) typ} = 0.64 \Omega$$

$$I_D = 9.5 A$$

TO-220



1.Gate 2. Drain 3. Source



Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Value	Units
V_{DSS}	Drain-Source Voltage	600	V
I_D	Drain Current – Continuous ($T_C = 25^\circ\text{C}$)	9.5	A
	Drain Current – Continuous ($T_C = 100^\circ\text{C}$)	6.03	A
I_{DM}	Drain Current – Pulsed (Note 1)	38	A
V_{GS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	520	mJ
I_{AR}	Avalanche Current (Note 1)	9.5	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	15.6	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	5.5	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	156	W
	– Derate above 25°C	1.25	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Thermal Resistance Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	--	0.8	$^\circ\text{C}/\text{W}$
$R_{\theta CS}$	Case-to-Sink	0.5	--	
$R_{\theta JA}$	Junction-to-Ambient	--	62.5	

Electrical Characteristics $T_C=25\text{ }^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
On Characteristics						
V _{GS}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.5	--	4.5	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 4.75 A	--	0.64	0.8	Ω
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	600	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25 °C	--	0.65	--	V / °C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V	--	--	1	μA
		V _{DS} = 480 V, T _C = 125 °C	--	--	10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	1300	1700	pF
C _{oss}	Output Capacitance		--	110	145	pF
C _{rss}	Reverse Transfer Capacitance		--	23	30	pF
Switching Characteristics						
t _{d(on)}	Turn-On Time	V _{DS} = 300 V, I _D = 9.5 A, R _G = 25 Ω (Note 4,5)	--	27	55	ns
t _r	Turn-On Rise Time		--	75	150	ns
t _{d(off)}	Turn-Off Delay Time		--	120	240	ns
t _f	Turn-Off Fall Time		--	80	160	ns
Q _g	Total Gate Charge	V _{DS} = 480V, I _D = 9.5 A, V _{GS} = 10 V (Note 4,5)	--	33	43	nC
Q _{gs}	Gate-Source Charge		--	7.3	--	nC
Q _{gd}	Gate-Drain Charge		--	15	--	nC
Source-Drain Diode Maximum Ratings and Characteristics						
I _S	Continuous Source-Drain Diode Forward Current		--	--	9.5	A
I _{SM}	Pulsed Source-Drain Diode Forward Current		--	--	38	
V _{SD}	Source-Drain Diode Forward Voltage	I _S = 9.5 A, V _{GS} = 0 V	--	--	1.4	V
t _{rr}	Reverse Recovery Time	I _S = 9.5 A, V _{GS} = 0 V	--	430	--	ns
Q _{rr}	Reverse Recovery Charge	di _F /dt = 100 A/μs (Note 4)	--	5.8	--	μC

Notes ;

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. $L=10.56\text{ mH}$, $I_{AS}=9.5\text{ A}$, $V_{DD}=50\text{ V}$, $R_G=25\text{ }\Omega$, Starting $T_J=25\text{ }^{\circ}\text{C}$
3. $I_{SD} \leq 9.5\text{ A}$, $di/dt \leq 300\text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25\text{ }^{\circ}\text{C}$
4. Pulse Test : Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2\%$
5. Essentially Independent of Operating Temperature

Typical Characteristics

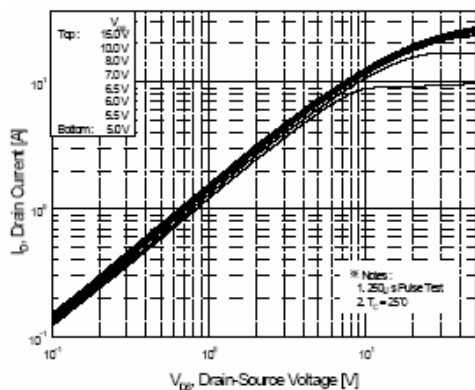


Figure 1. On Region Characteristics

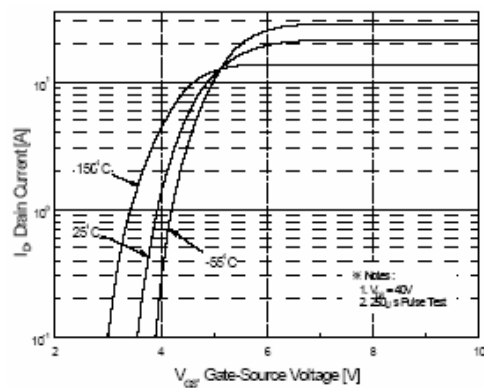


Figure 2. Transfer Characteristics

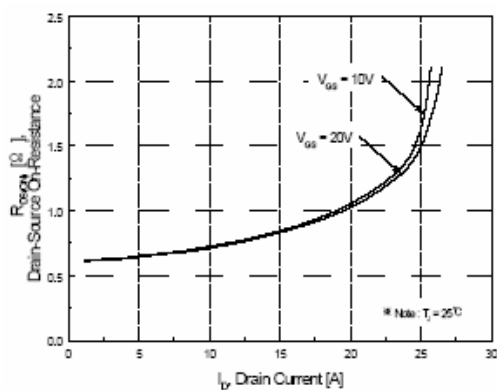


Figure 3. On Resistance Variation vs. Drain Current and Gate Voltage

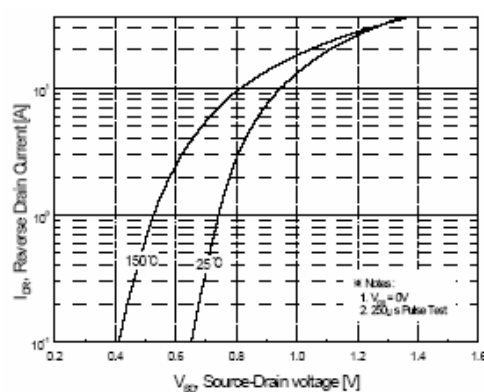


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

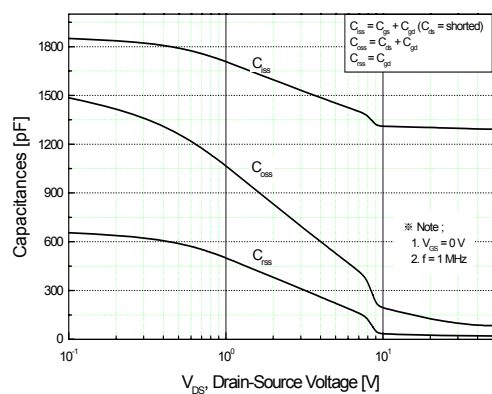


Figure 5. Capacitance Characteristics

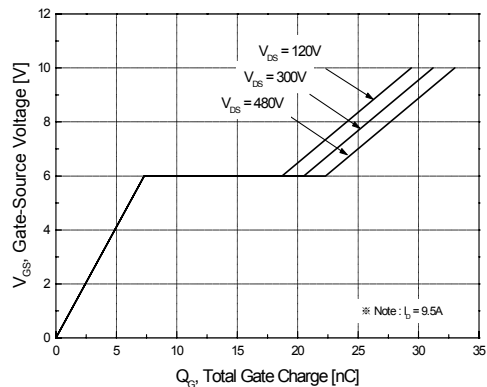


Figure 6. Gate Charge Characteristics

Typical Characteristics (continued)

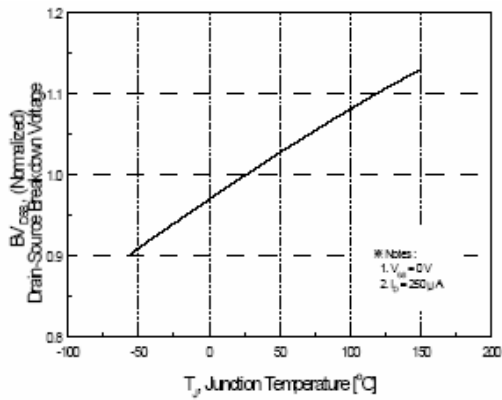


Figure 7. Breakdown Voltage Variation vs Temperature

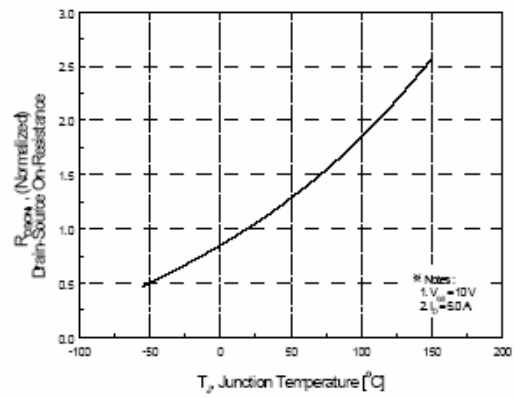


Figure 8. On-Resistance Variation vs Temperature

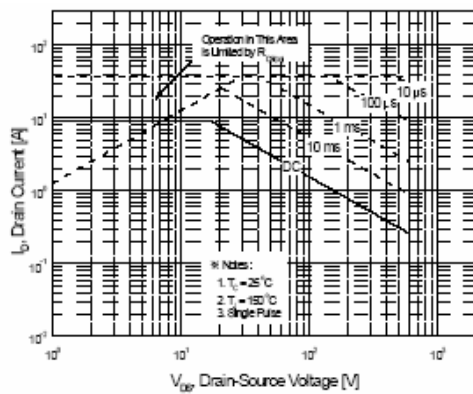


Figure 9. Maximum Safe Operating Area

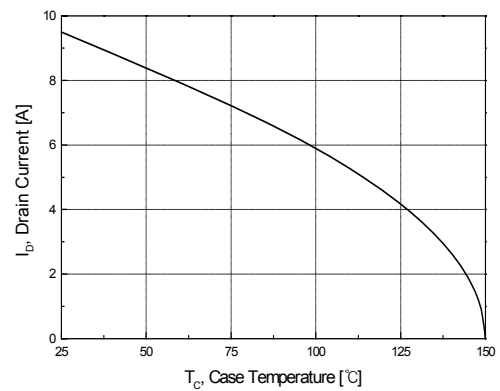


Figure 10. Maximum Drain Current vs Case Temperature

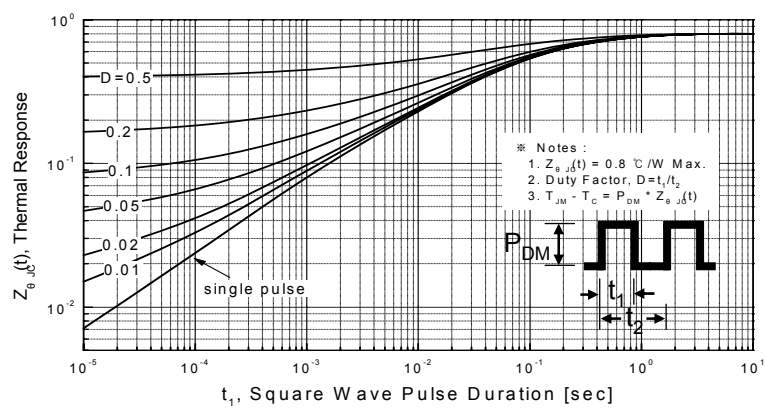


Figure 11. Transient Thermal Response Curve

Fig 12. Gate Charge Test Circuit & Waveform

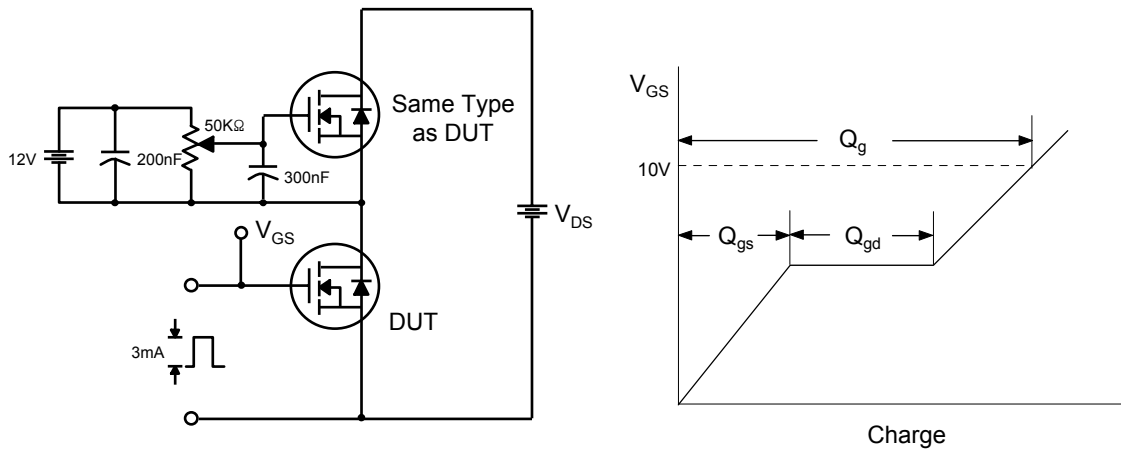


Fig 13. Resistive Switching Test Circuit & Waveforms

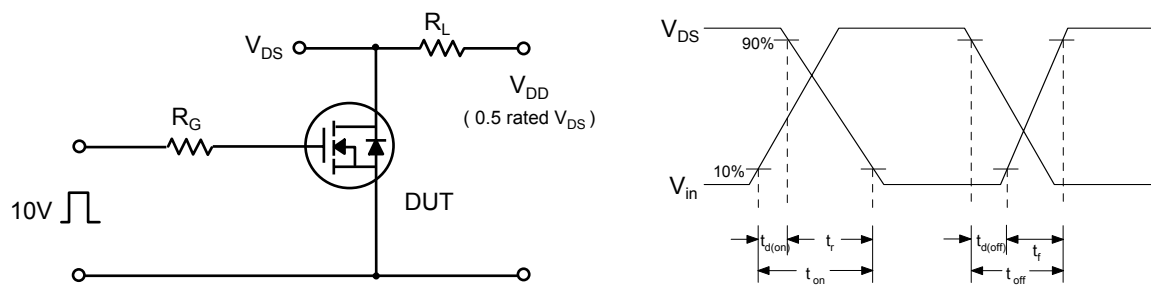


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms

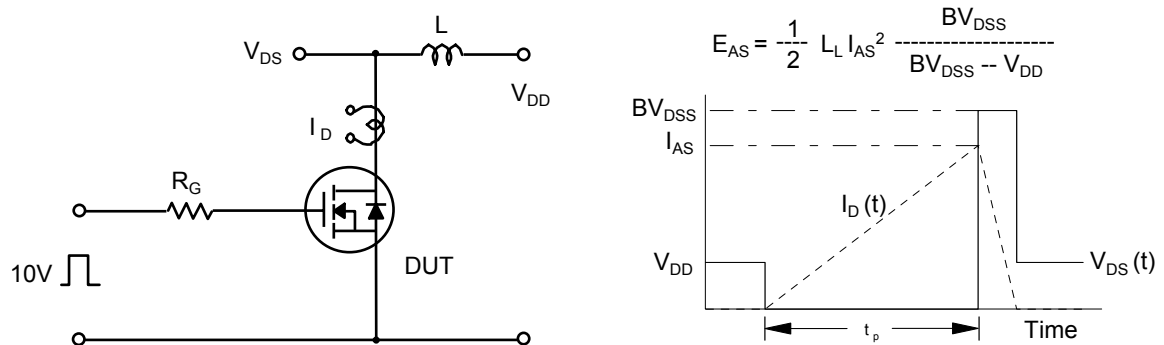
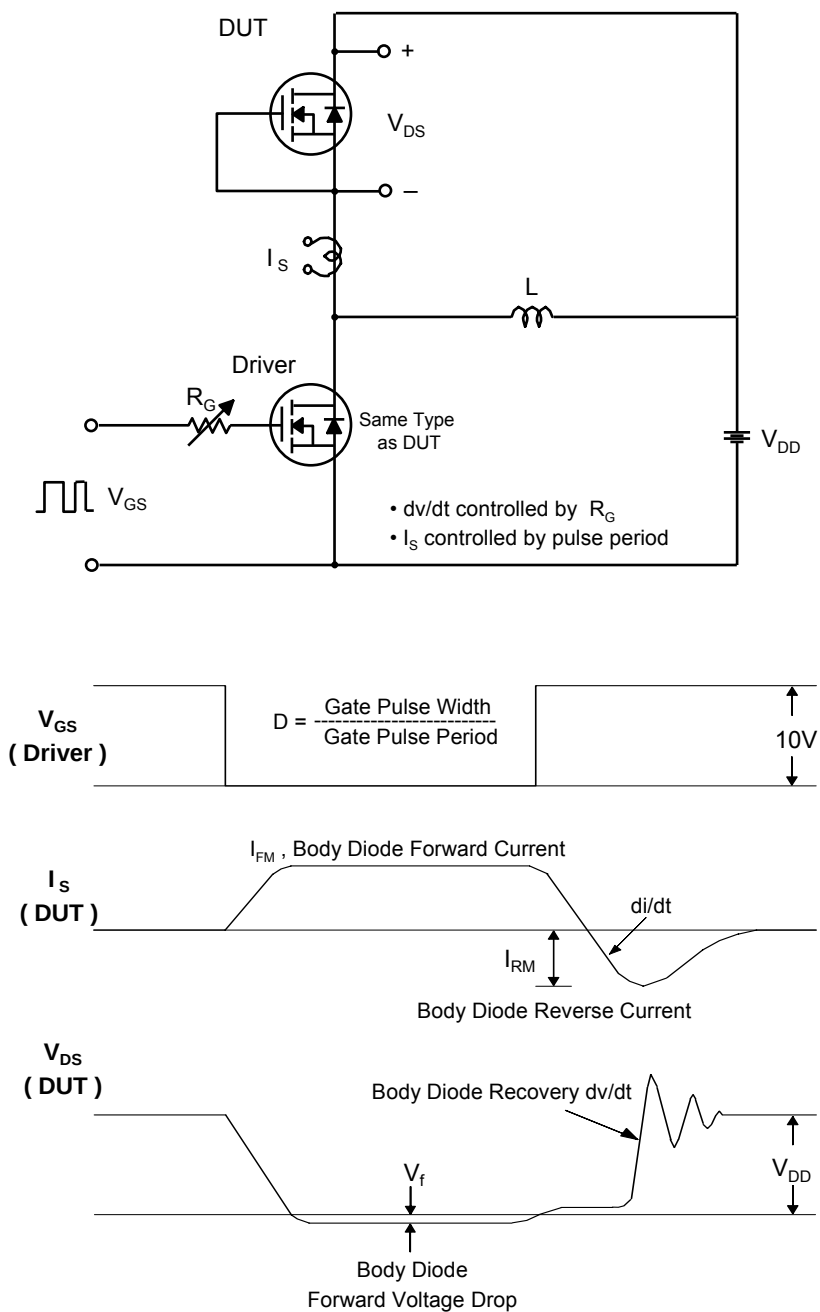
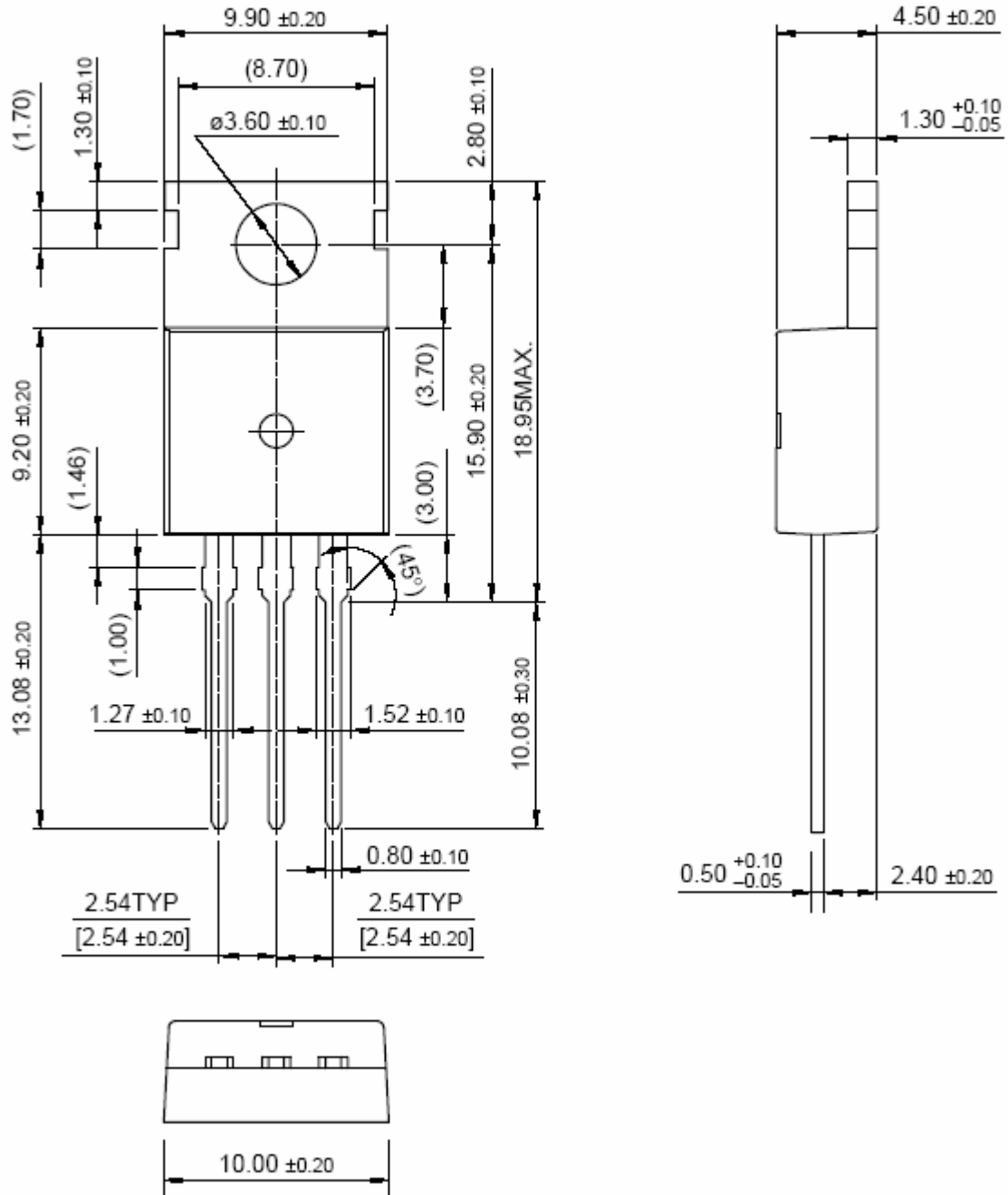


Fig 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



Package Dimension

TO-220



Reliability Qualification

A. High Temperature Reverse Bias (HTRB)

The purpose of this test is to determine the sensitivity of the product to mobile ion contamination and related failure mechanisms.

Conditions: JESD22-A108, JIS C 7021 B-8

$T_A=150^{\circ}\text{C}$ $V_{DS}=80\%$ max rated V_{DS}

Sample Size	#of Fail	Cum. Fail%	168hrs	300hrs
45	0	0.0%	0	0

B. High Temperature Gate Bias (HTGB)

The purpose of this test is to determine the sensitivity of the product to mobile ion contamination between gate and source and related failure mechanisms.

$T_A=150^{\circ}\text{C}$ $V_{DS}=V_{GSS}$ max

Sample Size	#of Fail	Cum. Fail%	168hrs	300hrs
45	0	0.0%	0	0

C. Pressure Cooker Test (PCT)

Autoclave (ACLV)

The purpose of this test is to evaluate the moisture resistance of non-hermetic components under pressure/temperature conditions.

Conditions: JESD22-A102, JIS C 7021 A-6

$T_A=121^{\circ}\text{C}$ RH=100% P=1 atmosphere (15psig)

Sample Size	#of Fail	Cum. Fail%	48hrs
45	0	0.0%	0

Reliability Qualification (Continued)

D. Temperature Humidity Bias (THBT)

The purpose of this test is to evaluate the moisture resistance of non-hermetic components.

The addition of voltage bias accelerates the corrosive effect after moisture penetration has taken place. with time, this is a catastrophically destructive test.

Conditions: JESD22-A101

$T_A = 85^{\circ}\text{C}$ RH=85% $V_{DS} = 80\%$ max rated V_{DS}

Sample Size	#of Fail	Cum. Fail%	168hrs	300hrs
45	0	0.0%	0	0

E. High Temperature Storage Life (HTSL)

The purpose of this test is to expose time/temperature failure mechanisms and to evaluate long-term strong stability.

Conditions: JESD22-A103, JIS C 7021 B-10

$T_A = T_{stg}(\text{max})$

Sample Size	#of Fail	Cum. Fail%	168hrs	300hrs
45	0	0.0%	0	0

F. Temperature Cycle Air-to Air (TMCL)

The purpose of this test is to evaluate the ability of the device to withstand both exposure to extreme temperature and the transition between temperature extreme, and to exposure excessive thermal mismatch between materials.

Conditions: JESD22-A104, JIS C 7021 A-4

Air to air, $-65^{\circ}\text{C} \sim 150^{\circ}\text{C}$, 15 minutes dwell time at each temperature

Sample Size	#of Fail	Cum. Fail%	100cycles	200cycles
45	0	0.0%	0	0