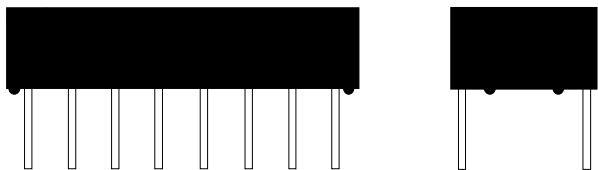
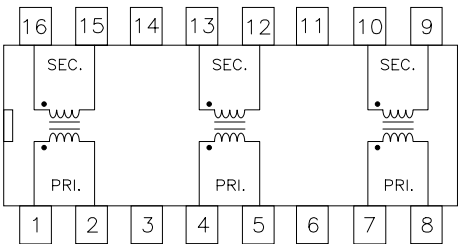


ETHERNET APPLICATIONS TRANSFORMERS



SCHEMATIC DIAGRAM



**IEEE 802.3 (10 BASE 2, 10 BASE 5)
and ECMA Compatible**
High Isolation 2000Vrms
Fast Rise Times
DIL Construction

ELECTRICAL SPECIFICATIONS AT 25° C

Part No.	Turns Ratio (±5%)	OCL (μH) (±20%)	E-T VμS min.	Rise Time (ns max)	Pri./Sec. C _{ww} (pF max)	L _L (μH) max.	DCR (Ω) max.
T-50701	1:1	35	1.8	3.0	8	.20	.20
T-50702	1:1	50	2.1	3.0	9	.20	.20
T-50703	1:1	75	2.3	3.0	10	.20	.20
T-50704	1:1	100	2.7	3.5	10	.20	.25
T-50705	1:1	150	3.0	3.5	12	.25	.30
T-50706	1:1	200	3.5	3.5	15	.25	.40
T-50707	1:1	250	3.5	3.5	15	.25	.40

16 Pin DIL Package Dimensions in inches (mm)

