EB51F5 Series

- Temperature Compensated Crystal Oscillator (TCXO)
- HCMOS Output
- 5.0V Supply Voltage
- Stability to 1.5ppm
- Internal mechanical trim
- External voltage control option available





NOTES

ELECTRICAL SPECIFICATIONS

Frequency Range					1.544MHz to 44.736MHz			
Operating Temperature Range					See Table 1			
Storage Temperature Range				-55°C to 125°C				
Supply Voltage (V _{DD})				5.0V _{DC} ±5%	5.0V _{DC} ±5%			
Input Current		Measured at Steady State at 25°C, at Nominal V _{DD} , at		t 15mA Maxi	15mA Maximum ≤ 20.000MHz,			
		Nominal V_c		30mA Maxi	30mA Maximum > 20.000MHz			
Frequency Stability		vs. Initial Frequency Tolerance		±1.0ppm (a	± 1.0 ppm (at Nominal V_{DD} and V_{C} , at 25°C)			
		vs. Operating Temperature Range		See Table 1	See Table 1 (at Nominal V _{DD} and V _C)			
		vs. Input Voltage (V _{DD} ±5%)		±0.3ppm M	±0.3ppm Maximum			
		vs. Load (±10%)	±0.2ppm M	±0.2ppm Maximum				
Aging (at 25°C) ±1ppm / year Max				ear Maximum				
Output Voltage Logic High (V _{OH})				V _{DD} - 0.5V _D	V _{DD} - 0.5V _{DC} Minimum			
Output Voltage Logic I	Low (V _{OL})			0.5V _{DC} Max	0.5V _{DC} Maximum			
Rise Time / Fall Time		20% to 80% of Wavefo	20% to 80% of Waveform		6 nSeconds Maximum			
Duty Cycle at 50% of Waveform			50 ±5(%)	50 ±5(%)				
Load Drive Capability				30pF HCMOS Load Maximum				
Control Voltage Range				$0.0V_{DC}$ to V	0.0V _{DC} to V _{DD}			
Control Voltage (Exter	mal)	Positive Transfer Char	2.5V _{DC} ±2.0	$2.5V_{DC} \pm 2.0V_{DC}$				
Frequency Deviation		Referenced to F_0 at $V_C = 2.5V_{DC}$, $V_{DD} = 5.0V_{DC}$		±7ppm Mir	±7ppm Minimum, ±20ppm Maximum			
Linearity			±10% Maxi	±10% Maximum				
Internal Trim		Measured at 25°C, V_{DD} =5.0 V_{DC} , V_{C} =2.5 V_{DC}		±3ppm Mir	±3ppm Minimum (Top Access)			
Input Impedance		10k0hms Typical						
Phase Noise (at 19.440MHz) Measured at 25°C, at Nominal V _{DD} , at Nominal V _C								
		at 10Hz Offset		-70dBc/Hz	-70dBc/Hz Typical			
		at 100Hz Offset		-100dBc/H	-100dBc/Hz Typical			
		at 1kHz Offset		-130dBc/H	-130dBc/Hz Typical			
		at 10kHz Offset		-140dBc/H	-140dBc/Hz Typical			
		at 100kHz Offset	-145dBc/H	-145dBc/Hz Typical				
MANUFACTURER CA	ATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV . DATE		

PART NUMBERING GUIDE

EB51F5 G 15 A V - 12.800M - G



AVAILABLE OPTIONS

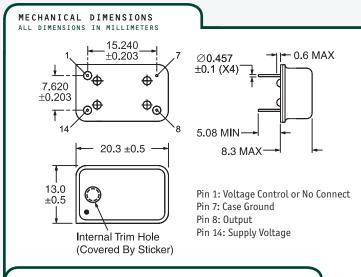
Blank=None (Standard)
CB=Cut Leads to 2.540 ±0.500 (0.100" ±0.020")
CC=Cut Leads to 3.175 ±0.500 (0.125" ±0.020")
CD=Cut Leads to 3.810 ±0.500 (0.150" ±0.020")
CE=Cut Leads to 4.445 ±0.500 (0.175" ±0.020")
G=Full Size Gull Wing

FREQUENCY

EXTERNAL TRIM

N=None (No Connection on Pin 1) V=Voltage Control on Pin 1

	TABLE 1: PART NUMBERING CODES										
Range			Frequency Stability X = Available from 1.544MHz to 32.768MHz Y = Available at any Frequency								
Ire R			±1.5ppm	±2.0ppm	±3.0ppm	±5.0ppm					
eratu		Code	15	20	30	50					
du:	0°C to +50°C	Α	Υ	Υ	Υ	Υ					
Operating Temperature	0°C to 70°C	В	Х	Υ	Υ	Υ					
	-20°C to +70°C	С	Х	Х	Υ	Υ					
	-30°C to +75°C	D		Х	Υ	Υ					
	-40°C to +85°C	E			Х	Y					



MARKING SPECIFICATIONS

Line 1: ECLIPTEK

Line 2: XX.XXX M M=MHz

—Frequency (5 Digits Maximum + Decimal)

Line 3: XX Y ZZ

Week of Year

Last Digit of Year

Ecliptek Manufacturing Identifier

Note: Pin 1 shall be designated with a dot

ENVIRONMENTAL/MECHANICAL SPECIFICATIONS

CharacteristicSpecificationFine Leak TestMIL-STD-883, Method 1014, Condition A (Internal Crystal Only)Gross Leak TestMIL-STD-883, Method 1014, Condition C (Internal Crystal Only)Mechanical ShockMIL-STD-202, Method 213, Condition CVibrationMIL-STD-883, Method 2007, Condition A

Lead Integrity MIL-STD-883, Method 2004
Solderability MIL-STD-883, Method 2002
Temperature Cycling MIL-STD-883, Method 1010
Resistance to Soldering Heat
Resistance to Solvents MIL-STD-883, Method 215

MANUFACTURER CATEGORY SERIES PACKAGE VOLTAGE CLASS REV.DATE ECLIPTEK CORP. OSCILLATOR EB51F5 14 pin DIP 5.0V OS3A 06/04