XO5019 Series

2x2 inch, 12.0 Volt, CMOS/Sinewave, OCXO





- Low aging and phase noise
- External or voltage control

Model XO5019		Temperature Range (°C)	Temperature Stability	Aging First Year	Output	Supply Voltage
SC-Cut	10	-20 to +70	±1.0 x 10 ⁻⁸	±1.5 x 10 ⁻⁷	Sinewave	+15 Vdc oven (+12 Vdc osc)
AT-Cut	10	-20 to +70	±5.0 x 10 ⁻⁸	±2.0 x 10 ⁻⁷	Sinewave	+15 Vdc oven (+12 Vdc osc)
Options	5 to 15	See	Table	Consult Factory	TTL/CMOS	+10 Vdc to 24 Vdc

	ecifica	

Aging daily $\pm 5.0 \times 10^{-10}$ for SC-cut

Turn on 600 mA max

Current 300 mA max @ 25°C steady state

Warm-up time @ 25°C, to within ±0.10 ppm of Fo in 2.5 min

±0.01 ppm in 5 min

Load Stability $\pm 1.0 \times 10^{-9}$

Aging adjustment External 10 k pot/voltage

Voltage range 0.5 V to 10 VDeviation $\pm 3.0 \text{ to } 10^{-7}$ Slope Positive

Phase Noise @	(SC-cut)	(AT-cut)
1 Hz	-90 dBc/Hz	-75 dBc/Hz
10 Hz	-120 dBc/Hz	-110 dBc/Hz
100 Hz	-145 dBc/Hz	-140 dBc/Hz
1 kHz	-155 dBc/Hz	-155 dBc/Hz
10 kHz	-160 dBc/Hz	-160 dBc/Hz
a :		

Sinewave Version

 Harmonics
 -20 dBc max

 Spurious
 -75 dBc max

 Load
 50 Ω to 3 k Ω

 Output level
 17 dDe min interest

Output level +7 dBm min into 50Ω

HCMOS Version

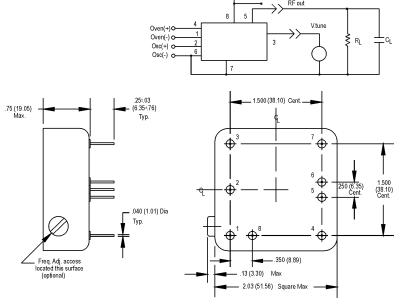
Duty cycle 40/60 Load 2 Gates

Optional Temperature Ranges and Frequncy Stabilities				
Temperature Range (°C)	Stability Limits			
	SC-cut	AT-cut		
0 to +50	±2.0 x 10 ⁻⁹	±1.0 x 10 ⁻⁸		
0 to +70	±3.0 x 10 ⁻⁹	±3.0 x 10 ⁻⁸		
-10 to +70	±4.0 x 10 ⁻⁹	±3.0 x 10 ⁻⁸		
-30 to +70	±5.0 x 10 ⁻⁹	±5.0 x 10 ⁻⁸		
-40 to +70	±7.0 x 10 ⁻⁹	±1.0 x 10 ⁻⁷		
-55 to +70	±3.0 x 10 ⁻⁹	±5.0 x 10 ⁻⁷		

VCC

5K oven ready





MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.