



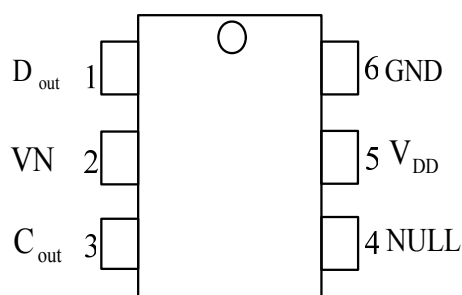
ONE-CELL LI-ION BATTERY PROTECTION IC

FEATURES

- Ideal for One-Cell Rechargeable Li-Ion Battery Packs
- High Accuracy Voltage Detection
- Low Current Consumption:
 - 3 μ A Supply Current (Typical)
 - 0.1 μ A Shutdown Current
- 3-Level Over Current Detection:
 - Over-Current Level 1 /Over Current Level 2 / Short Circuit
- Wide Operating Temperature Range:
 - 40°C to 85°C
- Small SOT26 Package

PIN CONFIGURATION

TOP VIEW



(SOT26)

GENERAL DESCRIPTION

The AAT8661 series are designed to protect one-cell rechargeable Li-Ion battery pack against over-charge, over-discharge, over-current and short circuit. They use CMOS process to provide high accuracy voltage detection and low current consumption.

Each of the AAT8661 devices incorporates voltage comparators, bandgap reference voltage generator, signal delay circuit, short circuit detector, and digital control circuit.

In the charge process, when the battery voltage is charged to a value greater than V_{C1} (Over-Charge Threshold Voltage), the output of C_{out} pin switches to the low level, i.e., the V_N pin level. The output of C_{out} pin will switch to high level when the battery voltage falls lower than V_{C2} (Over-Charge Release Voltage), or when the charger is disconnected from the battery pack and the battery voltage level ranges between V_{C1} and V_{C2} .

During the discharge process, when the battery voltage drops to a value lower than V_{D1} (Over-Discharge Threshold Voltage), the output of D_{out} pin switches to low level immediately after the internal delay time elapses. The output of D_{out} pin will switch to high level when the battery voltage is at a level higher than V_{D2} (Over-Discharge Release Voltage).

Over current level 1 voltage (V_{OC1}) is used to

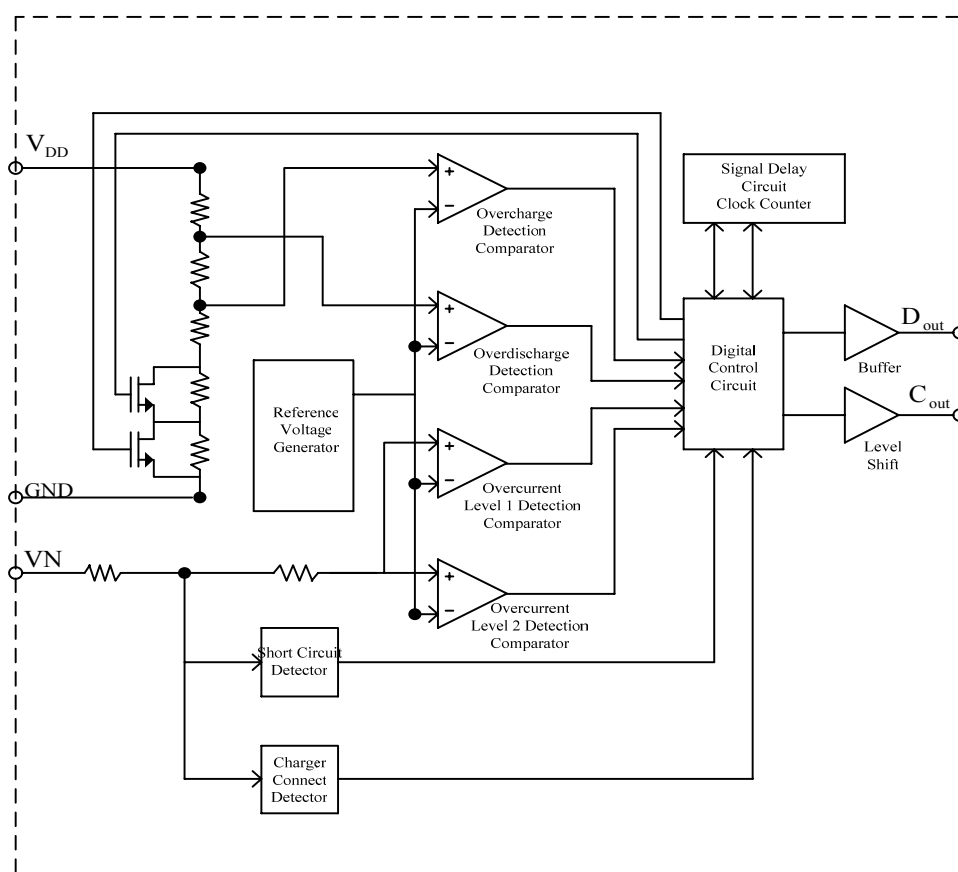


monitor the amount of discharge current. If the discharge current is high enough to cause VN pin voltage increase to a value greater than V_{OC1} , the output of D_{out} pin will switch to a low level after a delay time t_{OC1} . If the load is removed from battery pack, the output of D_{out} will change to a high level again.

The mechanism of short circuit protection is identical to a discharge current. If the short

circuit current is high enough to cause VN pin voltage increase to greater than V_{short} , the output of D_{out} pin will move to the low level after a delay time t_{short} , and the output of D_{out} level will change to high when the load is removed from battery pack.

BLOCK DIAGRAM:





PIN DESCRIPTION

PIN NO.	NAME	I/O	DESCRIPTION
1	D _{out}	O	Discharge Control Pin which Connects to External MOSFET Gate
2	VN	I	Voltage Detection Pin between VN and GND
3	C _{out}	O	Charge Control Pin which Connects to External MOSFET Gate.
4	NULL	×	Null Pad.
5	V _{DD}	I	Power Supply Input Pin
6	GND	I	Ground

ABSOLUTE MAXIMUM RATINGS

CHARACTERISTICS	SYMBOL	VALUE	UNIT
Supply Voltage	V _{DD}	-0.3 to 8	V
VN Pin Input Voltage	V _{VN}	V _{DD} - 20 to V _{DD} + 0.3	V
D _{out} Pin Output Voltage	V _{Dout}	-0.3 to V _{DD} + 0.3	V
C _{out} Pin Output Voltage	V _{Cout}	V _{VN} - 0.3 to V _{DD} + 0.3	V
Power Dissipation	P _d	150	mW
Operating Temperature Range	T _C	-40 to +85	°C
Storage Temperature Range	T _{storage}	-40 to +125	°C

RECOMMENDED OPERATING CONDITIONS

	TEST CONDITION	MIN	MAX	UNIT
Supply Voltage, V _{DD}	Voltage Defined as V _{DD} to GND	1.5	7.0	V
D _{out} Output Voltage		GND	V _{DD}	V
C _{out} Output Voltage		VN	V _{DD}	V

OPERATION VOLTAGE AND OPERATION CURRENT

PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Supply Current at Normal Operation Mode	V _{DD} =3.3V; VN=0V; GND=0V		3.0	5.0	μA
Standby Current at Power Down Mode		-	-	0.1	μA
Operation Voltage between V _{DD} and VN		1.5		20.0	V



AAT8661A DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.300	4.325	4.350	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.3$	$V_{C1}-0.25$	$V_{C1}-0.2$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.420	2.500	2.580	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}+0.3$	$V_{D1}+0.4$	$V_{D1}+0.5$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.4V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	130	150	170	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	500	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $VN = 0V$	1	2	10	k Ω
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $VN = 0V$	150	602	2,380	k Ω
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $VN = 0V$	2.5	5.0	10.0	k Ω
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $VN = 1.8V$	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $VN = 0V$	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $VN = 3.5V$	10	20	40	k Ω



AAT8661B DETECTION VOLTAGE and DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.325	4.350	4.375	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.2$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.220	2.300	2.380	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}+0.6$	$V_{D1}+0.7$	$V_{D1}+0.8$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.088	0.125	0.163	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	22.4	32	41.6	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	130	150	170	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	500	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	2.8	4.0	5.2	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{OUT} = 3.0V$; $VN = 0V$	1	2	10	k Ω
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{OUT} = 0.5V$; $VN = 0V$	150	602	2,380	k Ω
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $VN = 0V$	2.5	5.0	10.0	k Ω
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $VN = 1.8V$	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $VN = 0V$	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $VN = 3.5V$	10	20	40	k Ω



AAT8661C DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.275	4.300	4.325	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.2$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.220	2.300	2.380	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	80	100	120	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	480	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $V_N = 0V$	1	2	10	k Ω
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $V_N = 0V$	150	602	2,380	k Ω
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $V_N = 0V$	2.5	5.0	10.0	k Ω
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $V_N = 1.8V$	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $V_N = 0V$	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $V_N = 3.5V$	10	20	40	k Ω



AAT8661D DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.255	4.280	4.305	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.2$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.201	2.281	2.361	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	110	130	150	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	490	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $VN = 0V$	1	2	10	$k\Omega$
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $VN = 0V$	150	602	2,380	$k\Omega$
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $VN = 0V$	2.5	5.0	10.0	$k\Omega$
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $VN = 1.8V$	2.5	5.0	10.0	$k\Omega$
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $VN=0V$	100	300	900	$k\Omega$
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $VN = 3.5V$	10	20	40	$k\Omega$



AAT8661E DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.255	4.280	4.305	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.2$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.201	2.281	2.361	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}+0.5$	$V_{D1}+0.6$	$V_{D1}+0.7$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	80	100	120	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	480	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $V_N = 0V$	1	2	10	k Ω
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $V_N = 0V$	150	602	2,380	k Ω
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $V_N = 0V$	2.5	5.0	10.0	k Ω
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $V_N = 1.8V$	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $V_N = 0V$	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $V_N = 3.5V$	10	20	40	k Ω



AAT8661F DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.300	4.325	4.350	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.3$	$V_{C1}-0.25$	$V_{C1}-0.2$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.420	2.500	2.580	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}+0.3$	$V_{D1}+0.4$	$V_{D1}+0.5$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.4V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	80	100	120	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	480	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (When $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $V_N = 0V$	1	2	10	k Ω
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $V_N = 0V$	150	602	2,380	k Ω
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $V_N = 0V$	2.5	5.0	10.0	k Ω
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $V_N = 1.8V$	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $V_N = 0V$	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $V_N = 3.5V$	10	20	40	k Ω



AAT8661G DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.325	4.350	4.375	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.20$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.220	2.300	2.380	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}+0.6$	$V_{D1}+0.7$	$V_{D1}+0.8$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.088	0.125	0.163	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	22.4	32.0	41.6	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	180	200	220	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	510	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	2.8	4.0	5.2	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage(when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Resistance Level	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$;VN=0V	1	2	10	k Ω
C_{out} Low Resistance Level	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$;VN=0V	150	602	2,380	k Ω
D_{out} High Resistance Level	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$;VN=0V	2.5	5.0	10.0	k Ω
D_{out} Low Resistance Level	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$;VN=1.8V	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; VN=0V	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; VN=3.5V	10	20	40	k Ω



AAT8661H DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.275	4.300	4.325	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.20$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.220	2.300	2.380	V
Over Discharge Release Voltage	V_{D2}	Detect rising Edge of Supply Voltage	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	130	150	170	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	500	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Resistance Level	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$;VN=0V	1	2	10	k Ω
C_{out} Low Resistance Level	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$;VN=0V	150	602	2,380	k Ω
D_{out} High Resistance Level	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$;VN=0V	2.5	5.0	10.0	k Ω
D_{out} Low Resistance Level	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$;VN=1.8V	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; VN=0V	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; VN=3.5V	10	20	40	k Ω



AAT8661I DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.275	4.300	4.325	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.20$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.220	2.300	2.380	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	110	130	150	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	490	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Resistance Level	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $V_N=0V$	1	2	10	k Ω
C_{out} Low Resistance Level	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $V_N=0V$	150	602	2,380	k Ω
D_{out} High Resistance Level	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $V_N=0V$	2.5	5.0	10.0	k Ω
D_{out} Low Resistance Level	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $V_N=1.8V$	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $V_N=0V$	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $V_N=3.5V$	10	20	40	k Ω



AAT8661J DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.255	4.280	4.305	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.20$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.201	2.281	2.361	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 3.6V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	180	200	220	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	510	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$ Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.4 1.1	2.0 2.0	2.6 3.4	ms ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$;VN=0V	1	2	10	k Ω
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$;VN=0V	150	602	2,380	k Ω
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$;VN=0V	2.5	5.0	10.0	k Ω
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$;VN=1.8V	2.5	5.0	10.0	k Ω
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; VN=0V	100	300	900	k Ω
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; VN=3.5V	10	20	40	k Ω



AAT8661K DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	Detect Rising Edge of Supply Voltage	4.225	4.250	4.275	V
Over Charge Release Voltage	V_{C2}	Detect Falling Edge of Supply Voltage	$V_{C1}-0.25$	$V_{C1}-0.20$	$V_{C1}-0.15$	V
Over Discharge Threshold Voltage	V_{D1}	Detect Falling Edge of Supply Voltage	2.201	2.281	2.361	V
Over Discharge Release Voltage	V_{D2}	Detect Rising Edge of Supply Voltage	$V_{D1}+0.5$	$V_{D1}+0.6$	$V_{D1}+0.7$	V
Over Charge Delay Time	t_{C1}	$V_{DD} = 3.6V$ to $4.5V$	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	$V_{DD} = 2.8V$ to $2.2V$	87.5	125.0	162.5	ms
Over Current Level 1 Detection Voltage	V_{OC1}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC1} Delay Time)	80	100	120	mV
Over Current Level 2 Detection Voltage	V_{OC2}	Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{OC2} Delay Time)	400	480	600	mV
Short Circuit Detection Voltage	V_{short}	$V_{DD} = 3.0V$, Detect Rising Edge of “VN” Pin Voltage (D_{out} Response with t_{short} Delay Time)	$V_{DD}-1.7$	$V_{DD}-1.3$	$V_{DD}-0.9$	V
Over Current Level 1 Detection Delay Time	t_{OC1}	$V_{DD} = 3.0V$	5.6	8.0	10.4	ms
Over Current Level 2 Detection Delay Time	t_{OC2}	Room Temp. $\Rightarrow$	1.4	2.0	2.6	ms
		Low or High Temp. $\Rightarrow$ $V_{DD} = 3.0V$	1.1	2.0	3.4	ms
Short Circuit Detection Delay Time	t_{short}	$V_{DD} = 3.0V$		10	50	μs
Charger Detection Voltage	V_{CHR}	Detect Rising Edge of “ D_{out} ” Pin Voltage (when $V_{D1} < V_{DD} < V_{D2}$)	-2.0	-1.3	-0.6	V
C_{out} High Level Resistance	R_{COH}	$V_{DD} = 3.5V$; $C_{out} = 3.0V$; $VN = 0V$	1	2	10	$k\Omega$
C_{out} Low Level Resistance	R_{COL}	$V_{DD} = 4.5V$; $C_{out} = 0.5V$; $VN = 0V$	150	602	2,380	$k\Omega$
D_{out} High Level Resistance	R_{DOH}	$V_{DD} = 3.5V$; $D_{out} = 3.0V$; $VN = 0V$	2.5	5.0	10.0	$k\Omega$
D_{out} Low Level Resistance	R_{DOL}	$V_{DD} = 1.8V$; $D_{out} = 0.5V$; $VN = 1.8V$	2.5	5.0	10.0	$k\Omega$
Internal Resistance between VN and V_{DD}	R_{VND}	$V_{DD} = 1.8V$; $VN = 0V$	100	300	900	$k\Omega$
Internal Resistance between VN and GND	R_{VNG}	$V_{DD} = 3.5V$; $VN = 3.5V$	10	20	40	$k\Omega$



SUMMARY OF AAT8661 DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	DEVICE	MIN	TYP	MAX	UNIT
Over Charge Threshold Voltage	V_{C1}	AAT8661A	4.300	4.325	4.350	V
		AAT8661B	4.325	4.350	4.375	V
		AAT8661C	4.275	4.300	4.325	V
		AAT8661D	4.255	4.280	4.305	V
		AAT8661E	4.255	4.280	4.305	V
		AAT8661F	4.300	4.325	4.350	V
		AAT8661G	4.325	4.350	4.375	V
		AAT8661H	4.275	4.30	4.325	V
		AAT8661I	4.275	4.30	4.325	V
		AAT8661J	4.255	4.280	4.305	V
		AAT8661K	4.225	4.250	4.275	V
Over Charge Release Voltage	V_{C2}	AAT8661A	$V_{C1} - 0.30$	$V_{C1} - 0.25$	$V_{C1} - 0.20$	V
		AAT8661B	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661C	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661D	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661E	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661F	$V_{C1} - 0.30$	$V_{C1} - 0.25$	$V_{C1} - 0.20$	V
		AAT8661G	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661H	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661I	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661J	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
		AAT8661K	$V_{C1} - 0.25$	$V_{C1} - 0.20$	$V_{C1} - 0.15$	V
Over Discharge Threshold Voltage	V_{D1}	AAT8661A	2.420	2.500	2.580	V
		AAT8661B	2.220	2.300	2.380	V
		AAT8661C	2.220	2.300	2.380	V
		AAT8661D	2.201	2.281	2.361	V
		AAT8661E	2.201	2.281	2.361	V
		AAT8661F	2.420	2.500	2.580	V
		AAT8661G	2.220	2.300	2.380	V
		AAT8661H	2.220	2.300	2.380	V
		AAT8661I	2.220	2.300	2.380	V
		AAT8661J	2.201	2.281	2.361	V
		AAT8661K	2.201	2.281	2.361	V



AAT8661 Series

SUMMARY OF AAT8661 DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	DEVICE	MIN	TYP	MAX	UNIT
Over Discharge Release Voltage	V_{D2}	AAT8661A	$V_{D1}+0.3$	$V_{D1}+0.4$	$V_{D1}+0.5$	V
		AAT8661B	$V_{D1}+0.6$	$V_{D1}+0.7$	$V_{D1}+0.8$	V
		AAT8661C	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
		AAT8661D	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
		AAT8661E	$V_{D1}+0.5$	$V_{D1}+0.6$	$V_{D1}+0.7$	V
		AAT8661F	$V_{D1}+0.3$	$V_{D1}+0.4$	$V_{D1}+0.5$	V
		AAT8661G	$V_{D1}+0.6$	$V_{D1}+0.7$	$V_{D1}+0.8$	V
		AAT8661H	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
		AAT8661I	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
		AAT8661J	$V_{D1}-0.08$	V_{D1}	$V_{D1}+0.08$	V
		AAT8661K	$V_{D1}+0.5$	$V_{D1}+0.6$	$V_{D1}+0.7$	V
Over Charge Delay Time	t_{C1}	AAT8661A	0.700	1.000	1.300	s
		AAT8661B	0.088	0.125	0.163	s
		AAT8661C	0.700	1.000	1.300	s
		AAT8661D	0.700	1.000	1.300	s
		AAT8661E	0.700	1.000	1.300	s
		AAT8661F	0.700	1.000	1.300	s
		AAT8661G	0.088	0.125	0.163	s
		AAT8661H	0.700	1.000	1.300	s
		AAT8661I	0.700	1.000	1.300	s
		AAT8661J	0.700	1.000	1.300	s
		AAT8661K	0.700	1.000	1.300	s
Over Discharge Delay Time	t_{D1}	AAT8661A	87.5	125.0	162.5	ms
		AAT8661B	22.4	32.0	41.6	ms
		AAT8661C	87.5	125.0	162.5	ms
		AAT8661D	87.5	125.0	162.5	ms
		AAT8661E	87.5	125.0	162.5	ms
		AAT8661F	87.5	125.0	162.5	ms
		AAT8661G	22.4	32.0	41.6	ms
		AAT8661H	87.5	125.0	162.5	ms
		AAT8661I	87.5	125.0	162.5	ms
		AAT8661J	87.5	125.0	162.5	ms
		AAT8661K	87.5	125.0	162.5	ms



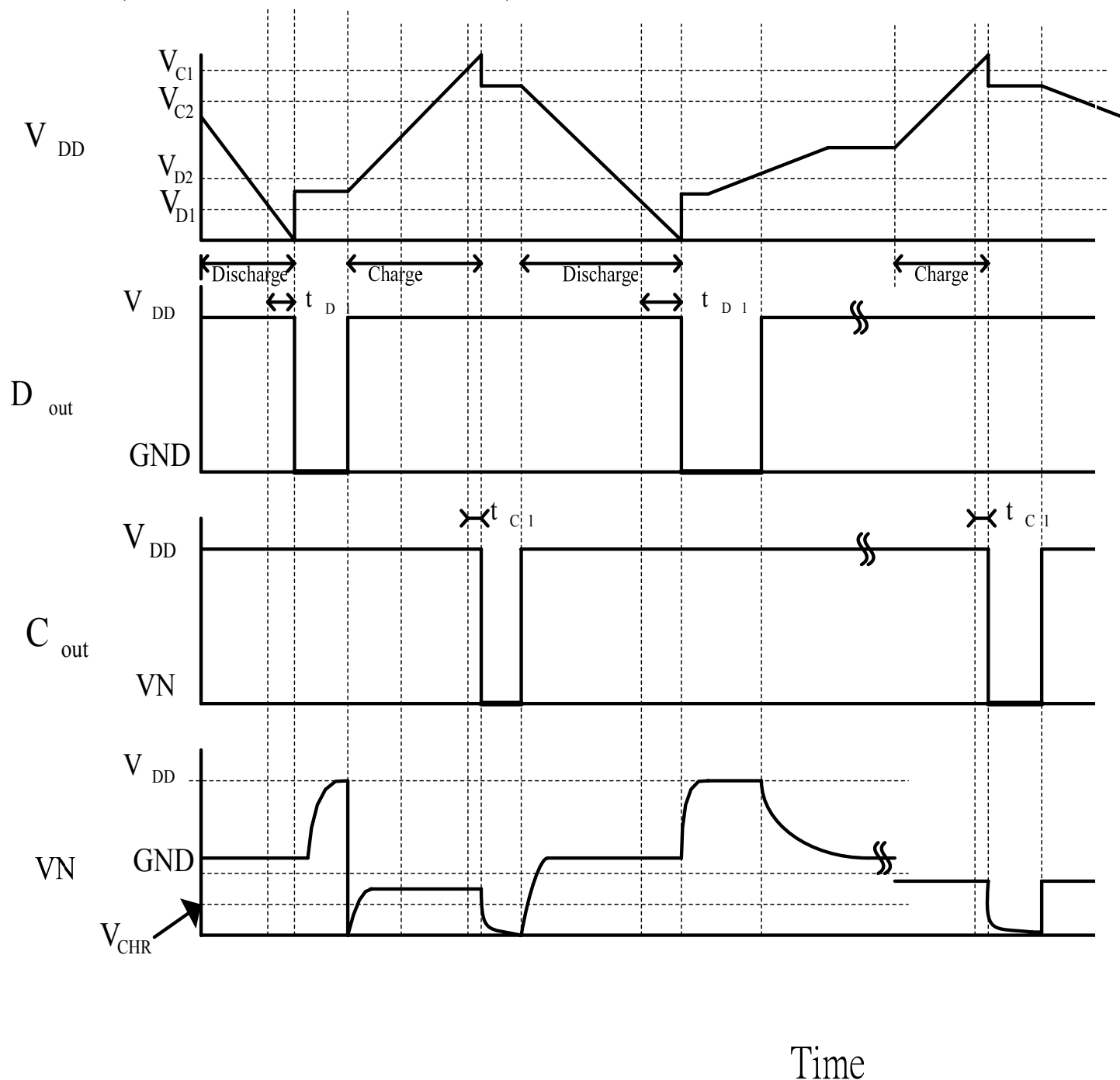
SUMMARY OF AAT8661 DETECTION VOLTAGE AND DELAY TIME (25°C)

PARAMETER	SYMBOL	DEVICE	MIN	TYP	MAX	UNIT
Over Current Level 1 Detection Voltage	V_{OC1}	AAT8661A	130	150	170	mV
		AAT8661B	130	150	170	mV
		AAT8661C	80	100	120	mV
		AAT8661D	110	130	150	mV
		AAT8661E	80	100	120	mV
		AAT8661F	80	100	120	mV
		AAT8661G	180	200	220	mV
		AAT8661H	130	150	170	mV
		AAT8661I	110	130	150	mV
		AAT8661J	180	200	220	mV
		AAT8661K	80	100	120	mV
Over Current Level 2 Detection Voltage	V_{OC2}	AAT8661A	400	500	600	mV
		AAT8661B	400	500	600	mV
		AAT8661C	400	480	600	mV
		AAT8661D	400	490	600	mV
		AAT8661E	400	480	600	mV
		AAT8661F	400	480	600	mV
		AAT8661G	400	510	600	mV
		AAT8661H	400	500	600	mV
		AAT8661I	400	490	600	mV
		AAT8661J	400	510	600	mV
		AAT8661K	400	480	600	mV
Over Current Level 1 Detection Delay Time	t_{OC1}	AAT8661A	5.6	8.0	10.4	ms
		AAT8661B	2.8	4.0	5.2	ms
		AAT8661C	5.6	8.0	10.4	ms
		AAT8661D	5.6	8.0	10.4	ms
		AAT8661E	5.6	8.0	10.4	ms
		AAT8661F	5.6	8.0	10.4	ms
		AAT8661G	2.8	4.0	5.2	ms
		AAT8661H	5.6	8.0	10.4	ms
		AAT8661I	5.6	8.0	10.4	ms
		AAT8661J	5.6	8.0	10.4	ms
		AAT8661K	5.6	8.0	10.4	ms



TIMING CHART

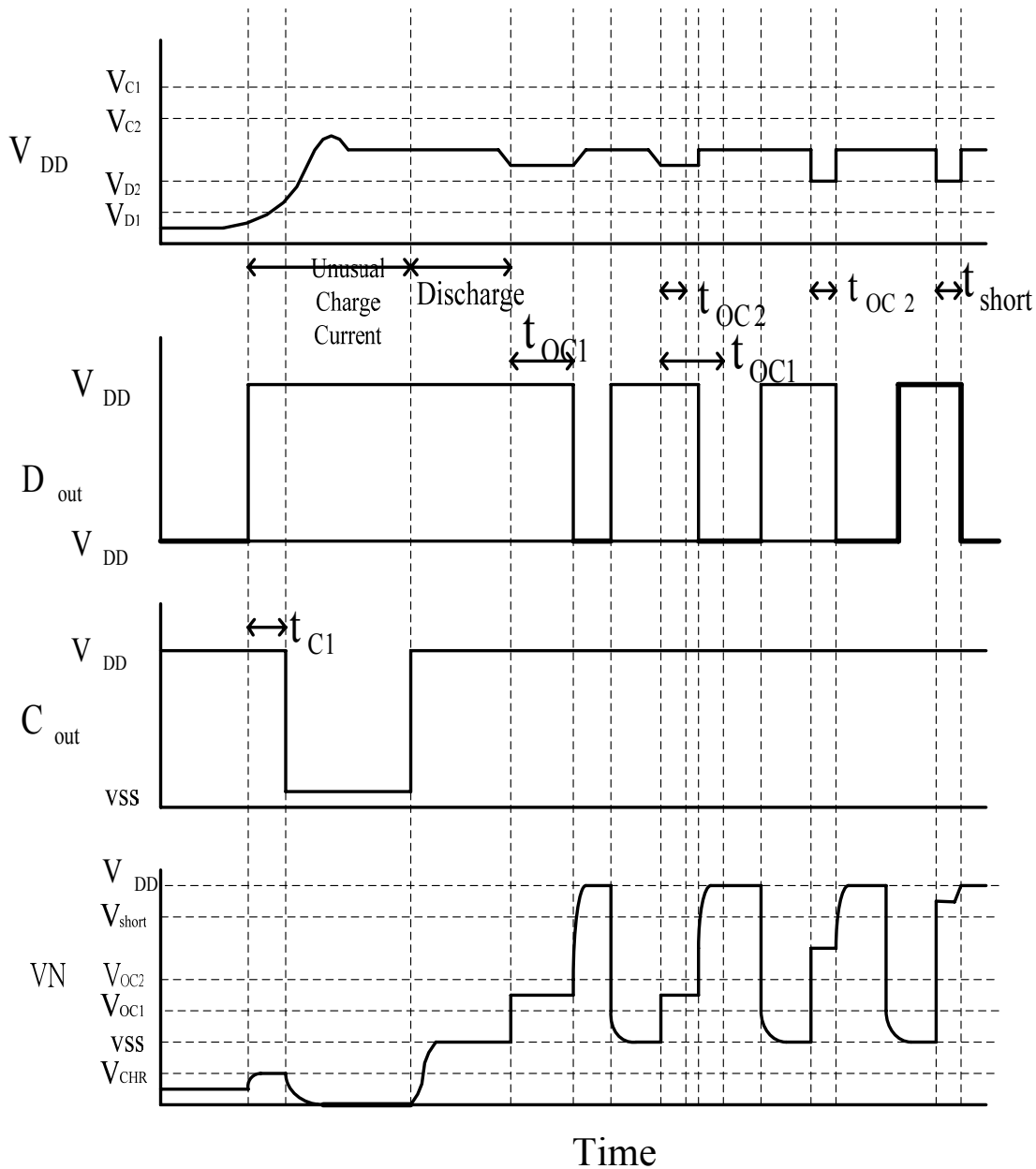
AAT8661 (CHARGE AND DISCHARGE)





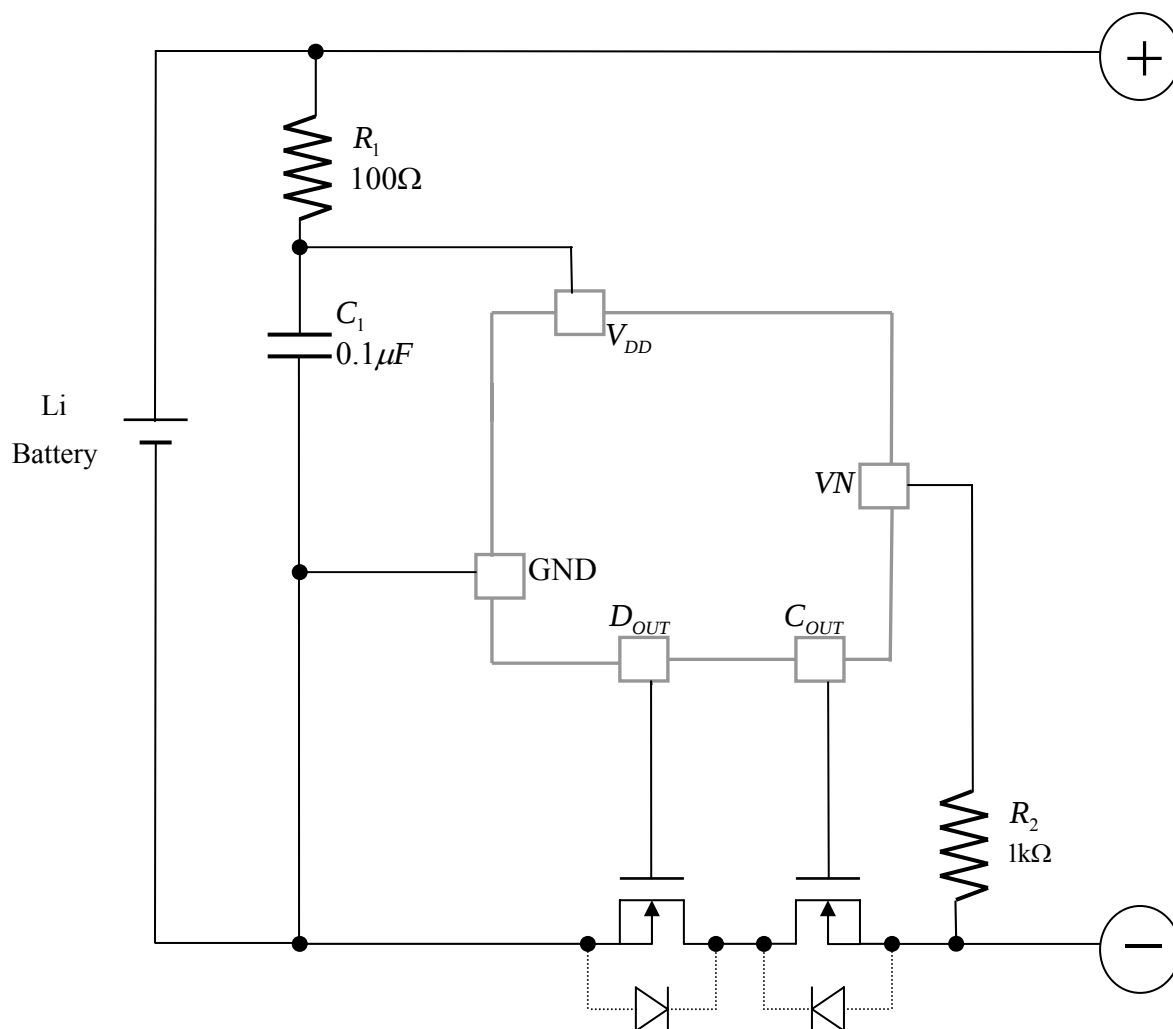
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AAT8661 (UNUSUAL CHARGE CURRENT, OVER CURRENT, SHORT CIRCUIT)



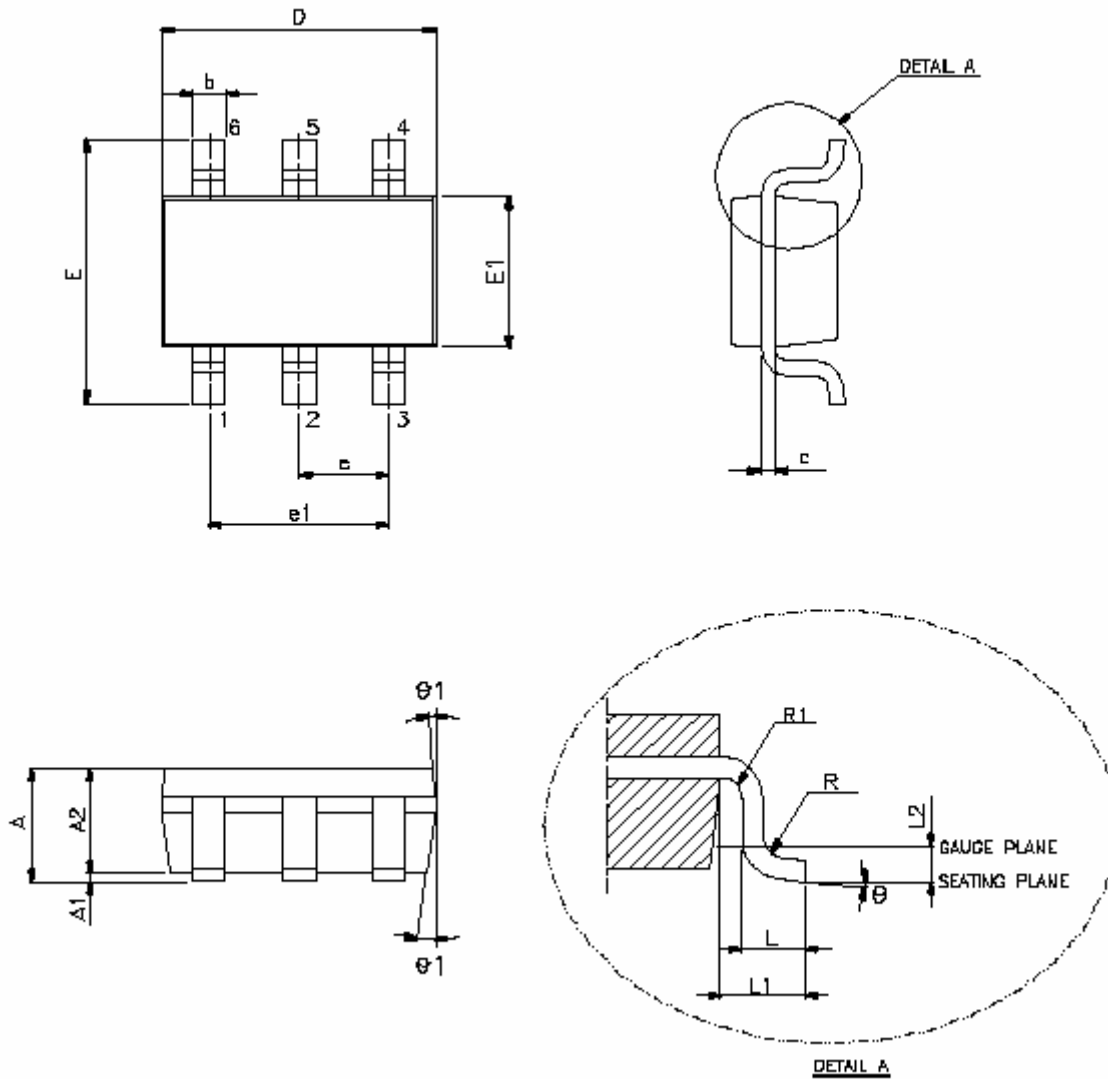


TYPICAL APPLICATION





PACKAGE DIMENSION





PACKAGE DIMENSION (CONT.)

VARIATION (ALL DIMENSIONS SHOWN IN MILLIMETERS)

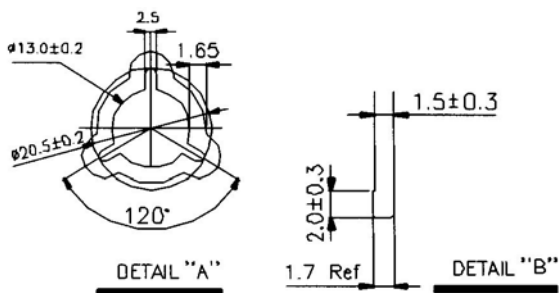
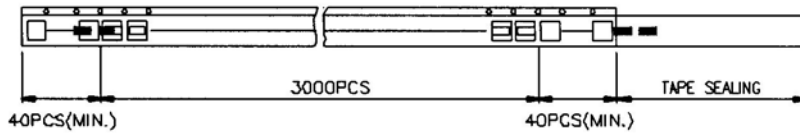
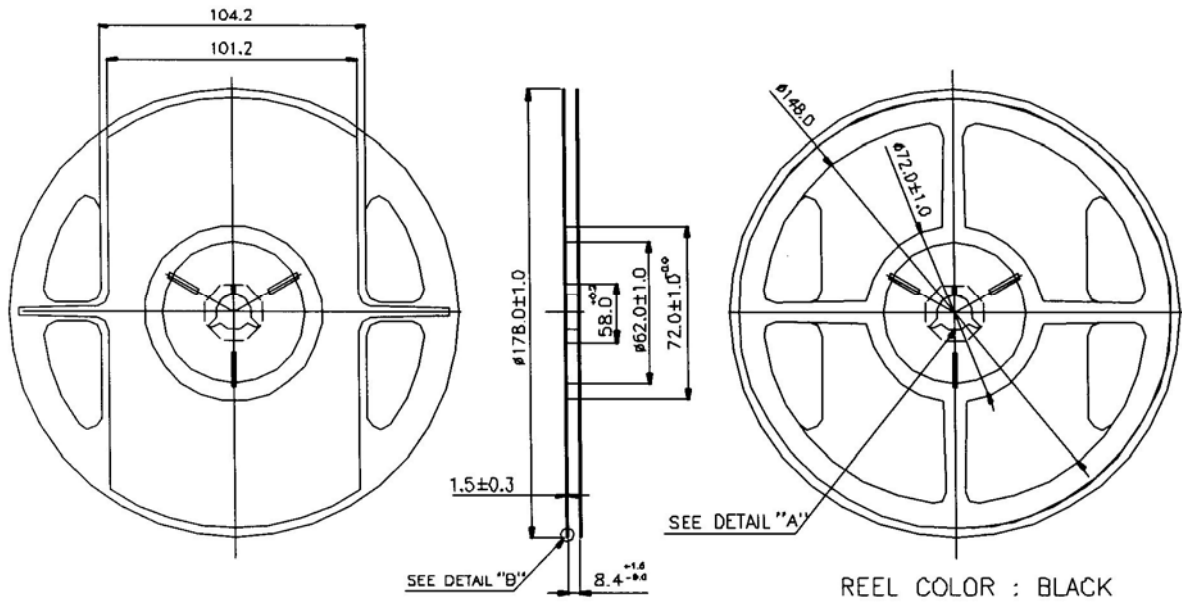
SYMBOL	MIN	TYP	MAX
A	-----	-----	1.45
A1	-----	-----	0.15
A2	0.90	1.15	1.30
b	0.30	-----	0.50
c	0.08	-----	0.22
D	2.90 BSC		
E	2.80 BSC		
E1	1.60 BSC		
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.45	0.60
L1	0.60 REF		
L2	0.25 BSC		
R	0.10	-----	-----
R1	0.10	-----	0.25
θ	0°	4°	8°
θ1	5°	10°	15°

NOTE:

1 JEDEC OUTLINE: MO-178 AB

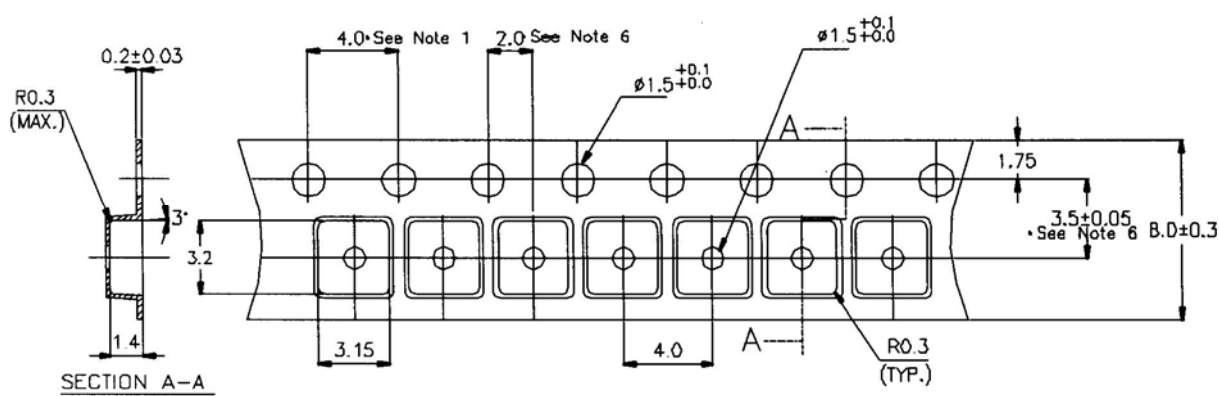
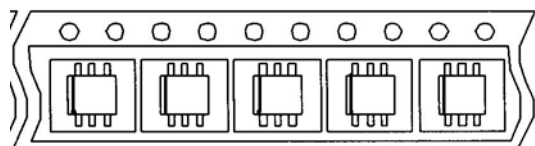


TAPE AND REEL





TAPE AND REEL (CONT.)



X.XXX X ± 0.0025

X.XXX ± 0.006

X.XX ± 0.025

X.X ± 0.10

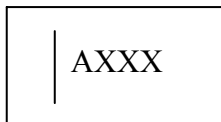
X ± 0.25

UNIT: MILLIMETERS



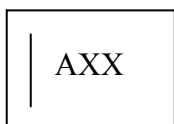
PART MARKING

PREVIOUS SOT26 TOP MARKING



NOTE: SOT26 HAS NO BACK MARKING.

CURRENT SOT26 TOP MARKING



ORDERING INFORMATION

