

HVL133A

Silicon Epitaxial Planar Pin Diode for Antenna Switching

REJ03G0066-0100Z

Rev.1.00

Jan.21.2004

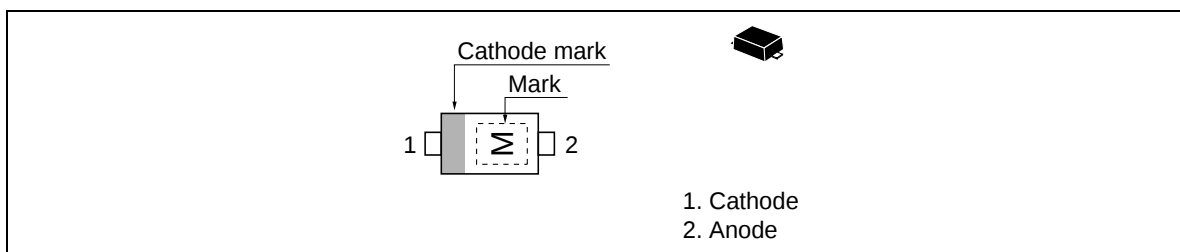
Features

- An optimal solution for antenna switching in mobile phones.
- Low capacitance. ($C_1 = 1.0 \text{ pF max}$)
- Low forward resistance. ($r_f = 0.7 \Omega \text{ max}$)
- Extremely small Flat Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVL133A	M	EFP

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	30	V
Power dissipation	P _d	100	mW
Junction temperature	T _j	125	°C
Storage temperature	T _{stg}	–55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V _R	30	—	—	V	I _R = 1 μA
Reverse current	I _R	—	—	100	nA	V _R = 25 V
Forward voltage	V _F	—	—	0.85	V	I _F = 2 mA
Capacitance	C ₁	—	—	1.00	pF	V _R = 1 V, f = 1 MHz
	C ₆	—	—	0.90		V _R = 6 V, f = 1 MHz
Forward resistance	r _f	—	0.55	0.70	Ω	I _F = 2 mA, f = 100 MHz

Notes: 1. Please do not use the soldering iron due to avoid high stress to the EFP package.
2. The material of lead is exposed for cutting plane. Therefore, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic

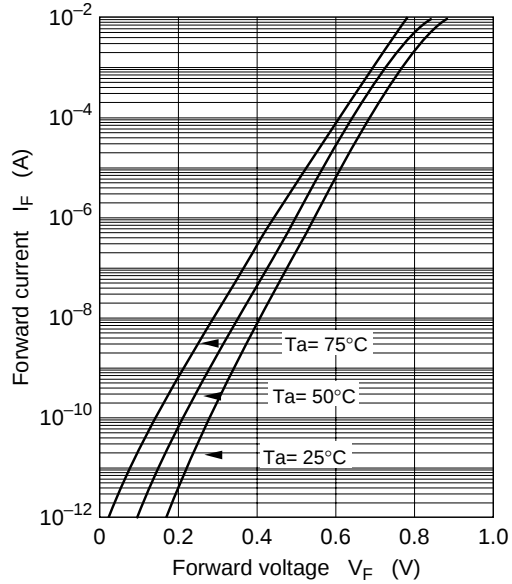


Fig.1 Forward current vs. Forward voltage

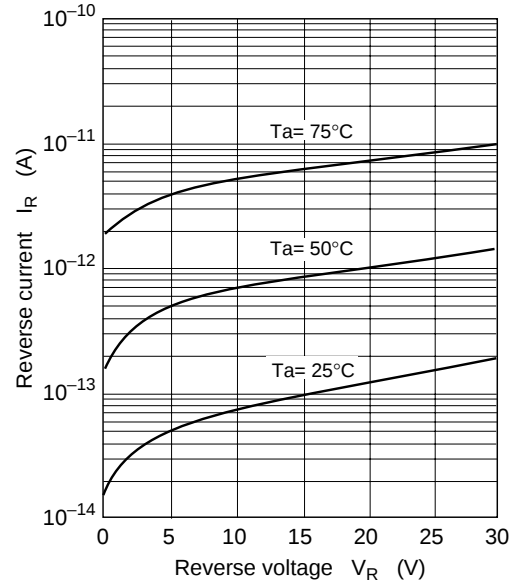


Fig.2 Reverse current vs. Reverse voltage

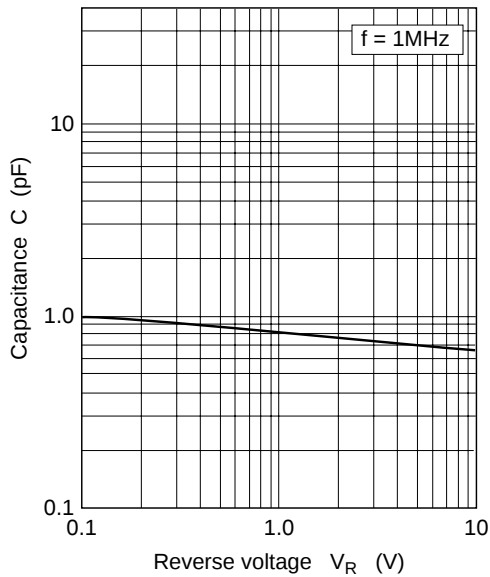


Fig.3 Capacitance vs. Reverse voltage

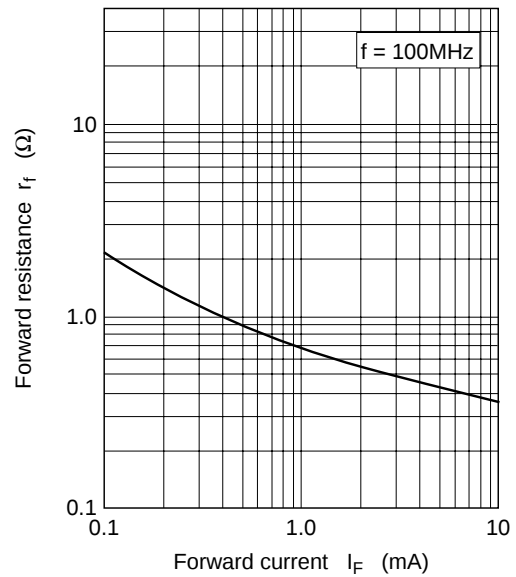
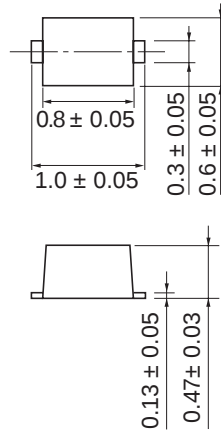


Fig.4 Forward resistance vs. Forward current

Package Dimensions

As of January, 2003

Unit: mm



Package Code	EFP
JEDEC	—
JEITA	—
Mass (reference value)	0.0007 g

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