EB72F72 Series

- Oven Controlled Crystal Oscillator (OCXO)
- SC-Cut Crystal
- HCMOS output
- 3.3V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±30ppb





ELECTRICAL SPECIFICATIONS

Frequency Range	10.000MHz, 12.288MH	lz, 12.800MHz, 16.000N					
Operating Temperature Range (OTR)							
Storage Temperature Range			-55°C to 125°C				
Supply Voltage (V _{DD})			3.3V _{DC} ±5%	3.3V _{DC} ±5%			
Frequency Tolerance / Stability							
vs. Initial Tolerance	at Nominal V_{DD} and V_{C} ,	at 25°C	±500ppb o	±500ppb or ±300ppb Maximum			
vs. Temperature Stability	at Nominal V_{DD} and V_{C}		±30ppb, ±	±30ppb, ±50ppb, ±80ppb, ±100ppb, ±200pp			
			or ±280pp	o Maximum			
vs. Vdd	$V_{DD} \pm 5\%$		±20ppb Ma	±20ppb Maximum			
vs. Load	Vload ±5%		±20ppb Ma	±20ppb Maximum			
vs. Aging (1 Day)	after 72 Hours of Oper	ation	2.0ppb Ma	2.0ppb Maximum			
vs. Aging (1 Year)	after 72 Hours of Oper	ation	±100ppb M	±100ppb Maximum			
vs. Aging (10 Years)	after 72 Hours of Oper	ation	±500ppb /	±500ppb Maximum			
Crystal Cut	·		SC-Cut				
Warm Up Time	to ±50ppb of Final Freq	uency at 1 Hour at 25°C	3 Minute M	3 Minute Maximum			
Power Consumption	at Steady State, at 25°		1.2 Watts N	1.2 Watts Maximum			
·	During Warm Up, at 25	o°C	3.6 Watts N	Maximum			
Output Voltage Logic High (V _{OH})	$I_{OH} = -4mA$		2.6V _{pc} Mini	2.6V _{DC} Minimum			
Output Voltage Logic Low (V _{OL})	$I_{01} = +4mA$			0.4V _{pc} Maximum			
Rise Time / Fall Time	Measured at 20% to 80	0% of Waveform		6nSec Maximum			
Duty Cycle	Measured at 50% of Wa	aveform	50 ±5(%)				
Load Drive Capability				15pF HCMOS Load Maximum			
Frequency Deviation	Referenced to F_0 at $V_c =$	$1.65V_{DC}$; $V_{DD} = 5.0V_{DC}$ over 0		±0.5ppm Minimum			
Control Voltage Range		DC. DD DC		0.0V _{DC} to V _{DD}			
Control Voltage (V _c)			1.65V _{DC} ±1.	, , , , , , , , , , , , , , , , , , ,			
Transfer Function				Positive Transfer Characteristic			
Reference Voltage Output				2.8V _{DC} ±0.2V _{DC} (Pin 4)			
Linearity			±10% Maxi				
Input Impedance 10k0hms Typical							
Typical Phase Noise (at 12.800MHz)		-90dBc/Hz					
.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,	-100dBc/Hz					
10Hz Offset 100Hz Offset			-130dBc/Hz				
	,	-145dBc/Hz					
	,	-150dBc/Hz					
	10kHz Offset		-13000C/T	IL			
MANUFACTURER CATEGORY ECLIPTEK CORP. OSCILLATOR	SERIES EB72F72	PACKAGE 5 pin DIP	VOLTAGE 3.3V	class OS2M	REV - DATE 05/07		

PART NUMBERING GUIDE

EB72F72 D 10 B V 2 - 20.000M

INITIAL TOLERANCE FREQUENCY D=±500ppb E=±300ppb **DUTY CYCLE** 2=50% ±5% FREQUENCY STABILITY **VOLTAGE CONTROL OPTION** 2 Digit Code Per Table 1 V=Voltage Control on Pin 3 and Reference Voltage Output on Pin 4

OPERATING TEMPERATURE RANGE

1 Letter Code Per Table 1

	TABLE 1: PART NUMBERING CODES										
Range		FREQUENCY STABILITY X Denotes availability									
ature			±30ppb	±50ppb	±80ppb	±100ppb	±200ppb	±280ppb			
Operating Temperature		Code	03	05	08	10	20	28			
	0°C to +50°C	А	Х	Х	Х	Х	Х	Х			
	0°C to +70°C	В		Х	Х	Х	Х	Х			
Ope	-20°C to +70°C	С			Х	×	×	Х			

