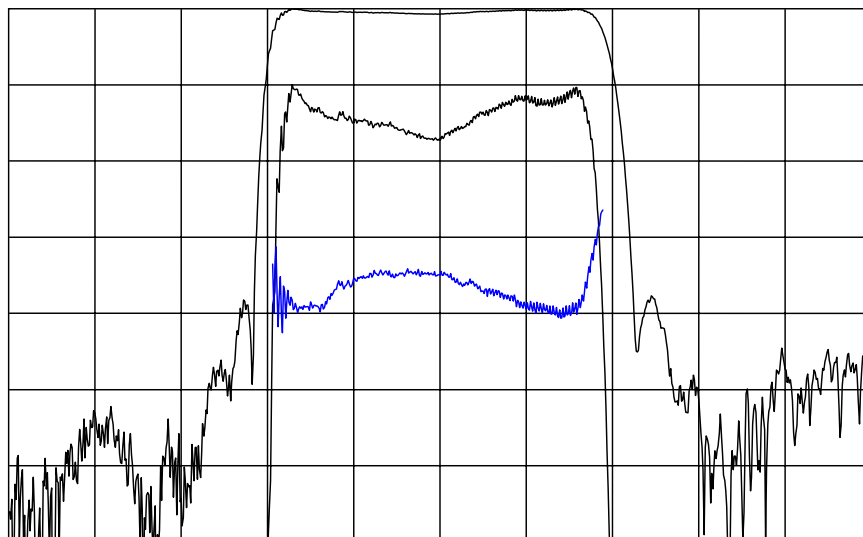




TYPICAL PERFORMANCE



Horizontal: 20 MHz/div

Vertical (from top):

Magnitude

10 dB/div

Magnitude

1 dB/div

Phase Linearity

5 deg/div

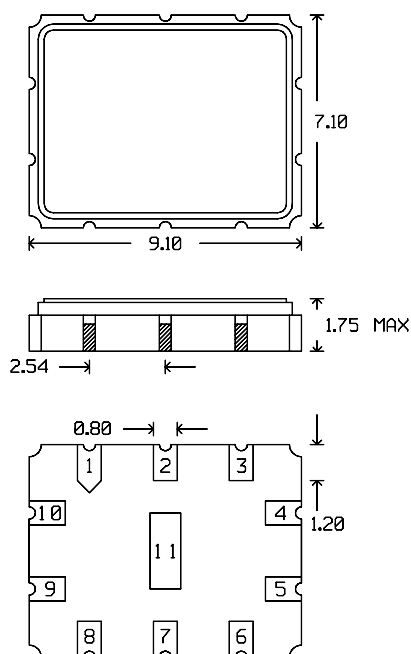
SPECIFICATION

Parameter ¹	Min.	Typ.	Max.	Units.
Center Frequency (Fc) ²		140		MHz
Insertion Loss at 140 MHz ³			22.5	dB
Lower 1.5 dB Band Edge			109.00	MHz
Upper 1.5 dB Band Edge	171.00			MHz
Lower 3 dB Band Edge			104.00	MHz
Upper 3 dB Band edge	176.00			MHz
Lower 35 dB Band edge	92.00			MHz
Upper 35 dB band Edge			187.00	MHz
Amplitude Variation (109-171 MHz))			1.5	dB p-p
Phase Linearity (109-171 MHz)			10	deg p-p
Group Delay Variation (109-171 MHz)			100	ns p-p
Rejection (15 to 75 MHz)	45			dB
(75 to 89 MHz)	40			dB
(193 to 215 MHz)	40			dB
(215 to 298 MHz)	40			dB
Source and Load Impedance	50			Ω
Substrate Material	YZ-LiNbO ₃			
Temperature Coefficient of frequency		-90		ppm/° C

- Notes:
1. All specifications for a device match to a 50 ohm system impedance.
 2. Average of the two 3 dB frequency points.
 3. All dB values are referenced to the insertion loss.



PACKAGE OUTLINE

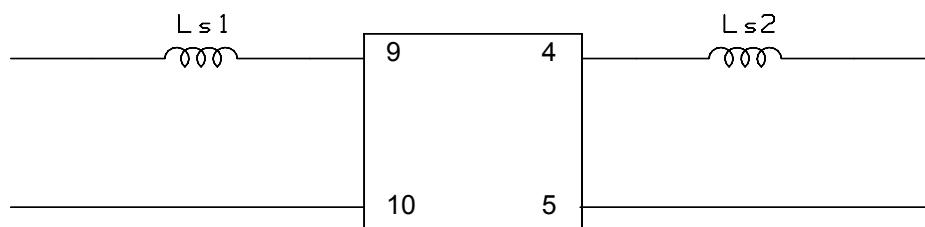


Units: mm

Pad Configuration:

Input: 9
Input return: 10
Output: 4
Output Return: 5
Case Ground: All other pins

MATCHING CIRCUIT



Typical component values: $L_{s1} = 94.3 \text{ nH}$ $L_{s2} = 82 \text{ nH}$

(Minimum inductor Q = 40)

Notes

- Recommend use of 2% tolerance components.
- Optimum values depend on board layout. Values intended as guide only.

ISO 9001
Registered

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