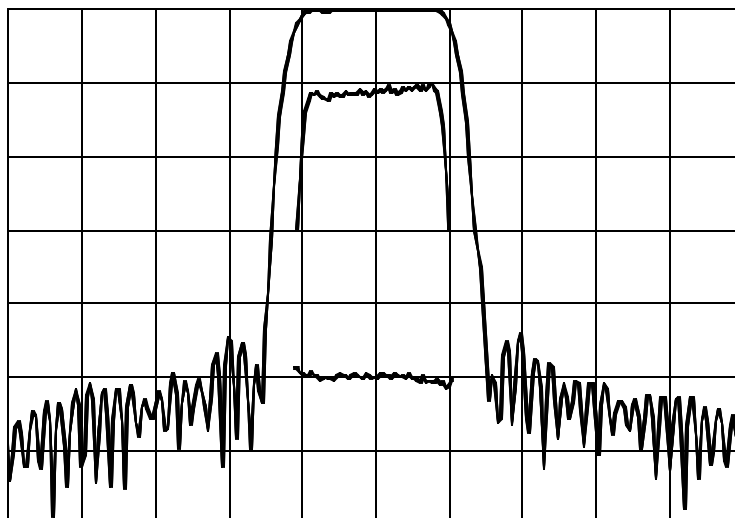


TYPICAL PERFORMANCE



Horizontal: 8 MHz/div

Vertical (from top): Magnitude 10 dB/div
Magnitude 1 dB/div
Group Delay 50 nsec/div

SPECIFICATION

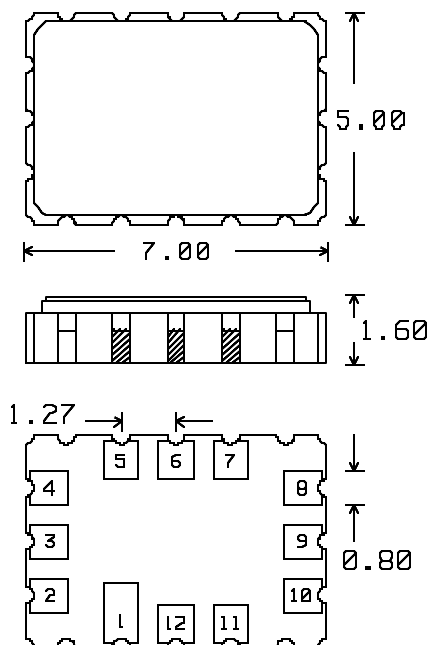
Parameter	Min	Typ	Max	Units
Center Frequency (F_c) ¹		326.4		MHz
Insertion Loss		15.5	18.5	dB
2 dB Bandwidth	15.0	16.7		MHz
40 dB Bandwidth		23.6	26	MHz
Rejection (10 MHz to 50 MHz)	30			dB
Rejection (50 MHz to F_c -50 MHz)	47	55		dB
Rejection (F_c -50 MHz to F_c -13 MHz)	40	42		dB
Rejection (F_c +13 MHz to F_c +50 MHz)	40	44		dB
Rejection (F_c +50 MHz to 450 MHz)	47	59		dB
Passband Amplitude Ripple ²		0.3	0.7	dB p-p
Passband Group Delay Ripple ³		30	50	ns p-p
Source and Load Impedance		50		Ω
Operating Temperature Range	-10	23	+80	$^{\circ}$ C

Notes: 1. Fixed reference. All specified bandwidths are centered at this frequency.

2. Over $F_c \pm 2.5$ MHz.

3. Over $F_c \pm 7.5$ MHz.

PACKAGE OUTLINE

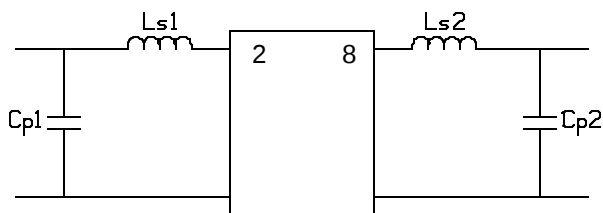


Units: mm

Pin Configuration:

Input: 2
Output: 8
Ground: 1,3,4,5,6,7,9,10,11,12

MATCHING CIRCUIT



Typical component values: Ls1 = 22 nH Ls2 = 18 nH
(minimum inductor Q = 45) Cp1 = 22 pF Cp2 = 22 pF

- Notes
- Requires 2% matching components.
 - Component values may change depending on board layout.
 - All pins other than 2 and 8 are connected together internally to the package

ISO 9001
Registered