

## Marketing Bulletin

**DATE:** Thursday, November 01, 2001  
**TO:** Affected Customers  
**FROM:** Marketing  
**RE:** EV32C1 Series Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the EV32C1 series Ecliptek oscillator effective Thursday, November 01, 2001.

In compliance with our End of Life (EOL) policy, this notice will serve as advanced notice of product termination. New orders will not be accepted after Friday, February 01, 2002, with delivery to be conclude by Tuesday, April 30, 2002.

The EV32C3 OR EV32C6 series is a recommended alternate for the EV32C1 series. This may not be an exact cross, so it is highly recommended that the data sheet(s) of the recommended alternate are reviewed and samples tested to ensure conformance.

If there are any questions pertaining to this bulletin, please contact your Ecliptek sales representative. Thank you again for your cooperation.

Ecliptek Marketing

## STANDARD SPECIFICATIONS

Frequency Range (Fo)	<b>Code</b>	16.384MHz to 44.736MHz (See TEN08-563-800 for a list of standard frequencies)
Frequency Tolerance & Stability / Operating Temperature Range (OTR) (All Values Inclusive of OTR, Vdd, and CLOAD, with Vc = 1.65VDC)	A B D E F H J	Not Specified over 0°C to +70°C Not Specified over -40°C to +85°C ±50ppm Max. over 0°C to +70°C ±25ppm Max. over 0°C to +70°C ±20ppm Max. over 0°C to +70°C ±50ppm Max. over -40°C to +85°C ±25ppm Max. over -40°C to +85°C
Storage Temperature Range (STR)		-55°C to +125°C
Supply Voltage (VDD) / Input Current (IDD)		3.3VDC ±10% / 15mA Maximum
Output Voltage Logic High (VOH)		90% of VDD Minimum
Output Voltage Logic Low (VOL)		10% of VDD Maximum
Rise/Fall Time (TR/TF)		5nSec Maximum (20% to 80% of waveform)
Duty Cycle (SYM)	1 2	50% ±10% (@ 50% of waveform) 50% ±5% (@ 50% of waveform)
Load Drive Capability (CLOAD)		15pF HCMOS Load Maximum
Start Up Time (TS) / Aging (@ 25°C)		10 mSec Maximum / ±2ppm/1st year Typical, ±10ppm/10 years Maximum
Typical Phase Noise (at offsets 10Hz to 1MHz)		-70dBc/Hz, -100dBc/Hz, -130dBc/Hz, -147dBc/Hz, -152dBc/Hz, and -155dBc/Hz
Period Jitter: RMS (TJRMS)		Not Specified
Absolute Pull Range (APR) [All Values Inclusive of OTR, Vdd, CLOAD, and Aging over Control Voltage Range (Vc)]	1 2 3 4 5	±20ppm Minimum ±32ppm Minimum ±50ppm Minimum ±80ppm Minimum ±100ppm Minimum
Linearity	A B C	±20% Maximum ±15% Maximum ±10% Maximum
Control Voltage Range (Vc) / Test Conditions for APR		1.65VDC ±1.35VDC (0.3VDC to 3.0VDC)
Control Voltage Range (VCR) / Transfer Function		0.0VDC to VDD / Positive Transfer Characteristic
Input Impedance (Zi)		50kOhms Typical
Modulation Bandwidth (MBW)		10kHz Minimum (-3dB, Vc = 1.65VDC)

## ENVIRONMENTAL & MECHANICAL

Shock:	Conditions and Criteria Listed in TQC41-883-007
Vibration:	Conditions and Criteria Listed in TQC41-883-008
Seal Integrity:	Conditions and Criteria Listed in TQC41-883-003
Solderability:	Conditions and Criteria Listed in TQC41-883-004 / 95% coverage
Marking Permanency:	Conditions and Criteria Listed in TQC41-883-001

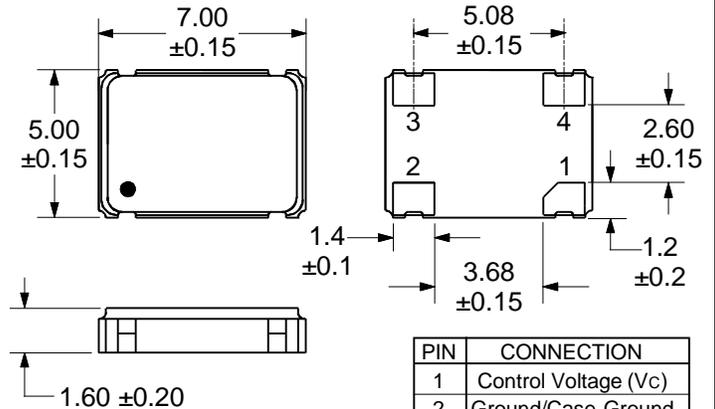
### PART NUMBERING GUIDE

**EV32C1 D 2 A 1 - 35.328M TR**

- Frequency**
- Duty Cycle**  
1 = 50 ±10%  
2 = 50 ±5%
- Linearity**  
A = 20% Maximum, B = 15% Maximum,  
C = 10% Maximum
- Absolute Pull Range (APR)**  
1 = ±20ppm Minimum, 2 = ±32ppm Minimum  
3 = ±50ppm Minimum, 4 = ±80ppm Minimum  
5 = ±100ppm Minimum
- Frequency Tolerance & Stability / OTR**  
A = Not Specified over 0°C to +70°C  
B = Not Specified over -40°C to +85°C  
D = ±50ppm Max. over 0°C to +70°C  
E = ±25ppm Max. over 0°C to +70°C  
F = ±20ppm Max. over 0°C to +70°C  
H = ±50ppm Max. over -40°C to +85°C  
J = ±25ppm Max. over -40°C to +85°C

#### Packaging Options

Blank = Tubes (CPA74-004-000)  
TR = Tape & Reel (CPA70-161-000)



All Dimensions In Millimeters

PIN	CONNECTION
1	Control Voltage (Vc)
2	Ground/Case Ground
3	Output
4	Supply Voltage

### MARKING GUIDE

(Line #1) **ECLIPTEK**

(Line #2) **XX.XXXM**

Frequency

(Line #3) **XX Y ZZ**

Week of Year  
Last Digit of Year

Ecliptek Manufacturing Code (Per TEN02-001-000)



**NOTE:** Pin 1 shall be marked with a dot. Marking shall conform to conditions listed in TQC41-001-000.

### SPECIFICATION CONTROL DRAWING

		Drawing Number <b>CSC12-560-000</b>	
Title <b>CERAMIC SMD 3.3V HCMOS/TTL VCXO</b>			
Revision <b>B</b>		Effectivity Date	
ECN Number <b>5632</b>		PAGE 1 OF 2	
Approved By	Date	Released By	Date