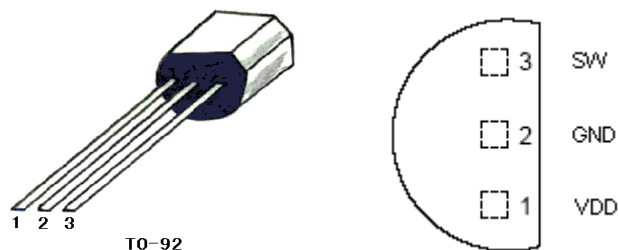


Pin Assignment & Description

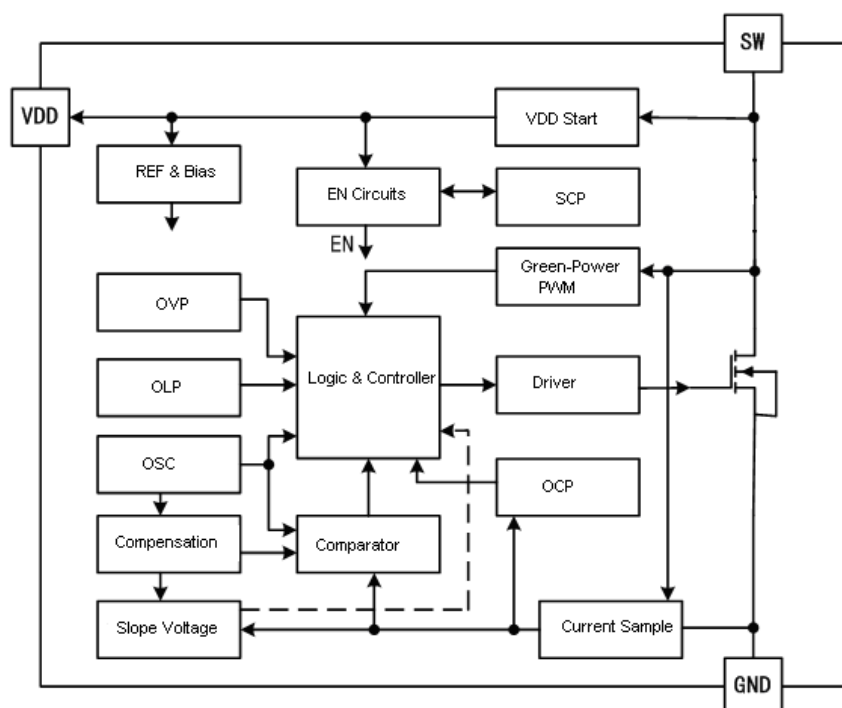


Pin	Name	Description
1	VDD	Power Supply Pin. Connect to optocoupler's emitter. Internally limited to 5.6V max. Bypass to GND with a proper compensation network.
2	GND	Ground
3	SW	Output Pin. Connect to emitter of the high voltage NPN or MOSFET.

RS2030X

Part No.	Freq.	Peak Current	Freq. Jitter	Package
RS2030A	67kHz	400mA	Y	TO-92
RS2030B	100kHz	400mA	Y	TO-92
RS2030C	67kHz	400mA	N	TO-92
RS2030D	100kHz	400mA	N	TO-92
RS2030E	67kHz	800mA	Y	TO-92
RS2030F	100kHz	800mA	Y	TO-92
RS2030G	67kHz	800mA	N	TO-92
RS2030H	100kHz	800mA	N	TO-92

Block Diagram



Absolute Maximum Ratings

PARAMETER	Value	Unit
VDD Pin Voltage	-0.3 ~ 6	V
VDD Current	20	mA
SW Pin Voltage	-0.3~30	V
SW Current	Internally limited	A
Maximum Power Dissipation (TO-92)	0.6	W
Operating Junction Temperature	-40 ~+150	°C
Storage Temperature	-55 ~+150	°C
Lead Temperature (Soldering, 10 sec)	300	°C
ESD Voltage (HBM)	5500	V

Electrical Characteristics (Ta=27°C unless otherwise noted, V_{DD} = 4.5V.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VDD Start Voltage	V _{START}	Rising edge	5.20	5.40	5.60	V
SW Start Voltage	V _{SWST}		8.00	8.60	9.20	V
VDD Under-voltage Threshold	V _{UV}	Falling edge	3.40	3.60	3.80	V
VDD Clamp Voltage	V _{VDD (Max)}	I _{VDD} =10mA	5.40	5.60	5.80	V
Startup Supply Current	I _{DDST}	VDD = 4V before VUV		0.20	0.40	mA
VDD Supply Current	I _{DD}			1.50	2.00	mA
Switching Frequency	f _{SW}	RS2030A	62	67	72	kHz
		RS2030B	90	100	110	
Maximum Duty Cycle	D _{MAX}	VDD = 5.0V	62.7	66.7	71.7	%
Minimum Duty Cycle	D _{MIN}	VDD = 5.0V		3.5		%
Effective Current Limit	I _{LIM}	VDD = V _{UV} + 0.1V	380	400	420	mA
			760	800	840	
VDD to SW Current Coefficient	G _{GAIN}			0.29		A/V
VDD Dynamic Impedance	R _{VDD}			9		kΩ
SW Driver On-Resistance	R _{SW}	I _{SW} =100mA		3.0		Ω
SW Rise Time		1nF load, 15Ω pull-up		30		ns
SW Fall Time		1nF load, 15Ω pull-up		20		ns
SW Switch Off Current		Driver off		5	10	uA

OPERATION DESCRIPTION

Current Model

Compared to voltage model control, current model control has a current feedback loop. When the voltage of the sample resistor peak current of the primary winding reaches the internal setting value V_{TH} , comparator reverse, register reset and power BJT or MOSFET cut-off. So that to detect and modulate the peak current cycle by cycle could control the output of the power supply. The current feedback has a good linear modulation rate and a fast input and output dynamic impact avoid the pole that the output filter inductance brings and the second class system descends to first class and so it widens the frequency range and optimizes overload protection and short circuit protection.

Startup Current and Under Voltage Lockout

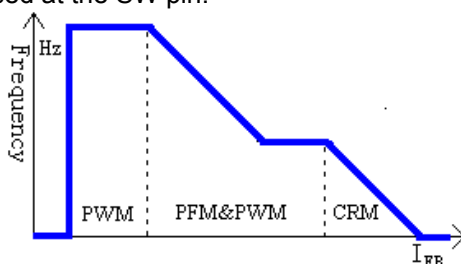
The startup current of RS2030 is set to be very low (3uA) so that a large value startup resistor can therefore be used to minimize the power loss. For AC to DC adaptor with universal input range design, a 1.5 M Ω , 1/8 W startup resistor and a 10uF/25V VDD hold capacitor could be used.

The turn-on and turn-off threshold of the RS2030 is designed to 5.4V/3.6V. During startup, the hold-up capacitor must be charge to 5.4V through the startup resistor. The hysteresis is implemented to prevent the shutdown from the voltage dip during startup.

Green Power Operation

The power dissipation of switching mode power supply is very important in zero load or light load condition. The major dissipation result from conduction loss, switching loss and consume of the control circuit. However, all of them related to the switching frequency. There are many difference topologies has been implemented in different chip. The basic operation theory of all these approaches intended to reduce the switching frequency under light-load or no-load condition.

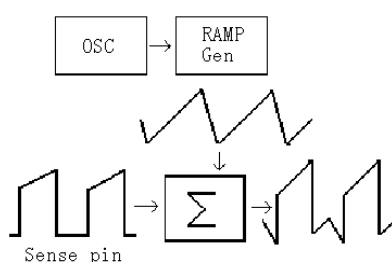
RS2030's green power function adapts PWM, PFM and CRM combining modulation. The PWM frequency is 65kHz/100kHz in medium or heavy load operation. Through modifying the pulse width, RS2030 could control output voltage. To decrease the standby consumption of the power supply, Chip-Rail introduces the Cycle Reset Mode technology; If the feedback current were over 1.5mA, mode controller of RS2030 would reset internal register all the time and cut off the power BJT, while the output voltage is lower than the set value, it would set register, SW pin operating again. Although the frequency of the internal OSC is invariable, the register would reset some pulses so that the practical frequency is decreased at the SW pin.



RS2030 Green-Power Function

Internal Synchronized Slope Compensation

Although there are more advantages of the current mode control than conventional voltage mode control, there are still several drawbacks of peak-sensing current-mode converter. Especially the open loop instability when it operates in higher than 50% of the duty-cycle. RS2030 is introduced an internal slope compensation adding voltage ramp to the current sense input voltage for PWM generation to solve this problem. It improves the close loop stability greatly at CCM, prevents the sub-harmonic oscillation and thus reduces the output ripple voltage.



High/Low Voltage Power Compensation

The current flowing by the power BJT has an extra value $\Delta I = \frac{V_{IN}}{L_P} \times T_D$ due to the system delay T_D that the current detected from the sense pin to power BJT cut off in the RS2030 (Among these, V_{IN} is the primary winding voltage of the transformer and L_P is the primary wind inductance, V_{IN} ranges from 85VAC to 264VAC.) To guarantee the output power is a constant for universal input AC voltage, there is a dynamic peak limit circuit to compensate the system delay T_D that the system delay brings on.

Soft Start

The VDD pin of the RS2030 is the power supply voltage pin and the feedback pin, so the duty cycle will be increase with VDD voltage decreasing. The soft start performance have been implemented by increasing the duty cycle in starting.

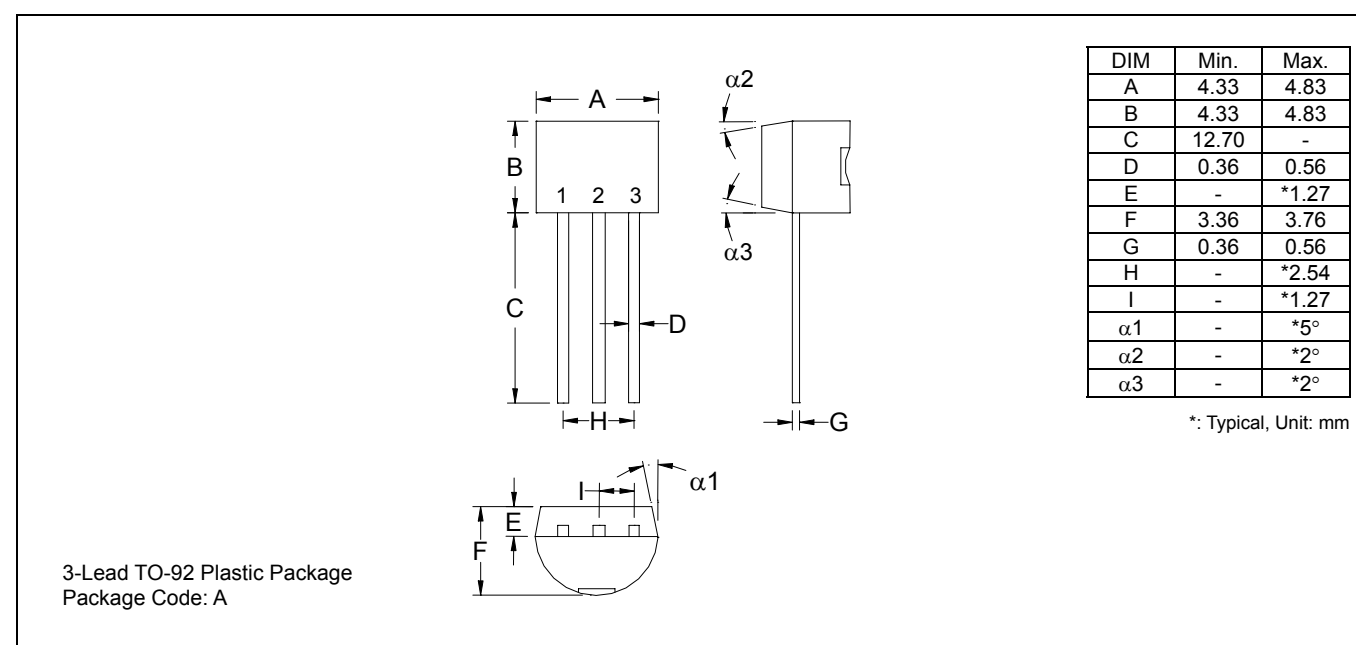
Leading-edge Blanking (LEB)

Each time the power BJT is switched on, a turn-on spike will inevitably occur at the sense pin, which would disturb the internal signal from the sampling of the R_{SENSE} . There is a 250n sec leading edge blanking time built in to avoid the effect of the turn-on spike and the power BJT cannot be switched off during this time. So that the conventional external RC filtering on sense input is no longer required.

Short Circuit Protection (SCP)

When the output circuit is shorted, the restart time of the RS2030 and the input power will be increased with input voltage increasing, then the power NPN BJT will be hot and destroyed. So the Chip-Rail secondary delay circuit has been introduced for decrease the restart time.

TO-92 Dimension

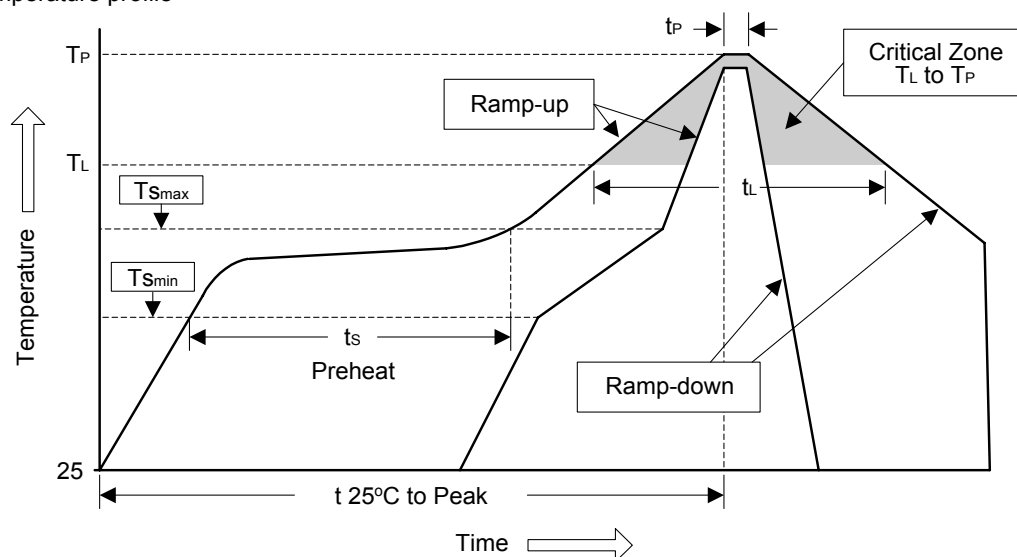


Soldering Methods for Orister's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%

2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60~120 sec	60~180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60~150 sec	60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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