

RKR104BKH

Silicon Schottky Barrier Diode for Rectifying

REJ03G1476-0200

Rev.2.00

Mar 25, 2008

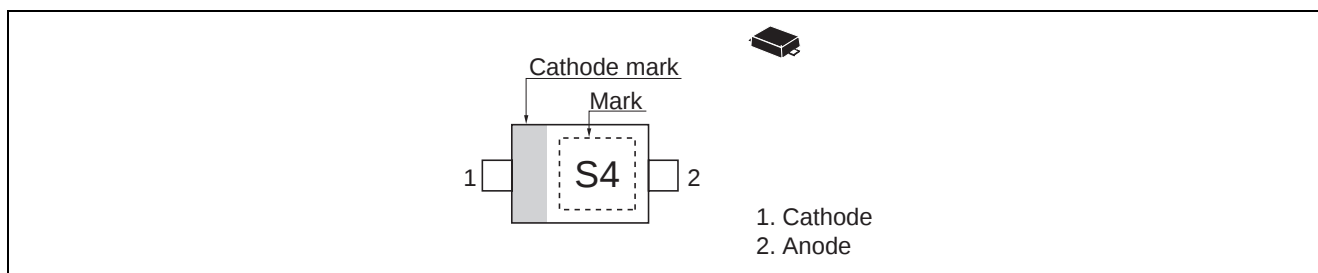
Features

- Low reverse current and suitable for high efficiency rectifying.
- Thin Ultra small Resin Package (TURP) is suitable for high density surface mounting and high speed assembly.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code
RKR104BKH	S4	TURP	PUSF0002ZC-A

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	40	V
Reverse voltage	V_R	40	V
Average rectified current	I_O^{*2}	1	A
Non-Repetitive peak forward surge current	I_{FSM}^{*1}	5	A
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. 10 ms sine wave 1 pulse

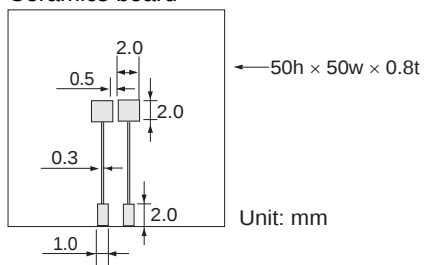
2. Ta = 36°C, With Ceramics board (board size: 50 mm × 50 mm, Land size 2 mm × 2 mm)
Short form wave (θ180°C), $V_R = 20$ V.

Electrical Characteristics

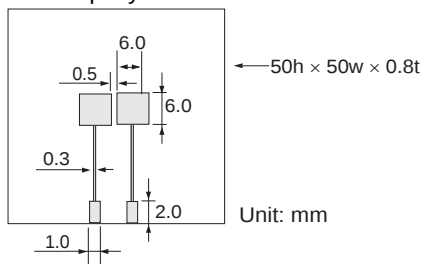
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.37	V	$I_F = 100$ mA
	V_{F2}	—	—	0.55		$I_F = 700$ mA
Reverse current	I_{R1}	—	—	10	μA	$V_R = 5$ V
	I_{R2}	—	—	50		$V_R = 40$ V
Capacitance	C	—	—	35	pF	$V_R = 10$ V, $f = 1$ MHz
Thermal resistance	$R_{th(j-a)}$	—	100	—	°C/W	Ceramics board ^{*1}
		—	200	—		Glass epoxy board ^{*2}

Notes: 1. Ceramics board



2. Glass epoxy board



3. TURP is the structure which radiates heat to a substrate, please perform mounting to a substrate by reflow.

Main Characteristics

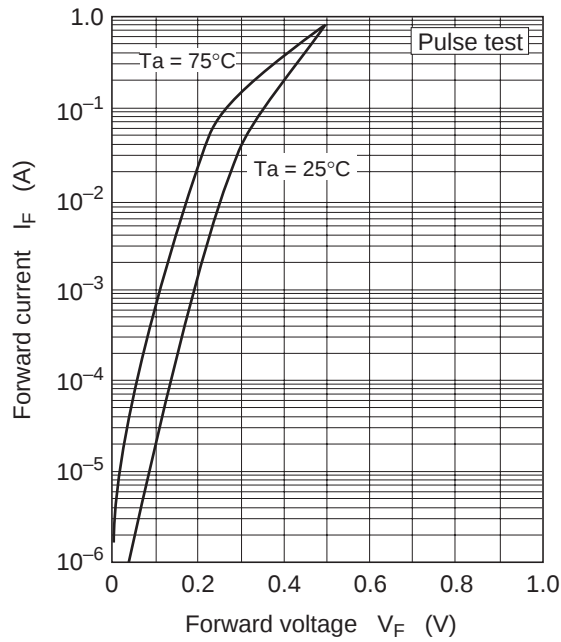


Fig.1 Forward current vs. Forward voltage

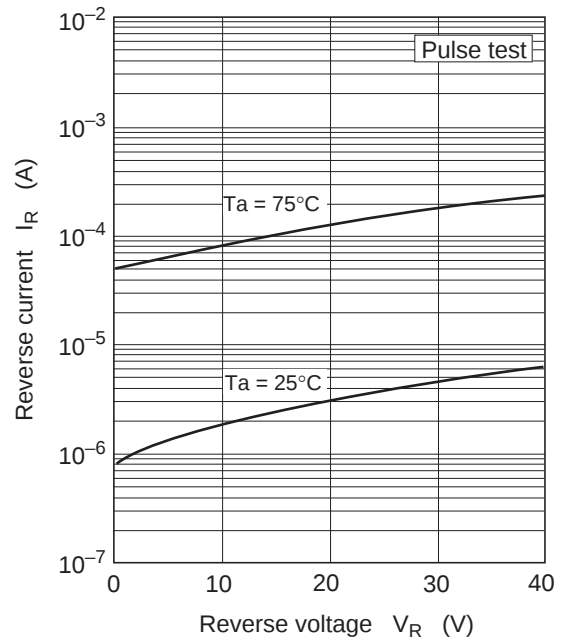


Fig.2 Reverse current vs. Reverse voltage

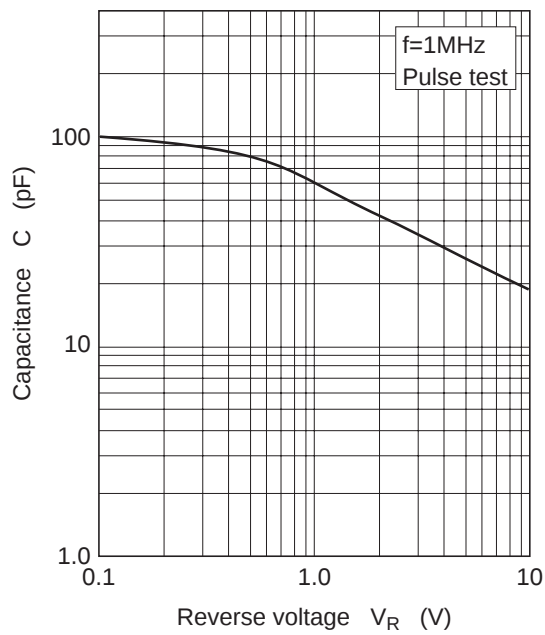


Fig.3 Capacitance vs. Reverse voltage

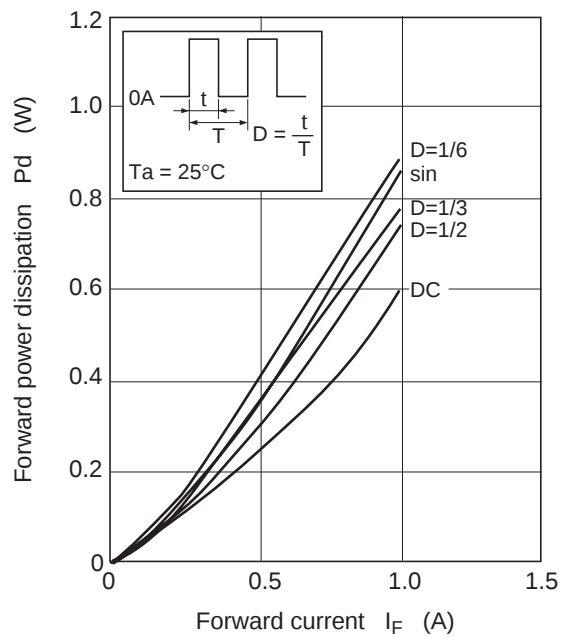


Fig.4 Forward power dissipation vs. Forward current

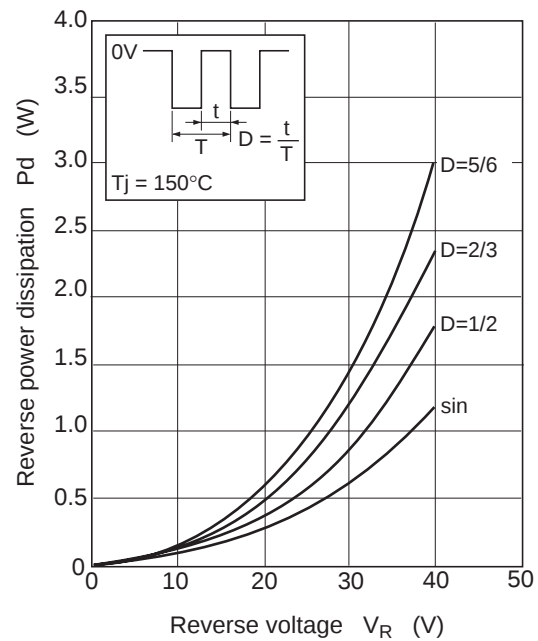


Fig.5 Reverse power dissipation vs. Reverse voltage

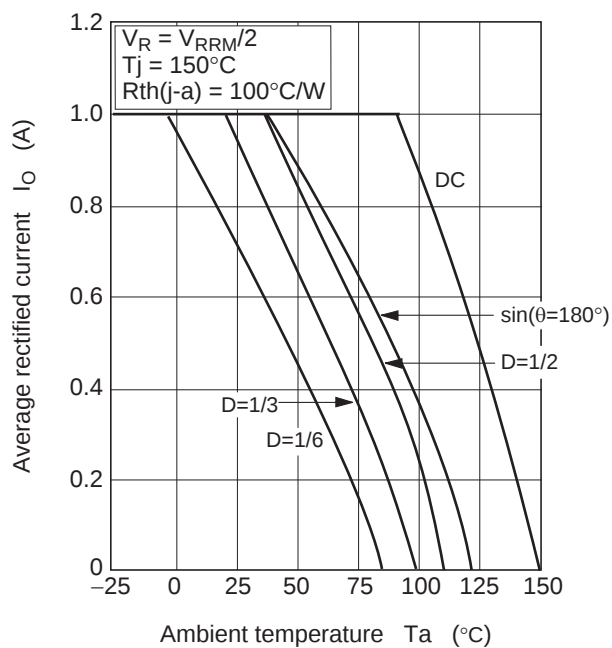
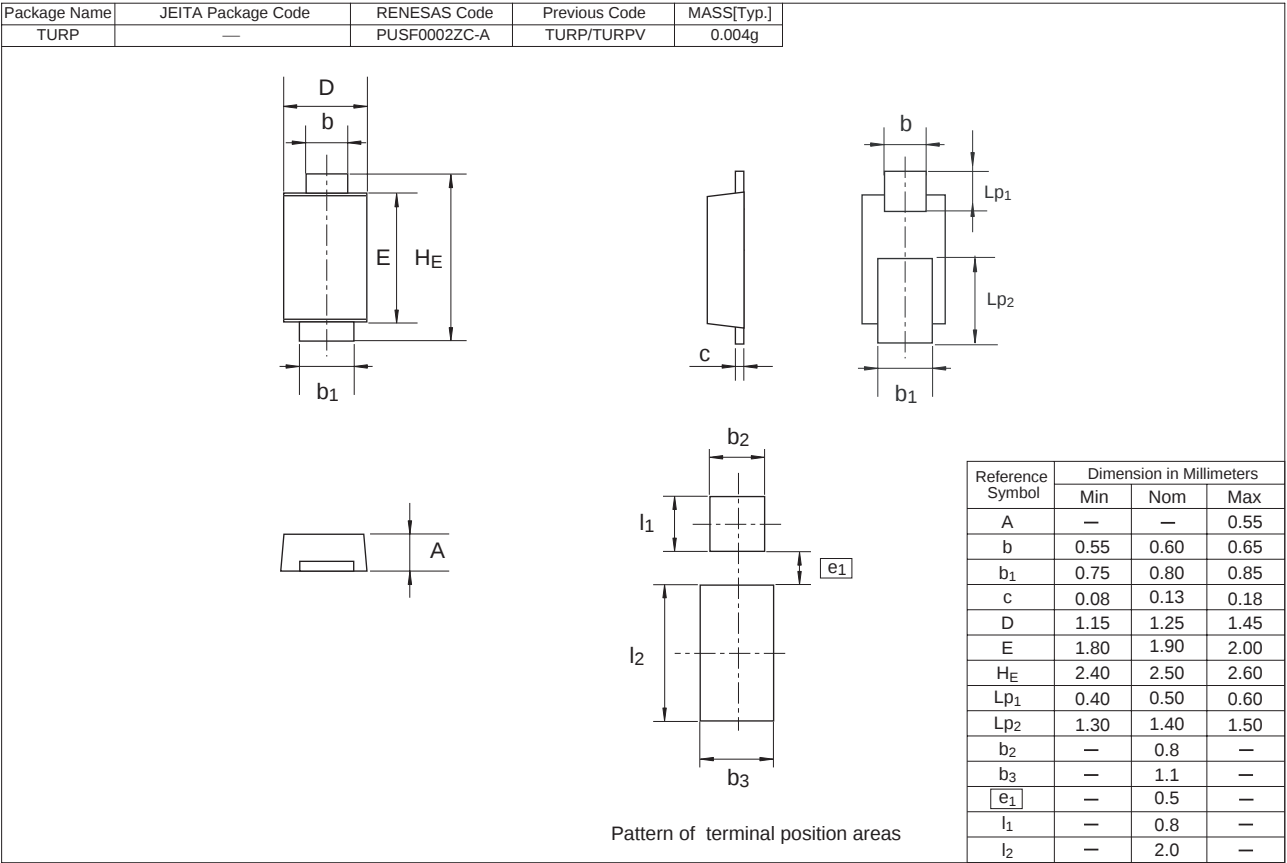


Fig.6 Average rectified current vs. Ambient temperature

Package Dimensions



Notes:

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