

May 2007

FGB3040CS

EcoSPARKTM 300mJ, 400V, N-Channel Current Sensing Ignition IGBT

General Description

The FGB3040CS is an Ignition IGBT that offers outstanding SCIS capability along with a ratiometric emitter current sensing capability. This sensing is based on a emitter active area ratio of 200:1. The output is provided through a fourth (sense) lead. This signal provides a current level that is proportional to the main collector to emitter current. The effective ratio as measured on the sense lead is a function of the sense output, the collector current and the gate to emitter drive voltage.

Applications

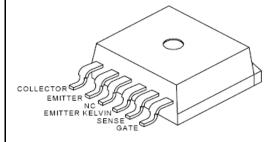
- Smart Automotive Ignition Coil Driver Circuits
- ECU Based Systems
- Distributorless Based Systems
- Coil on Plug Based Systems

Features

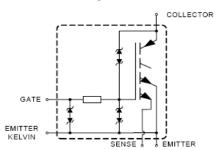
- SCIS Energy = 300mJ at T_J = 25°C
- Logic Level Gate Drive



Package



Symbol



Device Maximum Ratings $T_A = 25$ °C unless otherwise noted

| Symbol | Parameter | Ratings | Units |
|---------------------|-------------------------------------------------------------------------------------------|------------|-------|
| BV _{CER} | Collector to Emitter Breakdown Voltage (I _C = 2mA) | 430 | V |
| BV _{ECS} | Emitter to Collector Breakdown Voltage (I _C = 1mA) (Reverse Battery Condition) | 24 | V |
| E _{SCIS25} | Self Clamping Inductive Switching Energy (at starting T _J = 25°C) | 300 | mJ |
| | Self Clamping Inductive Switching Energy (at starting T _J = 150°C) | 170 | mJ |
| I _{C25} | Continuous Collector Current, at V _{GE} = 4.0V, T _C = 25°C | 21 | Α |
| I _{C110} | Continuous Collector Current, at V _{GE} = 4.0V, T _C = 110°C | | Α |
| V_{GEM} | Maximum Continuous Gate to Emitter Voltage | ±10 | V |
| D | Power Dissipation, at T _C = 25°C | | W |
| P_{D} | Power Dissipation Derating, for T _C > 25°C | 1 | W/°C |
| T_{J} | Operating Junction Temperature Range | -40 to 175 | °C |
| T _{STG} | Storage Junction Temperature Range | -40 to 175 | °C |
| T_L | | | °C |
| T _{PKG} | Max. Package Temp. for Soldering (Package Body for 10 sec) | 260 | °C |
| ESD | Electrostatic Discharge Voltage, HBM model (100pfd, 1500 ohms) | 4 | kV |

Package Marking and Ordering Information

| Device Marking | Device | Package | Reel Size | Tape Width | Quantity |
|----------------|-----------|---------------|-----------|------------|----------|
| 3040CS | FGB3040CS | TO-263 6 Lead | 300mm | 24mm | 800 |
| 3040CS | FGB3040CS | TO-263 6 Lead | Tube | N/A | 50 |

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

| Symbol Parameter Test Conditions Min Typ Max Units |
|----------------------------------------------------------------|
|----------------------------------------------------------------|

Off State Characteristics

| BV _{CER} | Collector to Emitter Breakdown Voltage | $I_{CE} = 2\text{mA}, V_{GE} = 0,$ $R_{GE} = 1\text{K}\Omega, \text{ See Fig. 17}$ $T_{J} = -40 \text{ to } 150^{\circ}\text{C}$ | | 370 | 410 | 430 | V |
|-------------------|----------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------|----------------------------------|-----|-----|-----|----|
| BV _{CES} | $T_{\rm J}$ = -40 to 150°C | | | 390 | 430 | 450 | V |
| BV _{ECS} | Emitter to Collector Breakdown Voltage | I_{CE} = -75mA, V_{GE} = 0V, T_{C} = 25°C | | 30 | - | 1 | V |
| BV _{GES} | Gate to Emitter Breakdown Voltage | $I_{GES} = \pm 2mA$ | | ±12 | ±14 | - | V |
| I_{GEO} | Gate to Emitter Leakage Current | $V_{GE} = \pm 10V$ | | - | - | ±9 | μА |
| | Collector to Emitter Leakage Current | V _{CES} = 250V, | $T_{\rm C} = 25^{\rm o}{\rm C}$ | - | - | 25 | μА |
| ICES | Collector to Emitter Leakage Current | See Fig. 13 | $T_{\rm C} = 150^{\rm o}{\rm C}$ | - | - | 1 | mA |
| | Emittanta Callactan Laglaga Comment | V _{EC} = 24V, | T _C = 25°C | - | - | 1 | A |
| IECS | | See Fig. 13 | T _C = 150°C | - | - | 40 | mA |
| R ₁ | Series Gate Resistance | | | ı | 100 | 1 | Ω |

On State Characteristics

| V _{CE(SAT)} | Collector to Emitter Saturation Voltage | I _{CE} = 6A, V _{GE} = 4V | $T_C = 25^{\circ}C$ See Fig. 5 | ı | 1.3 | 1.6 | V |
|----------------------|-----------------------------------------|-----------------------------------------------|--------------------------------------|---|-----|------|---|
| V _{CE(SAT)} | Collector to Emitter Saturation Voltage | I _{CE} = 10A, V _{GE} = 4.5V | T _C = 150°C See Fig. 6 | 1 | 1.6 | 1.85 | V |
| V _{CE(SAT)} | Collector to Emitter Saturation Voltage | I_{CE} = 15A, V_{GE} = 4.5V | $T_{\rm C} = 150^{\rm o}{\rm C}$ | 1 | 1.8 | 2.35 | V |
| I _{CE(ON)} | Collector to Emitter On State Current | V_{CE} = 5V, V_{GE} = 5V | | - | 37 | - | Α |

Dynamic Characteristics

| Q _{G(ON)} | Gate Charge | I _{CE} = 10A, V _{CE} = 12V, V _{GE} = 5V, See Fig. 16 | | - | 15 | - | nC |
|--------------------|-----------------------------------|------------------------------------------------------------------------------------|----------------------------------|------|-------|-----|----|
| V | Gate to Emitter Threshold Voltage | I _{CE} = 1mA, V _{CE} = V _{GE} | $T_C = 25^{\circ}C$ | 1.3 | 1.6 | 2.2 | V |
| $V_{GE(TH)}$ | Gate to Emitter Threshold Voltage | See Fig. 12 | $T_{\rm C} = 150^{\rm o}{\rm C}$ | 0.75 | 1.1 | 1.8 | V |
| V_{GEP} | Gate to Emitter Plateau Voltage | I _{CE} = 10A, V _{CE} = 12V | | - | 3.0 | | V |
| β_{AREA} | Emitter Sense Area Ratio | Sense Area/Total Area | | - | 1/200 | | - |
| $\beta_{5\Omega}$ | Emitter Current Sense Ratio | I_{CE} = 8.0A, V_{GE} = 5V, R_{SEI} | NSE = 5 Ω | - | 230 | | - |
| $\beta_{20\Omega}$ | Emitter Current Sense Ratio | I_{CE} = 9.0A, V_{GE} = 5V, R_{SE} | NSE = 20 Ω | 550 | 640 | 765 | - |

Switching Characteristics

| $t_{d(ON)R}$ | Current Turn-On Delay Time-Resistive | 02 | - | 0.6 | 4 | μ\$ |
|----------------------|---------------------------------------|-----------------------------------------------------------------------------------------------|---|-----|-----|-----|
| t_{rR} | Current Rise Time-Resistive | V_{GE} = 5V, R_G = 1K Ω T_J = 25°C, See Fig. 14 | 1 | 1.5 | 7 | μS |
| t _{d(OFF)L} | Current Turn-Off Delay Time-Inductive | $V_{CE} = 300V, L = 500\mu Hy,$ | - | 4.7 | 15 | μS |
| t _{fL} | Current Fall Time-Inductive | V_{GE} = 5V, R_G = 1K Ω T _J = 25°C, See Fig. 14 | ı | 2.6 | 15 | μS |
| SCIS | Self Clamped inductive Switching | T_J = 25°C, L = 3.0mHy, I_{CE} = 14.2A, R_G = 1k Ω , V_{GE} = 5V, See Fig. 3&4 | i | ı | 300 | mJ |

Thermal Characteristics

| $R_{\theta JC}$ | Thermal Resistance Junction to Case | All Packages | 1 | 1 | 1.0 | °C/W |
|-----------------|-------------------------------------|--------------|---|---|-----|------|

Typical Performance Curves

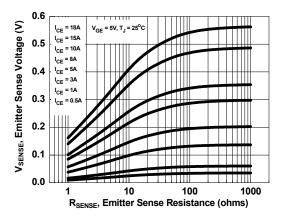


Figure 1. Emitter Sense Voltage vs. Emitter Sense Resistance

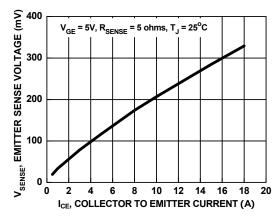


Figure 2. Emitter Sense Voltage vs. Collector to Emitter Current

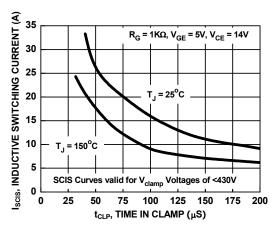


Figure 3. Self Clamped Inductive Switching Current vs. Time in Clamp

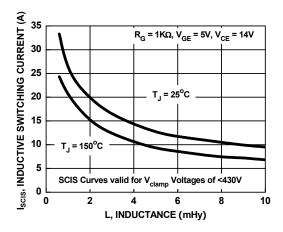


Figure 4. Self Clamped Inductive Switching Current vs. Inductance

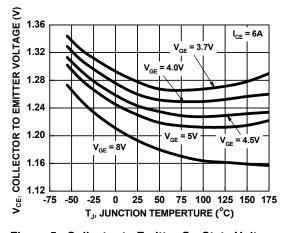


Figure 5. Collector to Emitter On-State Voltage vs. Junction Temperature

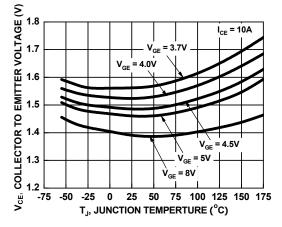


Figure 6. Collector to Emitter On-State Voltage vs. Junction Temperature

Typical Performance Curves (Continued)

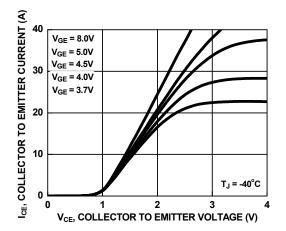


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

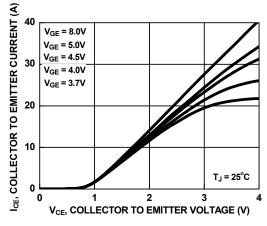


Figure 8. Collector to Emitter On-State Voltage vs. Collector Current

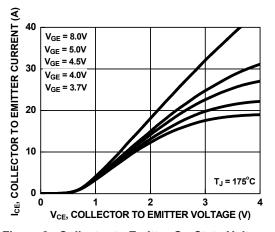


Figure 9. Collector to Emitter On-State Voltage vs. Collector Current

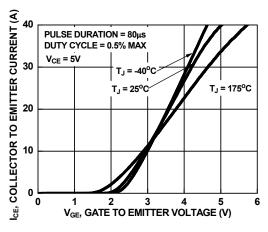


Figure 10. Transfer Characteristics

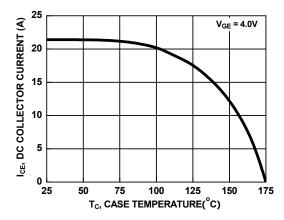


Figure 11. DC Collector Current vs. Case Temperature

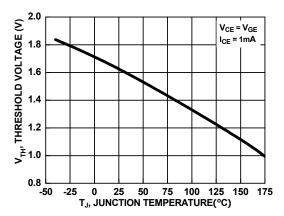


Figure 12. Threshold Voltage vs. Junction Temperature

Typical Performance Curves (Continued)

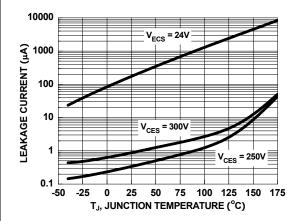


Figure 13. Leakage Current vs. Junction Temperature

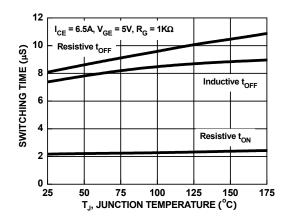


Figure 14. Switching Time vs. Junction Temperature

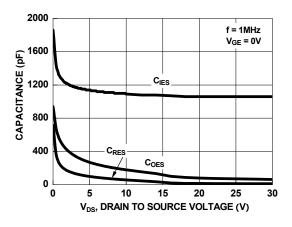


Figure 15. Capacitance vs. Collector to Emitter Voltage

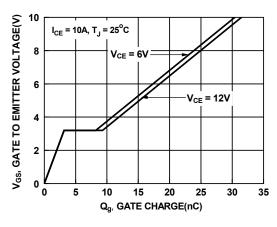


Figure 16. Gate Charge

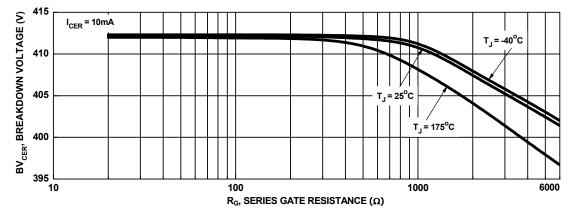


Figure 17. Break down Voltage vs. Series Gate Resistance

Typical Performance Curves

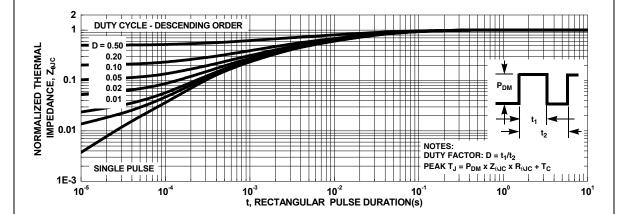


Figure 18. IGBT Normalized Transient Thermal Impedance, Junction to Case

Test Circuit and Waveforms

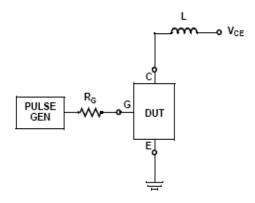


Figure 19. Inductive Switching Test Circuit

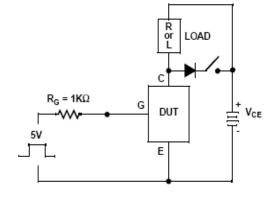


Figure 20. t_{ON} and t_{OFF} Switching Test Circuit

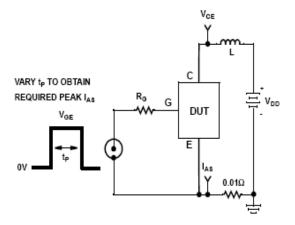


Figure 21. Energy Test Circuit

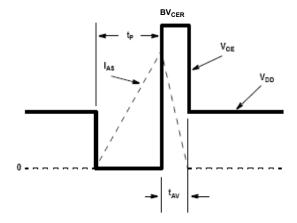
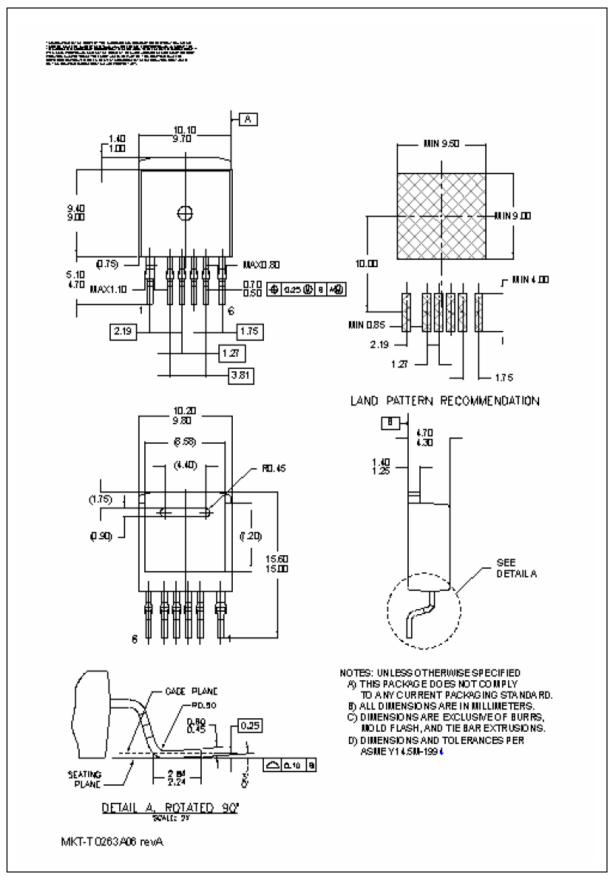


Figure 22. Energy Waveforms







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