



SANYO Semiconductors

## DATA SHEET

# FTD2019A

N-Channel Silicon MOSFET Transistor

## Load Switching Applications

### Features

- Low ON-resistance.
- 2.5V drive.
- Mount height 1.1mm.
- Composite type, facilitating high-density mounting.

### Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		30	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±12	V
Drain Current (DC)	I <sub>D</sub>		6	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	40	A
Allowable Power Dissipation	P <sub>D</sub>	Mounted on a ceramic board (1000mm <sup>2</sup> ×0.8mm)1unit	1.3	W
Total Dissipation	P <sub>T</sub>	Mounted on a ceramic board (1000mm <sup>2</sup> ×0.8mm)	1.4	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±8V, V <sub>DS</sub> =0V			±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.5		1.3	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =6A	7.8	13		S
Static Drain-to-Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =6A, V <sub>GS</sub> =4V		19	26	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =3A, V <sub>GS</sub> =3.1V		21	32	mΩ
	R <sub>DS(on)3</sub>	I <sub>D</sub> =3A, V <sub>GS</sub> =2.5V		23	34	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, f=1MHz		1430		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =10V, f=1MHz		195		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>DS</sub> =10V, f=1MHz		190		pF

Marking : D2019A

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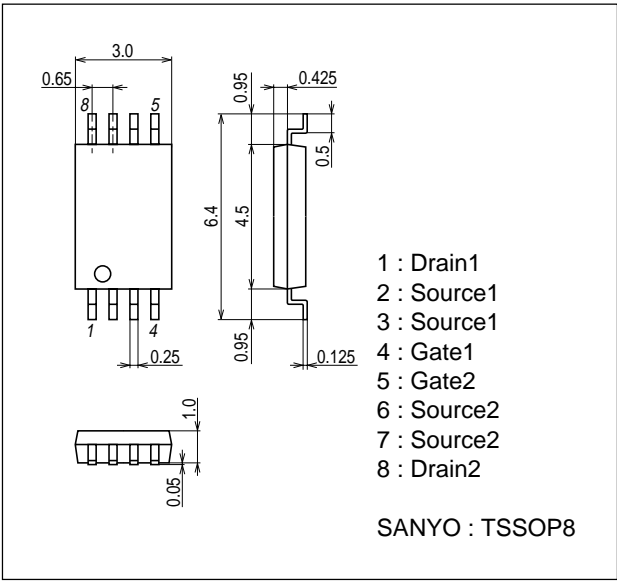
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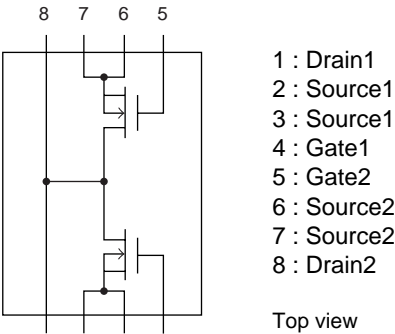
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			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		24		ns
Rise Time	$t_r$	See specified Test Circuit.		165		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		110		ns
Fall Time	$t_f$	See specified Test Circuit.		130		ns
Total Gate Charge	$Q_g$	$V_{DS}=10V, V_{GS}=4V, I_D=6A$		19		nC
Gate-to-Source Charge	$Q_{gs}$	$V_{DS}=10V, V_{GS}=4V, I_D=6A$		3.2		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$	$V_{DS}=10V, V_{GS}=4V, I_D=6A$		4.5		nC
Diode Forward Voltage	$V_{SD}$	$I_S=6A, V_{GS}=0V$		0.83	1.2	V

Package Dimensions

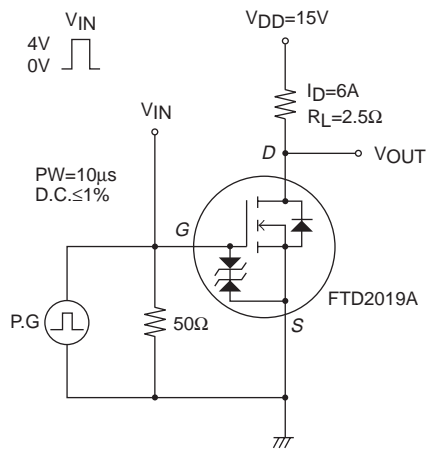
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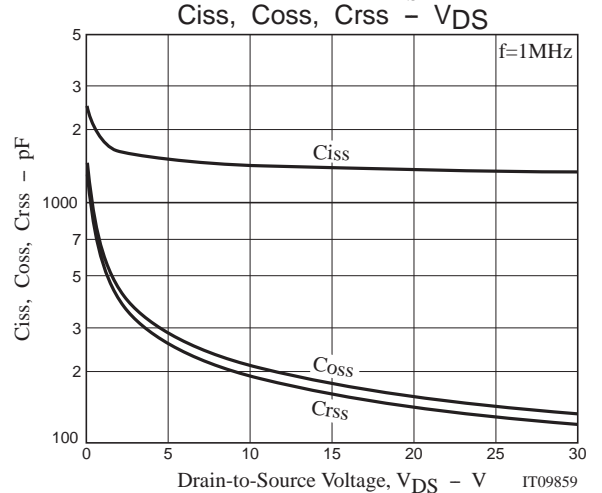
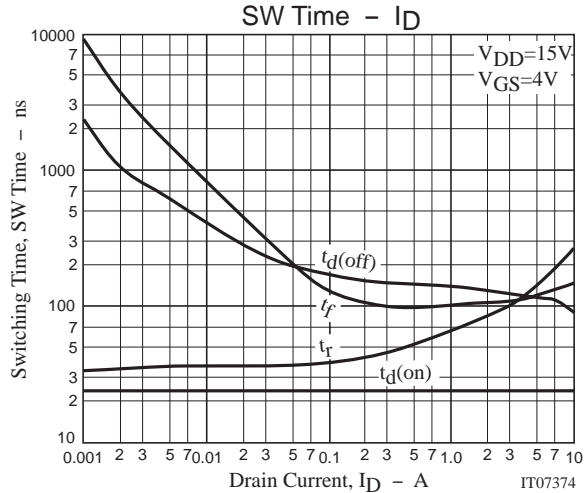
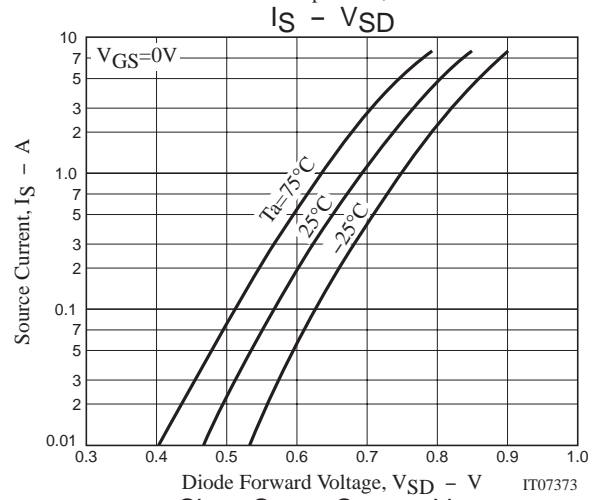
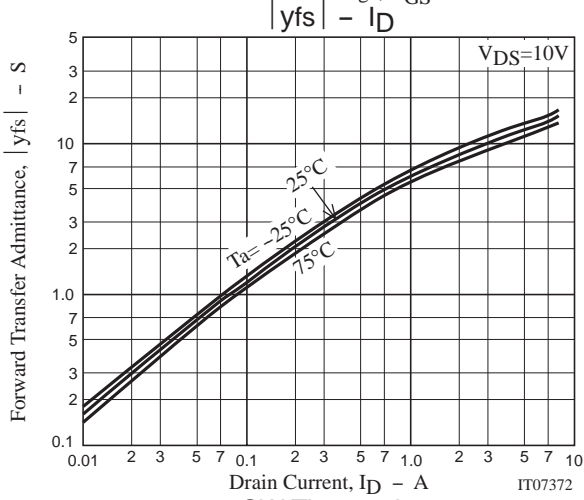
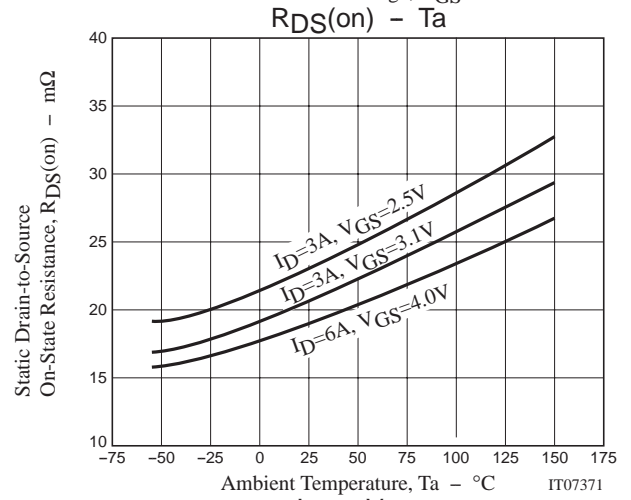
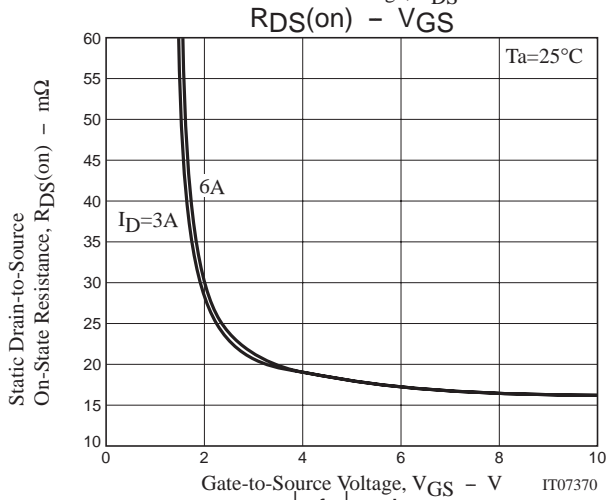
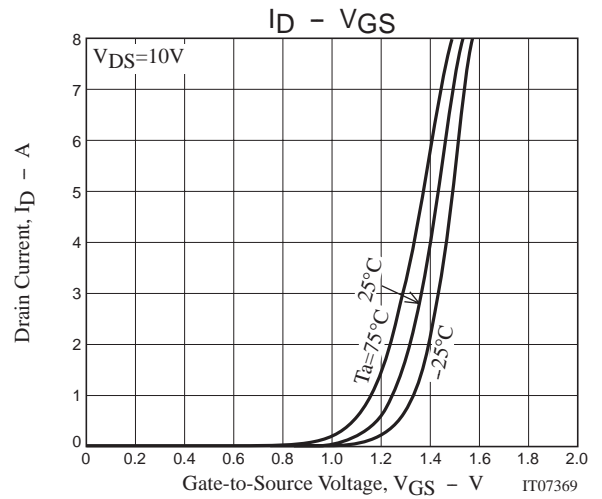
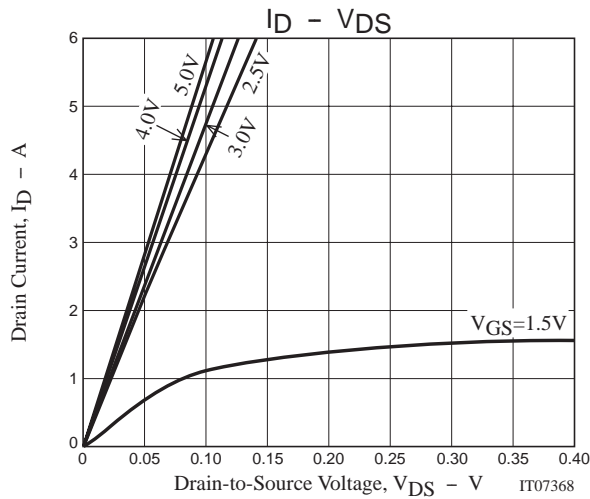


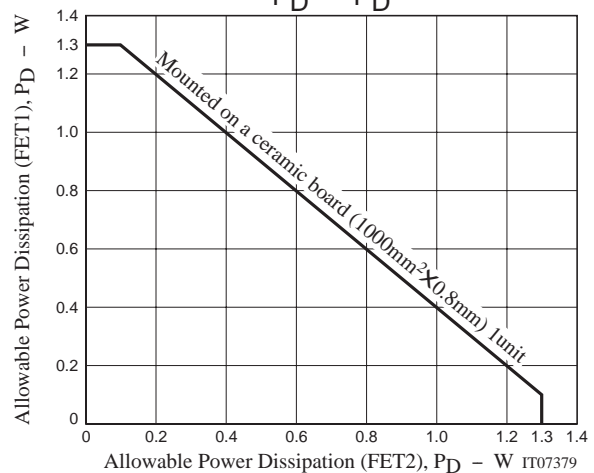
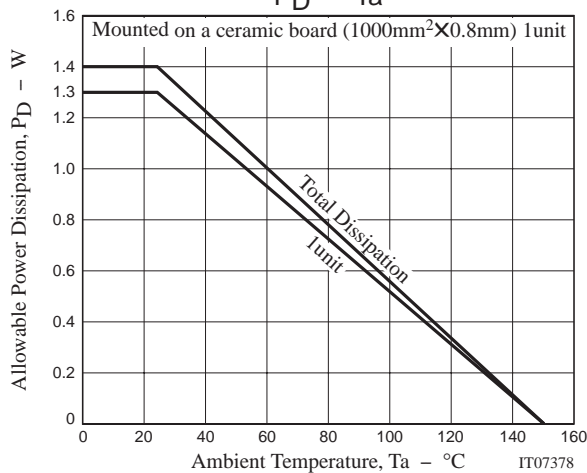
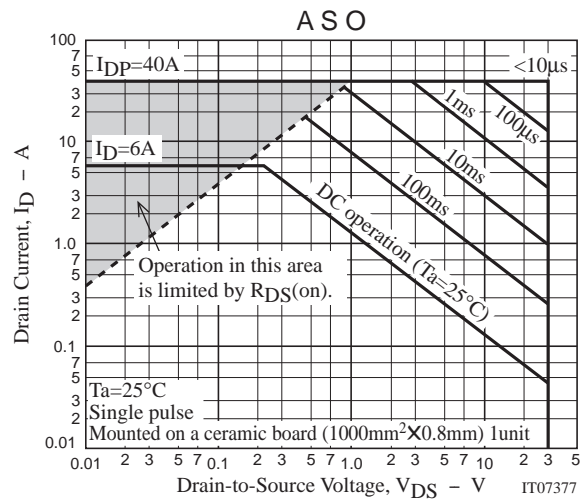
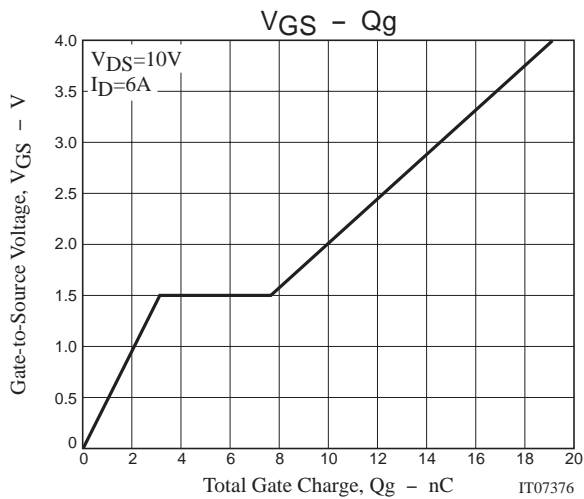
Electrical Connection



Switching Time Test Circuit







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