

SANYO Semiconductors DATA SHEET

FTS2022 — DC / DC Converter Applications

Features

- · Low ON-resistance.
- · 4V drive.
- · Ultrahigh-speed switching.
- · Mounting height 1.1mm.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		7	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	28	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm ² X0.8mm) 1unit	1.3	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			1.1
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =7A	7.7	11		S
Static Drain-to-Source On-State Resistance	RDS(on) 1	ID=7A, VGS=10V		19	25	mΩ
	R _{DS} (on) 2	I _D =4A, V _G S=4.5V		27	38	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		750		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		300		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		120		pF

Marking: S2022 Continued on next page.

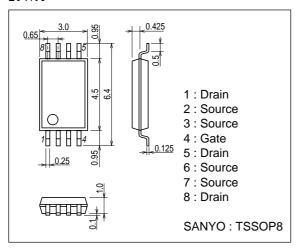
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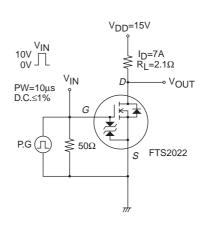
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onn
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		10		ns
Rise Time	t _r	See specified Test Circuit.		147		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		53		ns
Fall Time	tf	See specified Test Circuit.		58		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =7A		14		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =7A		2.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =7A		1.3		nC
Diode Forward Voltage	V _{SD}	I _S =7A, V _{GS} =0			1.2	V

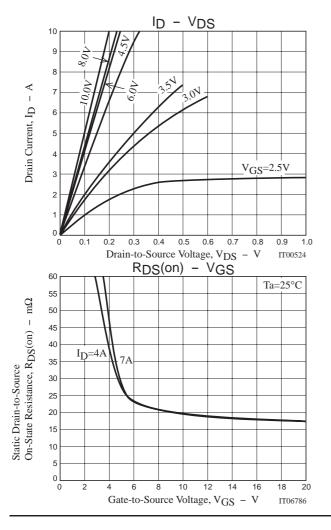
Package Dimensions

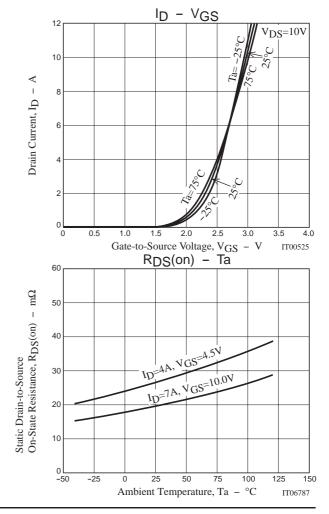
unit : mm 2147A

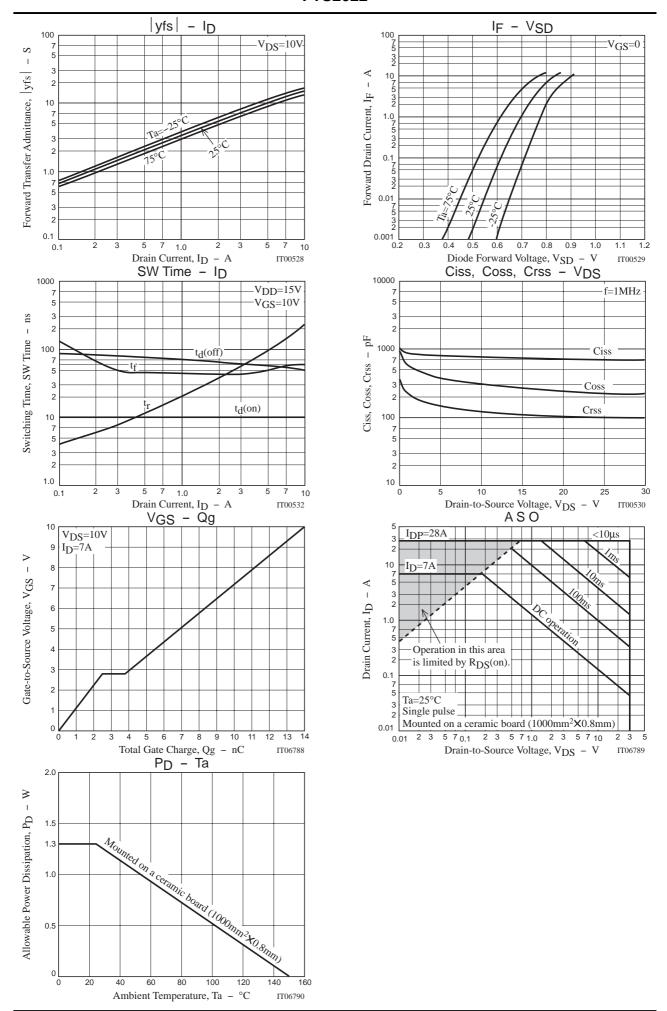


Switching Time Test Circuit









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