

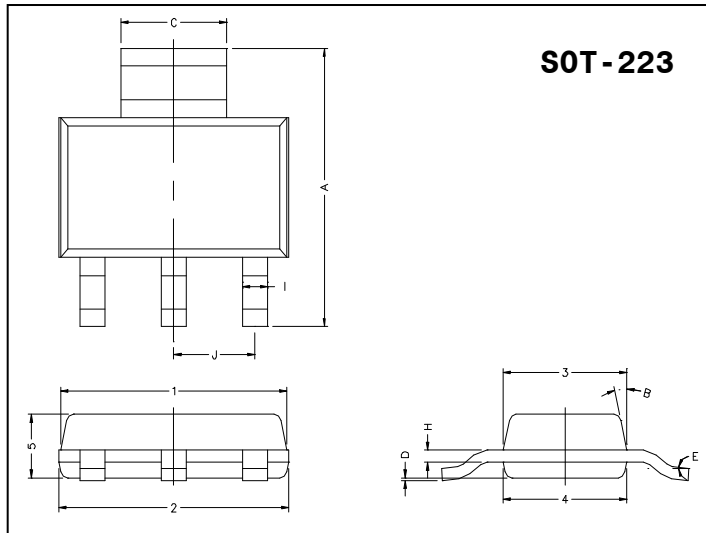
GL5401

PNP EPITAXIAL PLANAR TRANSISTOR

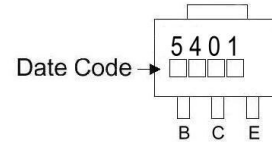
Description

The GL5401 is designer for general purpose applications requiring high breakdown voltages.

Package Dimensions



Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.70	7.30	B	13 TYP.	
C	2.90	3.10	J	2.30 REF.	
D	0.02	0.10	1	6.30	6.70
E	0°	10°	2	6.30	6.70
I	0.60	0.80	3	3.30	3.70
H	0.25	0.35	4	3.30	3.70
			5	1.40	1.80

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Junction Temperature	T _J	+150	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C
Collector to Base Voltage	V _{CB0}	-160	V
Collector to Emitter Voltage	V _{CEO}	-150	V
Emitter to Base Voltage	V _{EBO}	-5	V
Collector Current	I _C	-600	mA
Total Power Dissipation	P _D	1.5	W

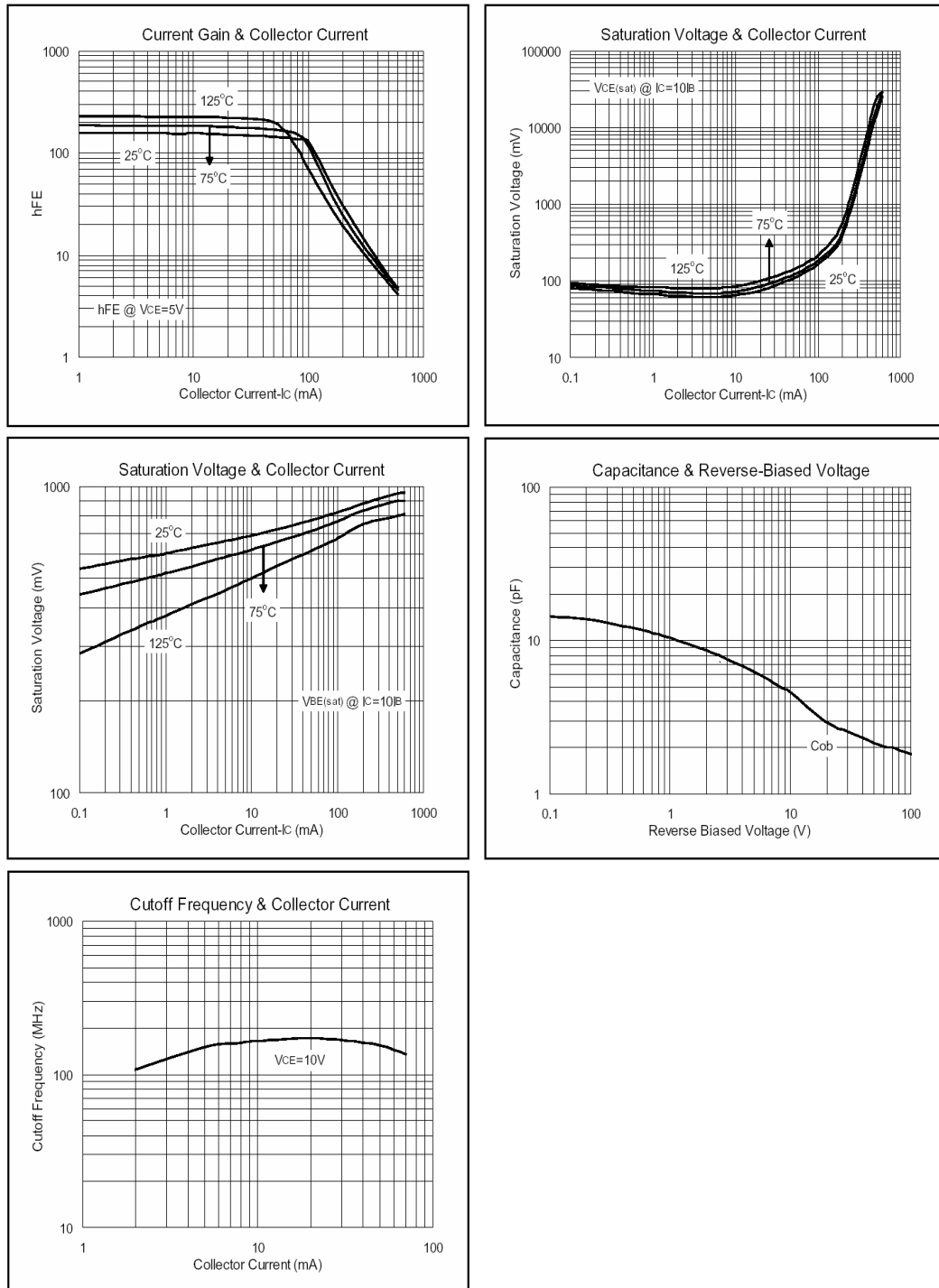
Characteristics at Ta = 25°C

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
V _{CB0}	-160	-	-	V	I _C =-100μA, I _E =0
V _{CEO}	-150	-	-	V	I _C =-1mA, I _B =0
V _{EBO}	-5	-	-	V	I _E =-10μA, I _C =0
I _{CB0}	-	-	-50	nA	V _{CB} =-120V, I _E =0
I _{EBO}	-	-	-50	nA	V _{EB} =-3V, I _C =0
V _{CE(sat)1}	-	-	-200	mV	I _C =-10mA, I _B =-1mA
V _{CE(sat)2}	-	-	-500	mV	I _C =-50mA, I _B =-5mA
V _{BE(sat)1}	-	-	-1	V	I _C =-10mA, I _B =-1mA
V _{BE(sat)2}	-	-	-1	V	I _C =-50mA, I _B =-5mA
h _{FE1}	50	-	-		V _{CE} =-5V, I _C =-1mA
h _{FE2}	80	160	400		V _{CE} =-5V, I _C =-10mA
h _{FE3}	50	-	-		V _{CE} =-5V, I _C =-50mA
f _T	100	-	300	MHz	V _{CE} =-10V, I _C =-10mA, f=100MHz
C _{ob}	-	-	6	pF	V _{CB} =-10V, I _E =0, f=1MHz

Classification Of hFE

Rank	A	N	C
Range	80 - 200	100 - 240	160 - 400

Characteristics Curve



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