

STTH1R02

Ultrafast recovery diode

Main product characteristics

I _{F(AV)}	1.5 A
V _{RRM}	200 V
T _j (max)	175 °C
V _F (typ)	0.7 V
t _{rr} (typ)	15 ns

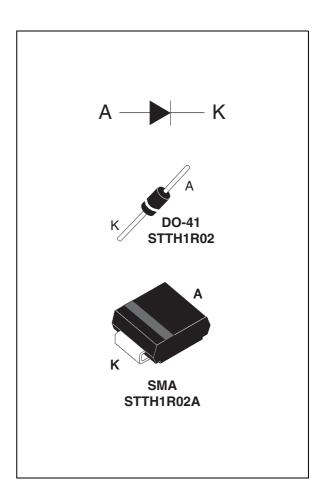
Features and benefits

- Very low conduction losses
- Negligible switching losses
- Low forward and reverse recovery times
- High junction temperature

Description

The STTH1R02 uses ST's new 200 V planar Pt doping technology, and it is specially suited for switching mode base drive and transistor circuits.

Packaged in DO-41 and SMA, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection.



Order codes

Part Number	Marking
STTH1R02	STTH1R02
STTH1R02RL	STTH1R02
STTH1R02A	R1A

Characteristics STTH1R02

Characteristics 1

Absolute ratings (limiting values at T_j = 25° C, unless otherwise specified) Table 1.

Symbol	Parameter			Value	Unit
V _{RRM}	Repetitive peak reverse voltage			200	V
	Repetitive peak forward current		$t_p = 5 \mu s, F = 5 kHz$	- 30	Α
I _{FRM}			SMA		A
	DMO forward comment		DO-41		Α
IF(RMS)	RMS forward current	SMA		50	A
1	A common formation of the comm	verage forward current, $\delta = 0.5$ $\frac{\text{DO-41}}{\text{SMA}} \frac{\text{T}_{\text{lead}} = 110^{\circ} \text{ C}}{\text{T}_{\text{c}} = 110^{\circ} \text{ C}}$		1.5	Α
I _{F(AV)}	Average lorward current, 0 = 0.3			1.5	^
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms	Sinusoidal	60	Α
T _{stg}	Storage temperature range	e temperature range			°C
Tj	Maximum operating junction temperature	DO-41 ⁽¹⁾		175	ڻ ت
	iviaximum operating junction temperature	SMA		150)

^{1.} On infinite heatsink with 10 mm lead length

Table 2. Thermal parameters

Symbol	Parameter			Value	Unit
D	Junction to lead	Lead Length = 10 mm on infinite heatsink	DO-41	45	°C/W
R _{th(j-c)}	Junction to case		SMA	30	C/VV

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
I _R ⁽¹⁾	Povorco logizado gurrant	T _j = 25 °C	V - V			3	^
'R`	I _R ⁽¹⁾ Reverse leakage current	T _j = 125 °C	$V_R = V_{RRM}$		2	20	μA
	V _F ⁽²⁾ Forward voltage drop	T _j = 25 °C	I _F = 4.5 A			1.2	
V (2)		T _j = 25 °C			0.89	1	V
VF`		T _j = 100 °C	I _F = 1.5 A		0.76	0.85	ď
		T _j = 150 °C			0.70	0.80	

^{1.} Pulse test: t_p = 5 ms, δ < 2 %

To evaluate the conduction losses use the following equation: P = 0.68 x $I_{F(AV)}$ + 0.08 $I_{F}^{2}_{(RMS)}$

$$P = 0.68 \times I_{F(AV)} + 0.08 I_{F}^{2}(RMS)$$

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^{2.} Pulse test: t_p = 380 μ s, δ < 2 %

STTH1R02 Characteristics

Table 4. Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур	Max.	Unit
		$I_F = 1 \text{ A, } dI_F/dt = -50 \text{ A/}\mu\text{s,}$ $V_R = 30 \text{ V, } T_j = 25 \text{ °C}$		23	30	ns
t _{rr} Reverse recovery time	$I_F = 1 \text{ A, } dI_F/dt = -100 \text{ A/}\mu\text{s,}$ $V_R = 30 \text{ V, } T_j = 25 \text{ °C}$		15	20	ns	
I _{RM}	Reverse recovery current	$I_F = 1.5 \text{ A, } dI_F/dt = -200 \text{ A/}\mu\text{s,}$ $V_R = 160 \text{ V, } T_j = 125 \text{ °C}$		3	4	Α
t _{fr}	Forward recovery time	$I_F = 1.5 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}$ $V_{FR} = 1.1 \text{ x } V_{Fmax}, T_j = 25 \text{ °C}$		50		ns
V _{FP}	Forward recovery voltage	$I_F = 1.5 \text{ A, } dI_F/dt = 100 \text{ A/}\mu\text{s,}$ $T_j = 25 ^{\circ}\text{C}$		2.1		٧

Figure 1. Peak current versus duty cycle

Figure 2. Forward voltage drop versus forward current (typical values)

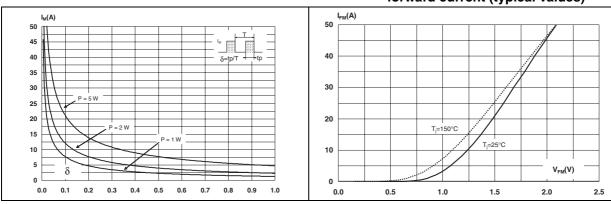
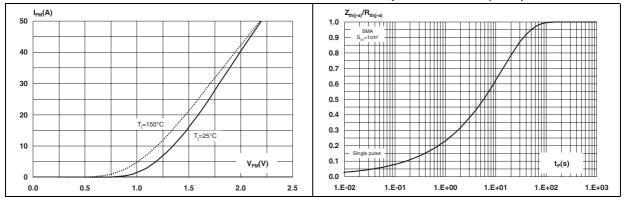


Figure 3. Forward voltage drop versus forward current (maximum values)

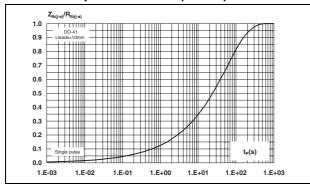
Figure 4. Relative variation of thermal impedance junction to case versus pulse duration (SMA)



Characteristics STTH1R02

Figure 5. Relative variation of thermal impedance junction to case versus pulse duration (DO-41)

Figure 6. Junction capacitance versus reverse applied voltage (typical values)



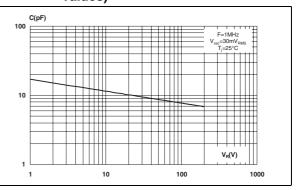
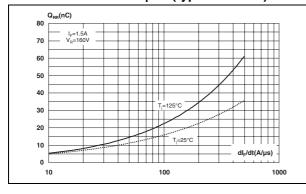


Figure 7. Reverse recovery charges versus dl_F/dt (typical values)

Figure 8. Reverse recovery time versus dI_F/dt (typical values)



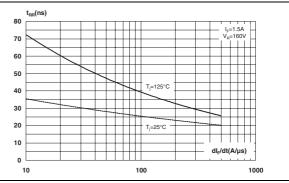
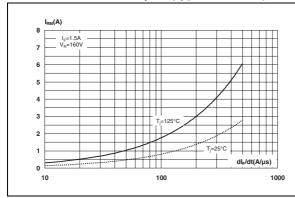


Figure 9. Peak reverse recovery curent versus dl_F/dt (typical values)

Figure 10. Dynamic parameters versus junction temperature



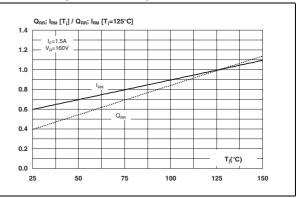


Figure 11. Thermal resistance, junction to ambient, versus copper surface under each lead - SMA (Epoxy FR4, e_{cu} = 35 μ m)

R_{In(+a)}(°C/W)

100

80

60

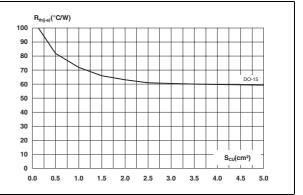
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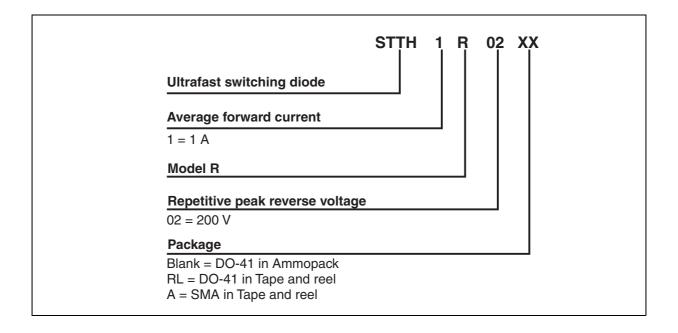
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1 2 3 4 5

Figure 12. Thermal resistance, junction to ambient, versus copper surface under each lead - DO-41 (Epoxy FR4, e_{cu} = 35 μm)



2 Ordering information scheme



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Package information STTH1R02

3 Package information

Epoxy meets UL94, V0

Table 5. DO-41 Dimensions

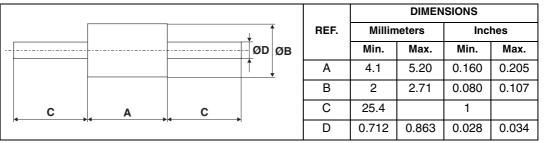


Table 6. SMA dimensions

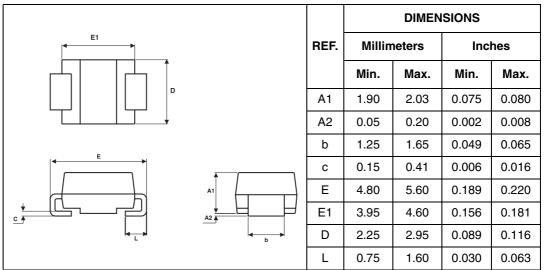
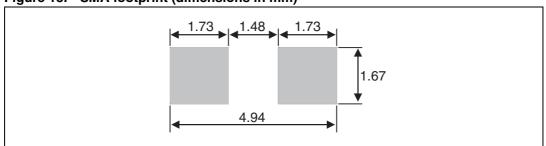


Figure 13. SMA footprint (dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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4 Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode
STTH1R02	STTH1R02	DO-41	0.34 g	2000	Ammopack
STTH1R02RL	STTH1R02	DO-41	0.34 g	5000	Tape and reel
STTH1R02A	R1A	SMA	0.068 g	5000	Tape and reel

5 Revision history

Date	Revision	Description of Changes
03-May-2006	1	First issue

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