TOSHIBA MULTI CHIP DISCRETE DEVICE

HN2E02F

Super High Speed Switching Application Audio Frequency Amplifier Application AM Amplifier Application

Q1

Q2

 $\label{eq:continuous} \begin{array}{ll} \mbox{High Voltage} & :\mbox{V}_{\mbox{CEO}} = 50\mbox{V} \\ \mbox{High Collector Current} & :\mbox{I}_{\mbox{C}} = 150\mbox{mA}(\mbox{max.}) \end{array}$

Good hFE Linearity

 $h_{FE}(I_C=0.1mA)/h_{FE}(I_C=2mA)=0.95$

Q1 (Diode) : 1SS352 Equivalent
Q2 (Transistor) : 2SC4738 Equivalent

Q1 (Diode) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	V_{RM}	85	V
Reverse voltage	V _R	80	V
Maximum (peak) forward current	I _{FM}	300	mA
Average forward current	IO	100	mA
Surge current (10ms)	I _{FSM}	1	Α

Unit: mm 2.8 - 0.3 2.8 - 0.3 4 - 0.2 2.8 - 0.3 4 - 0.2 1.6 - 0.1 2.6 - 0.1 3 - 0.4 - 0.2 2.8 - 0.3 4 - 0.2 2.8 - 0.3 4 - 0.2 2.8 - 0.3 4 - 0.2 2.8 - 0.3 4 - 0.2 2.8 - 0.3 4 - 0.2 2.8 - 0.3 4 - 0.2 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4 2.8 - 0.3 5 - 0.4

Weight: 0.015g (typ.)

Q2 (Transistor) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	60	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	IC	150	mA
Base current	Ι _Β	30	mA

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

	·-		
Characteristic	Symbol	Rating	Unit
Collector power dissipation	P _C *	300	mW
Junction temperature	Tj	125	°C
Storage temperature range	T _{stg}	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

^{*}Total rating: Power dissipation per element should not exceed 200mW per element.

Q1 (Diode) Electrical Characteristics (Ta = 25°C)

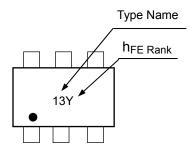
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _{F (1)}	_	I _F = 1mA	_	0.62	ı	٧
	V _{F (2)}	_	I _F = 10mA	ı	0.75	ı	
	V _{F (3)}	_	I _F = 100mA	_	0.98	1.2	
Reverse current	I _{R (1)}	_	V _R = 30V	_	_	0.1	μА
	I _{R (2)}	_	V _R = 80V	-	1	0.5	
Total capacitance	C _T	_	V _R = 0, f = 1MHz	_	0.5	_	pF
Reverse recovery time	t _{rr}	_	I _F = 10mA (fig.1)	_	1.6	_	ns

Q2 (Transistor) Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	_	V _{CB} = 60V, I _E = 0	_	_	100	nA
Emitter cut-off current	I _{EBO}	_	V _{EB} = 5V, I _C = 0	_	_	100	nA
DC current gain	h _{FE} *	_	V _{CE} = 6V, I _C = 2mA	120	_	700	
Collector-emitter saturation voltage	V _{CE(sat)}	_	I _C =100mA, I _B =10mA	_	0.1	0.25	V
Transition Frequency	f _T	_	V _{CE} = 10V, I _C =10mA	60	_	-	MHz
Collector Output Capacitance	C _{ob}	_	V_{CB} = 10V, I_E = 0,f=1MHz	_	2.0	_	pF

^{*} h_{FE} Rank Y(Y): 120~240, GR(G): 200~400,BL(L): 350~700

Marking



Equivalent Circuit (Top View)

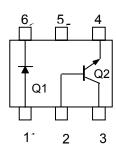
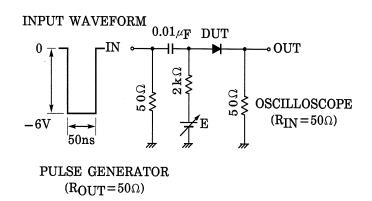
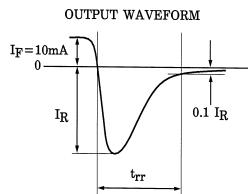


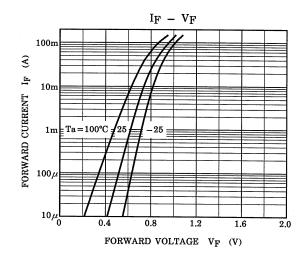
Fig. 1 : Reverse Recovery Time (t_{rr}) Test Circuit

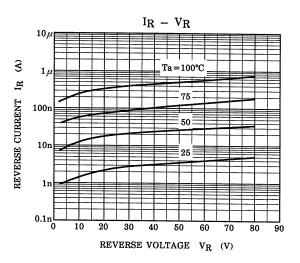


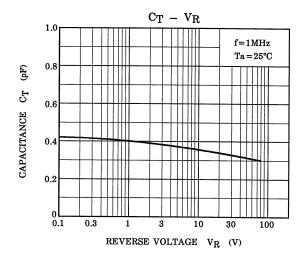


^() Marking Symbol

Q1

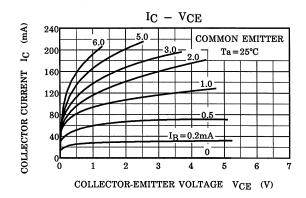


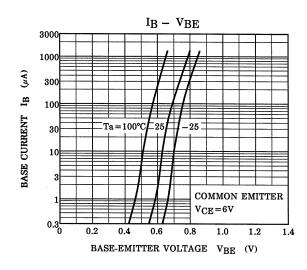


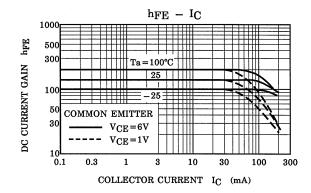


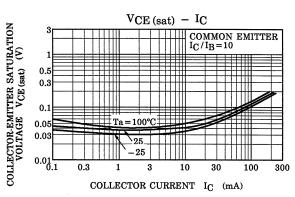
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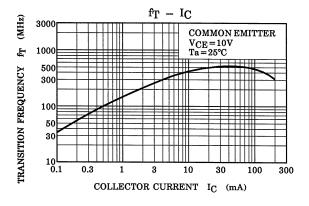
Q2

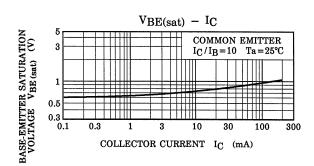




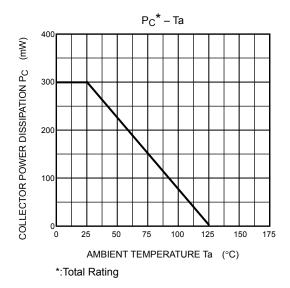








Q1, Q2 Common



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RESTRICTIONS ON PRODUCT USE

Handbook" etc.

20070701-EN GENERAL

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