



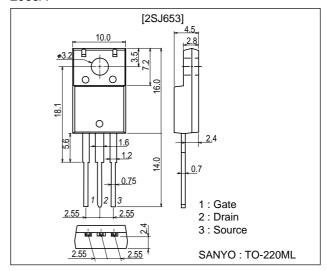
General-Purpose Switching Device Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.
- · Motor drive, DC / DC converter.

Package Dimensions

unit : mm 2063A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-37	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-148	Α
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C	35	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _G S=0	-60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	VDS=-10V, ID=-19A	26.5	38		S

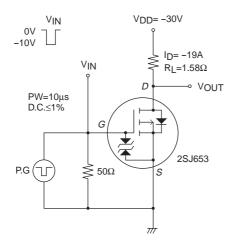
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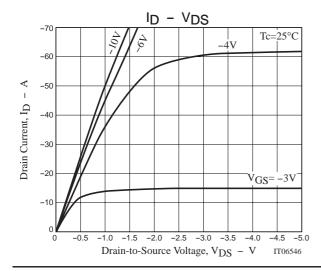
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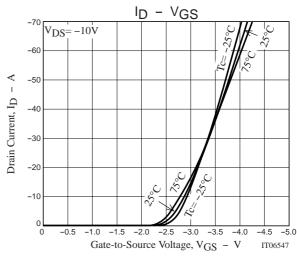
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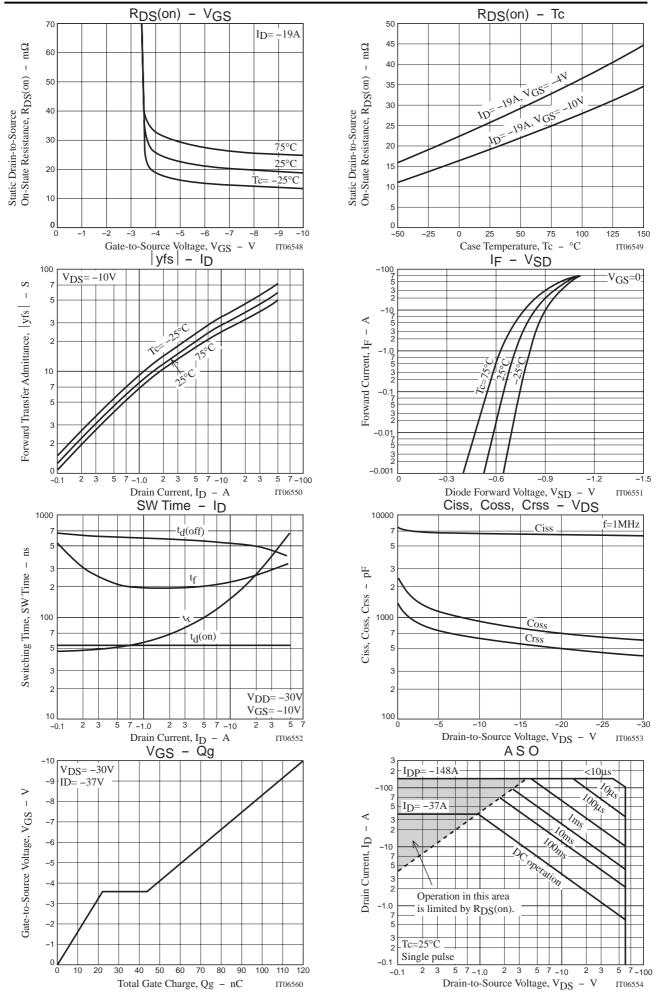
Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-19A, V _G S=-10V		19	25	mΩ
	R _{DS} (on)2	I _D =-19A, V _G S=-4V		26	37	mΩ
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		6500		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		700		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		500		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit.		53		ns
Rise Time	t _r	See specified Test Circuit.		245		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		495		ns
Fall Time	tf	See specified Test Circuit.		255		ns
Total Gate Charge	Qg	V _{DS} =-30V, V _{GS} =-10V, I _D =-37A		120		nC
Gate-to-Source Charge	Qgs	V _{DS} =-30V, V _{GS} =-10V, I _D =-37A		22		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-30V, V _{GS} =-10V, I _D =-37A		22		nC
Diode Forward Voltage	V _{SD}	I _S =-37A, V _G S=0		-0.99	-1.2	V

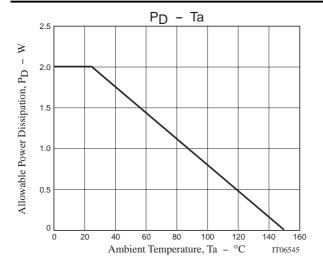
Switching Time Test Circuit

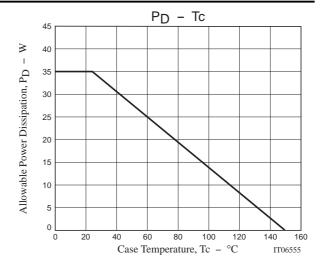












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