



SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

NEC

These products are N-channel MOS Field Effect Transistors designed for high current switching applications.

<R> ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE	
NP82N055EHE-E1-AY Note1, 2			TO-263 (MP-25ZJ) typ. 1.4 g	
NP82N055EHE-E2-AY Note1, 2		Tana 800 n/raal		
NP82N055KHE-E1-AY Note1	Pure Sn (Tin)	Tape 800 p/reel		
NP82N055KHE-E2-AY Note1			TO-263 (MP-25ZK) typ. 1.5 g	
NP82N055CHE-S12-AZ Note1, 2	Sn-Ag-Cu		TO-220 (MP-25) typ. 1.9 g	
NP82N055DHE-S12-AY Note1, 2			TO-262 (MP-25 Fin Cut) typ. 1.8 g	
NP82N055MHE-S18-AY Note1	Pure Sn (Tin)	Tube 50 p/tube	TO-220 (MP-25K) typ. 1.9 g	
NP82N055NHE-S18-AY Note1			TO-262 (MP-25SK) typ. 1.8 g	

Notes 1. Pb-free (This product does not contain Pb in the external electrode.)

2. Not for new design

FEATURES

- Channel temperature 175 degree rated
- Super low on-state resistance
- $R_{DS(on)} = 8.6 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 41 \text{ A})$
- Low input capacitance
- Ciss = 3500 pF TYP.
- Built-in gate protection diode









(TO-263)



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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	55	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25° C) ^{Note1}	ID(DC)	±82	А
Drain Current (pulse) Note2	D(pulse)	±300	А
Total Power Dissipation (T _A = 25°C)	Р⊤	1.8	W
Total Power Dissipation (Tc = 25° C)	Р⊤	163	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	–55 to +175	°C
Single Avalanche Current Note3	las	72/49/17	А
Single Avalanche Energy Note3	Eas	51/240/289	mJ

Notes 1. Calculated constant current according to MAX. allowable channel temperature.

2. PW \leq 10 μ s, Duty cycle \leq 1%

3. Starting T_{ch} = 25°C, V_{DD} = 28 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V (See Figure 4.)

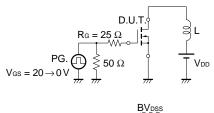
THERMAL RESISTANCE

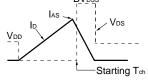
Channel to Case Thermal Resistance	Rth(ch-C)	0.92	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

ELECTRICAL CHARACTERISTICS (TA = 25°C)

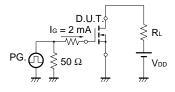
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 55 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V_{GS} = ±20 V, V_{DS} = 0 V			±10	μA
Gate to Source Threshold Voltage	V _{GS(th)}	V_{DS} = V_{GS} , I_D = 250 μ A	2.0	3.0	4.0	V
Forward Transfer Admittance	y fs	V _{DS} = 10 V, I _D = 41 A	19	38		S
Drain to Source On-state Resistance	RDS(on)	V _{GS} = 10 V, I _D = 41 A		6.9	8.6	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V,		3500	5250	pF
Output Capacitance	Coss	$V_{GS} = 0 V,$		550	830	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		270	490	pF
Turn-on Delay Time	td(on)	V _{DD} = 28 V, I _D = 41 A,		31	69	ns
Rise Time	tr	V _{GS} = 10 V, R _G = 1 Ω		18	45	ns
Turn-off Delay Time	td(off)			61	120	ns
Fall Time	tr			19	47	ns
Total Gate Charge	QG	V _{DD} = 44 V,		65	100	nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V, I _D = 82 A		18		nC
Gate to Drain Charge	Qgd			24		nC
Body Diode Forward Voltage	VF(S-D)	IF = 82 A, V _{GS} = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 82 A, V _{GS} = 0 V,		45		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		63		nC

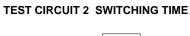
TEST CIRCUIT 1 AVALANCHE CAPABILITY



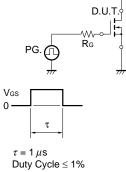


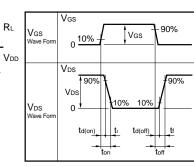
TEST CIRCUIT 3 GATE CHARGE



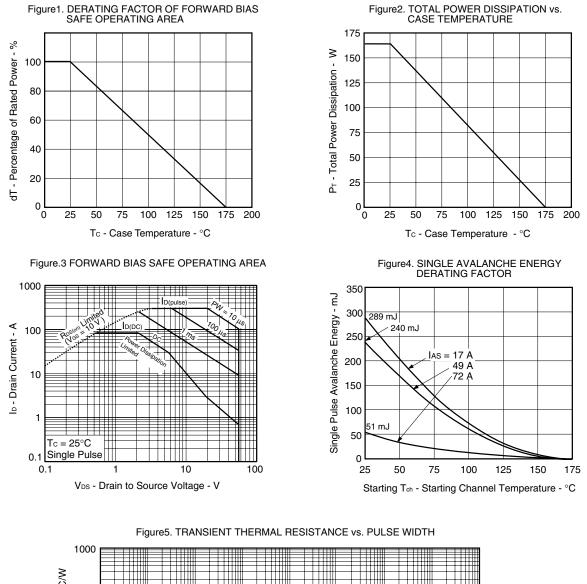


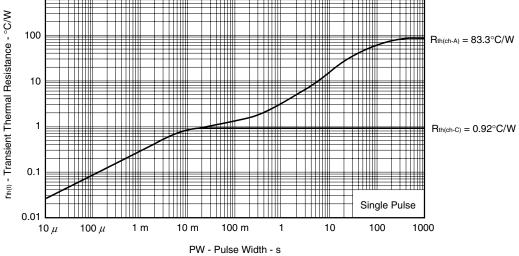
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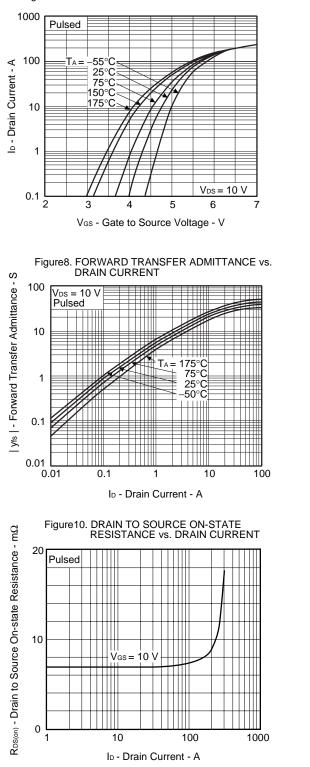
TYPICAL CHARACTERISTICS (TA = 25°C)





Data Sheet D14138EJ6V0DS

Figure6. FORWARD TRANSFER CHARACTERISTICS



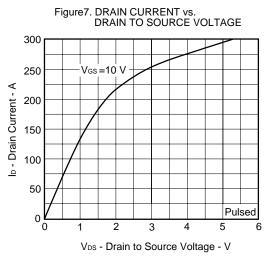


Figure9. DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

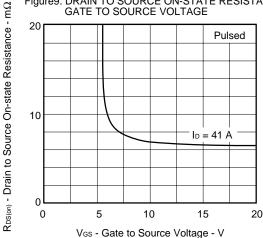
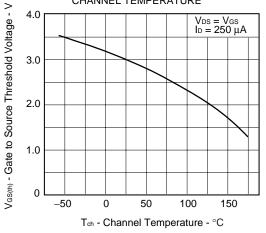
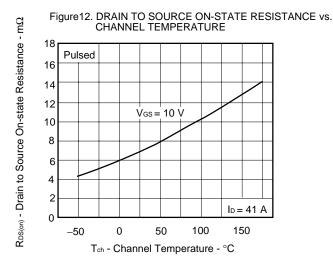
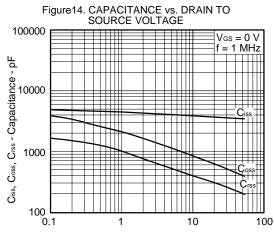
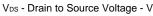


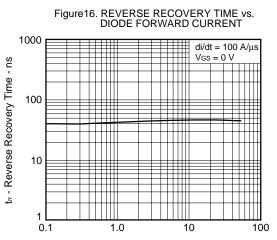
Figure11. GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE



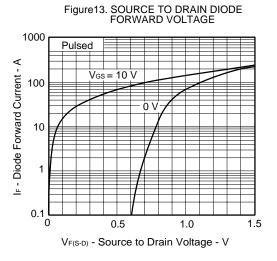




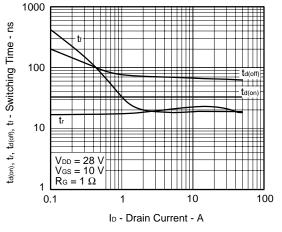


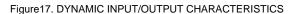


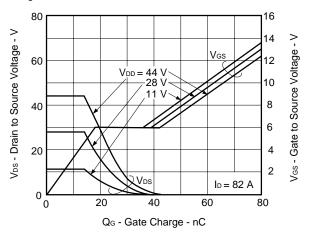
IF - Diode Forward Current - A





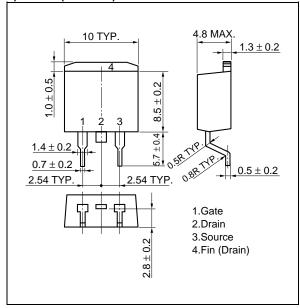




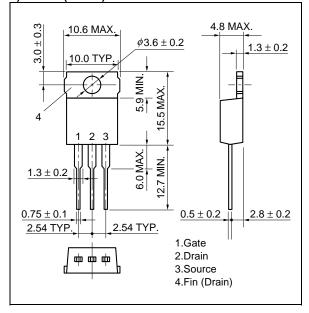


<R> PACKAGE DRAWINGS (Unit: mm)

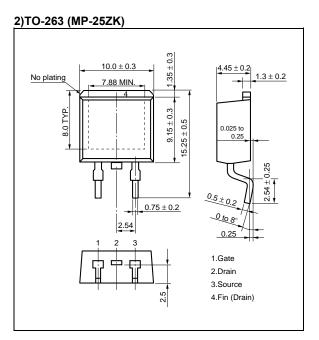
1)TO-263 (MP-25ZJ) Note



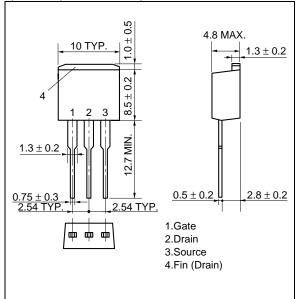
3)TO-220 (MP-25) Note

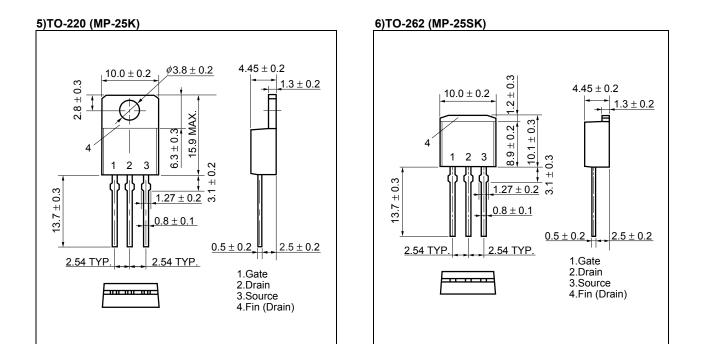


Note Not for new design

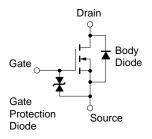


4)TO-262 (MP-25 Fin Cut) Note





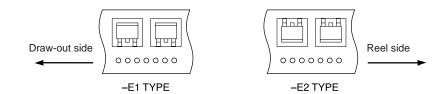
EQUIVALENT CIRCUIT



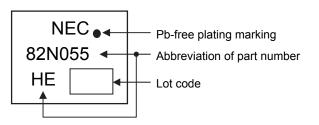
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

<R> TAPE INFORMATION

There are two types (-E1, -E2) of taping depending on the direction of the device.



<R> MARKING INFORMATION



<R> RECOMMENDED SOLDERING CONDITIONS

These products should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol	
Infrared reflow	Maximum temperature (Package's surface temperature): 260°C or below		
MP-25ZJ, MP-25ZK	Time at maximum temperature: 10 seconds or less		
	Time of temperature higher than 220°C: 60 seconds or less		
	Preheating time at 160 to 180°C: 60 to 120 seconds	IR60-00-3	
	Maximum number of reflow processes: 3 times		
	Maximum chlorine content of rosin flux (percentage mass): 0.2% or less		
Wave soldering	Maximum temperature (Solder temperature): 260°C or below		
MP-25, MP-25K, MP-25SK,	Time: 10 seconds or less	THDWS	
MP-25 Fin Cut	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		
Partial heating	Maximum temperature (Pin temperature): 350°C or below		
MP-25ZJ, MP-25ZK,	Time (per side of the device): 3 seconds or less	P350	
MP-25K, MP-25SK	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		
Partial heating	Maximum temperature (Pin temperature): 300°C or below		
MP-25, MP-25 Fin Cut	Time (per side of the device): 3 seconds or less	P300	
	Maximum chlorine content of rosin flux: 0.2% (wt.) or less		

Caution Do not use different soldering methods together (except for partial heating).

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