

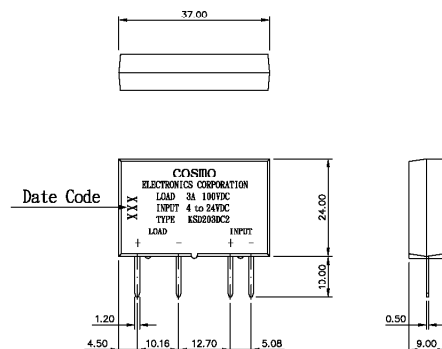
Features

1. Molded epoxy body.
2. High input/output insulation.
3. Small size and light weight.
4. Can be installed directly on the P.C. board.
5. Fast reactive speed.
6. Normally open.

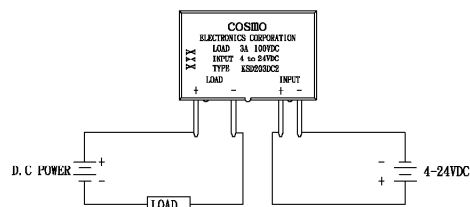
Applications

1. Household Appliances.
2. Temperature Control System.
3. Industrial Automatic Control.
4. Lighting System.
5. Office Appliances.
6. Factory Appliances.

Outside Dimension : Unit (mm)



Schematic : Top View



Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
Input	Input Signal Voltage	V _{IN}	4~24
	Drop-out Voltage	V _{DO}	1
Output	Output Power Dissipation	P _C	30
	Collector Voltage	V _{CEO}	100
Output	Output Current	I _O	3
	Peak Surge Current 50 μs	I _{surge}	9
Isolation Voltage		V _{ISO}	4000
Operating Temperature		T _{OPR}	-30~100
Storage Temperature		T _{STG}	-30~125
Soldering Temperature 10 Sec		T _{sol}	260

Electrical Characteristics

(Ta=25°C)

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
Input	Pick-up Voltage	V _{PU}	I _T =1A _{rms}		4	VDC
	Input Current	I _{IN}	V _{IN} =4-24V		25	mA
	Terminal Capacitance	C _T	V=0, f=1KHz	30		pF
Output	Collector-emitter breakdown voltage	BV _{CEO}	I _F =0	100		
	Output Leak Current	I _{leak}	I _F =0, V=100V		1.5	μA
Collector Current		I _C	I _F =1mA, V _{CE} =2V	0.05	3	A
Collector-Emitter Saturation Voltage		V _{CE(sat)}	I _F =20mA, I _C =100mA		1.5	V
Isolation Resistance		R _{ISO}	DC500V	10 ¹⁰		Ω
Floating Capacitance		C _r	V=0, f=1MHz		3	pF
Cut-Off Frequency		F _C	V _{CE} =2V, I _C =200mA, R _L =100Ω	2		KHz
Response Time (Rise)		T _r	V _{CE} =2V, I _C =20mA		500	μS
Response Time (Fall)		T _f	R _L =100Ω		200	μS

Fig.1 Input Voltage vs. Ambient Temperature

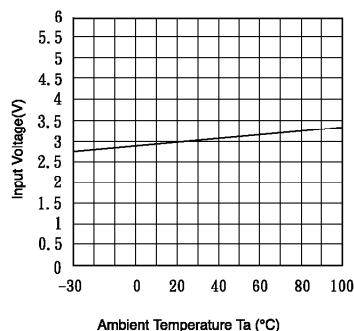


Fig.2 Collector Power Dissipation vs. Ambient Temperature

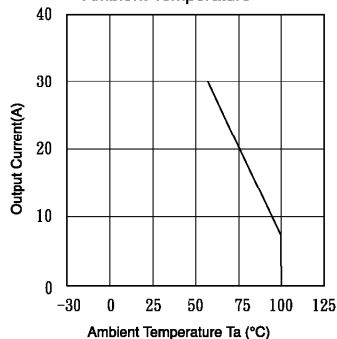


Fig.3 Input Current vs. Input Voltage

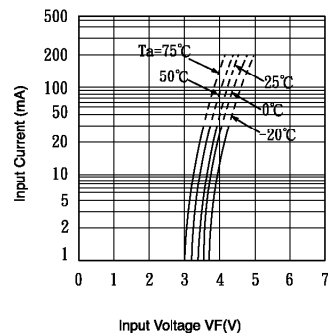


Fig.4 Collector to Emitter Voltage vs. Collector Current

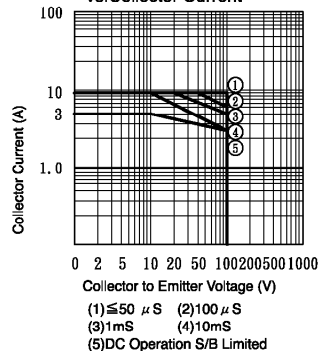


Fig.5 Input Current vs. Input Voltage

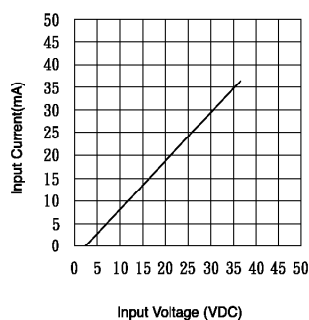


Fig.6 Output Current vs. Ambient Temperature

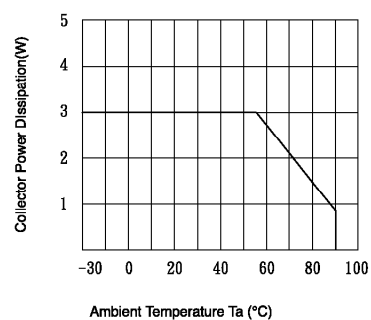


Fig.7 Action Waveform

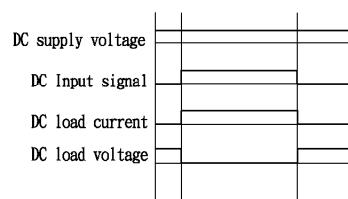


Fig.8 WIRING DIAGRAM

