Chip resistor networks **MNR02** (1005 × 2 size)

Features

1) Extremely small and light

Area ratio is 60% smaller than that of chip 1616 (MNR12), while weight ratio has been cut 75%.

2) High-density mounting

Can be mounted even more densely than two 1005 chips (MCR01). Also, the cost of mounting has been reduced.

3) Compatible with a wide range of mounting equipment.

Squared corners make it excellent for mounting using image recognition devices.

4) Convex electrodes

Easy to check the fillet after soldering is finished.

5) ROHM resistors have obtained ISO-9001 certification.

Design and specifications are subject to change without notice. Carefully check the specification sheet before using or ordering it.

Ratings

Item	Conditions	Specifications	
Rated power	Power must be derated according to the power derating curve in Figure 1 when ambient temperature exceeds 70°C.		
Rated voltage	The voltage rating is calculated by the following equation. If the value obtained exceeds the limiting element voltage, the voltage rating is equal to the maximum operating voltage. E: Rated voltage (V) $E = \sqrt{P \times R}$ P: Rated power (W) R: Nominal resistance (Ω)	Limiting element voltage 25V	
Nominal resistance	See <u>Table 1</u> .		
Operating temperature		–55°C to +125°C	

Table 1

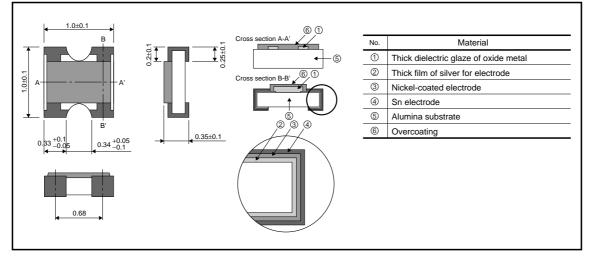
Resistance tolerance	Resistance range (Ω)		Resistance temperature coefficient (ppm / °C)	
J (±5%)	10≤R≤1M	(E24)	±300	

•Before using components in circuits where they will be exposed to transients such as pulse loads (short-duration, high-level loads), be certain to evaluate the component in the mounted state. In addition, the reliability and performance of this component cannot be guaranteed if it is used with a steady state voltage that is greater than its rated voltage.

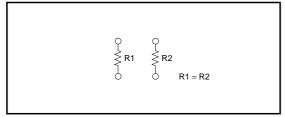
Characteristics

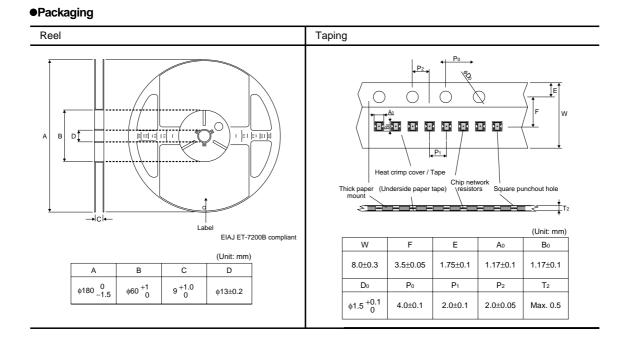
Items	Guaranteed value	Test conditions (JIS C 5201-1)	
	Resistor type		
Resistance	J : ±5%	JIS C 5201-1 4.5	
Variation of resistance with temperature	See <u>Table.1</u>	JIS C 5201-1 4.8 Measurement : -55 / +25 / +125°C	
Overload	± (2.0%+0.1Ω)	JIS C 5201-1 4.13 Rated voltage (current) ×2.5, 2s. Limiting Element Voltage×2 : 50V JIS C 5201-1 4.17 Rosin-Ethanol (25%WT) Soldering condition : 235±5°C Duration of immersion : 2.0±0.5s. JIS C 5201-1 4.18 Soldering condition : 260±5°C Duration of immersion : 10±1s.	
Solderability	A new uniform coating of minimum of 95% of the surface being immersed and no soldering damage.		
Resistance to soldering heat	\pm (1.0%+0.05 Ω) No remarkable abnormality on the appearance.		
Rapid change of temperature	± (1.0%+0.05Ω)	JIS C 5201-1 4.19 Test temp. : -55°C to +125°C 5cyc	
Damp heat, steady state	± (3.0%+0.1Ω)	JIS C 5201-1 4.24 40°C, 93%RH Test time : 1,000h to 1,048h	
Endurance at 70°C	± (3.0%+0.1Ω)	JIS C 5201-1 4.25.1 Rated voltage (current), 70°C 1.5h : ON – 0.5h : OFF Test time : 1,000h to 1,048h	
Endurance	± (3.0%+0.1Ω)	JIS C 5201-1 4.25.3 125°C Test time : 1,000h to 1,048h	
Resistance to solvent	± (1.0%+0.05Ω)	JIS C 5201-1 4.29 23±5°C, Immersion cleaning, 5±0.5min. Solvent : 2-propanol	
Bend strength of the end face plating	\pm (1.0%+0.05 Ω) Without mechanical damage such as breaks.	JIS C 5201-1 4.33	

•External dimensions (Unit: mm)

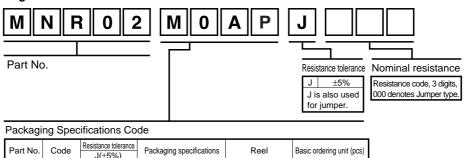


Equivalent circuit





Product designation

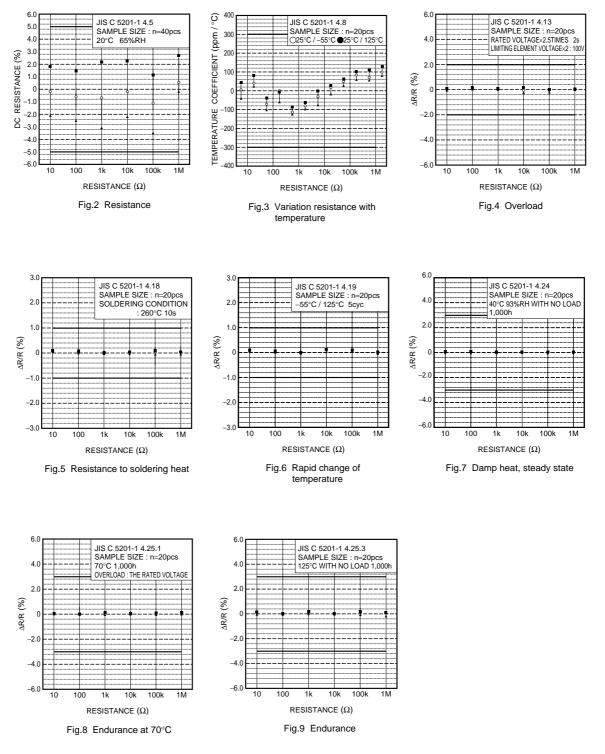


		J(±5%)				
MNR02	M0AP	O	Paper tape (2mmPitch)	φ180mm (7in).	10,000	
Real (#180mm) · Compatible with JEITA standard "EIA LET-7200P"						

Reel (\u03c6180mm) : Compatible with JEITA standard "EIAJ ET-7 ((i) : Standard product

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Electrical characteristics



Notes

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