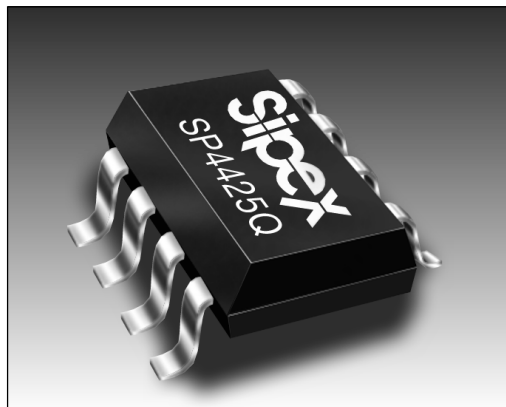


## Electroluminescent Lamp Driver for Low Noise Applications

- Low Noise Waveform
- Tunable Waveshaping
- DC to AC Inverter for EL Backlit Display Panels
- Externally Adjustable Internal Oscillator
- Low Current Standby Mode

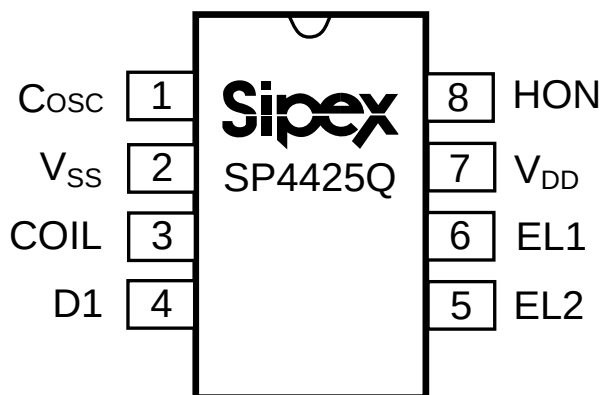
### APPLICATIONS

- Cellular Phones
- Cordless Phones
- Handsets
- Backlit LCD Displays



### DESCRIPTION

The **SP4425Q** is a high voltage output DC-AC converter that can operate from a single 3.0  $V_{DC}$  power supply. The **SP4425Q** is capable of supplying up to 220  $V_{PP}$  signals, making it ideal for driving electroluminescent lamps. The device features 100 nA (typical) standby current for use in low power portable products. One external inductor is required to generate the high voltage charge and one external capacitor is used to select the oscillator and lamp frequencies. The **SP4425Q** is offered in an 8-pin  $\mu$ SOIC package. For delivery in die form, please consult the factory.



**SP4425Q Block Diagram**

## ABSOLUTE MAXIMUM RATINGS

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

|                          |                                   |
|--------------------------|-----------------------------------|
| V <sub>DD</sub> .....    | 5V                                |
| Input Voltages/Currents  |                                   |
| HON (pin1).....          | -0.5V to (V <sub>DD</sub> + 0.5V) |
| COIL (pin3).....         | 100mA                             |
| Lamp Outputs.....        | 230V <sub>pp</sub>                |
| Storage Temperature..... | -65°C to +150°C                   |

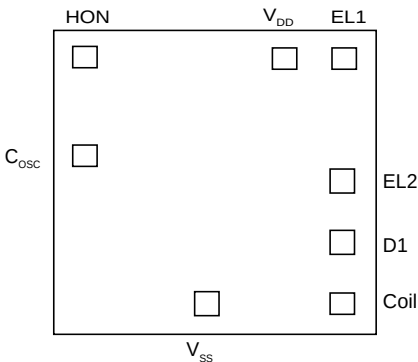
## SPECIFICATIONS

(T= 25°C; V<sub>DD</sub> = 3.0V; see test circuit schematic page 6; Coil = 2mH/44ohms; C<sub>OSC</sub> = 180pF, C<sub>INT</sub> = 820pF unless otherwise noted)

| PARAMETER                                                               | MIN.                           | TYP.                 | MAX.                           | UNITS           | CONDITIONS                                                                                                                                                        |
|-------------------------------------------------------------------------|--------------------------------|----------------------|--------------------------------|-----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Supply Voltage, V <sub>DD</sub>                                         | 2.2                            | 3.0                  | 3.3                            | V               |                                                                                                                                                                   |
| Supply Current, I <sub>COIL</sub> + I <sub>DD</sub>                     |                                | 28                   | 40                             | mA              | V <sub>HON</sub> = V <sub>DD</sub> = 3V                                                                                                                           |
| Coil Voltage, V <sub>COIL</sub>                                         | V <sub>DD</sub>                |                      | 3.3                            | V               |                                                                                                                                                                   |
| HON Input Voltage, V <sub>HON</sub><br>LOW: EL off<br>HIGH: EL on       | -0.25<br>V <sub>DD</sub> -0.25 | 0<br>V <sub>DD</sub> | 0.25V<br>V <sub>DD</sub> +0.25 | V               |                                                                                                                                                                   |
| HON Current, EL on                                                      |                                | 5                    | 20                             | μA              | internal pulldown, V <sub>HON</sub> = V <sub>DD</sub> = 3V                                                                                                        |
| Shutdown Current, I <sub>SD</sub> = I <sub>COIL</sub> + I <sub>DD</sub> |                                | 0.1                  | 1.0                            | μA              | V <sub>HON</sub> = 0V                                                                                                                                             |
| <b>INDUCTOR DRIVE</b>                                                   |                                |                      |                                |                 |                                                                                                                                                                   |
| Coil Frequency, f <sub>COIL</sub> = f <sub>LAMP</sub> x 64              |                                | 28.8                 |                                | kHz             |                                                                                                                                                                   |
| Coil Duty Cycle                                                         |                                | 90                   |                                | %               |                                                                                                                                                                   |
| Peak Coil Current, I <sub>PK-COIL</sub>                                 |                                |                      | 90                             | mA              | Guaranteed by design.                                                                                                                                             |
| <b>EL LAMP OUTPUT</b>                                                   |                                |                      |                                |                 |                                                                                                                                                                   |
| EL Lamp Frequency, f <sub>LAMP</sub>                                    | 300<br>225                     | 450                  | 500<br>775                     | Hz              | T <sub>AMB</sub> = +25°C, V <sub>DD</sub> = 3.0V<br>T <sub>AMB</sub> = -40°C to +85°C, V <sub>DD</sub> = 3.0V                                                     |
| Peak to Peak Output Voltage                                             | 90<br>140<br>90                | 120<br>160           |                                | V <sub>PP</sub> | T <sub>AMB</sub> = +25°C, V <sub>DD</sub> = 2.2V<br>T <sub>AMB</sub> = +25°C, V <sub>DD</sub> = 3.0V<br>T <sub>AMB</sub> = -40°C to +85°C, V <sub>DD</sub> = 3.0V |

This data sheet specifies environmental parameters, final test conditions and limits as well suggested operating conditions. For applications which require performance beyond the specified condition and or limits please consult the factory.

## Bonding Diagram:



## Power Dissipation Per Package

8-pin μSOIC (derate 4.85mW/°C above +70°C).....390mW

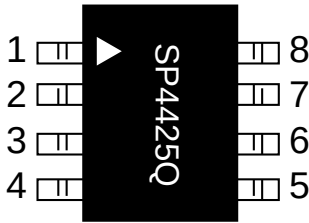
The information furnished herein by Sipex has been carefully reviewed for accuracy and reliability. Its application or use, however, is solely the responsibility of the user. No responsibility for the use of this information is assumed by Sipex, and this information shall not explicitly or implicitly become part of the terms and conditions of any subsequent sales agreement with Sipex. Specifications are subject to change without prior notice. By the sale or transfer of this information, Sipex assumes no responsibility for any infringement of patents or other rights of third parties which may result from its use. No license or other proprietary rights are granted by implication or otherwise under any patent or patent rights of Sipex Corporation.

| PAD              | X      | Y      |
|------------------|--------|--------|
| V <sub>DD</sub>  | 261.0  | 427.0  |
| EL1              | 813.0  | 429.0  |
| EL2              | 813.0  | 28.0   |
| D1               | 813.0  | -172.0 |
| COIL             | 767.0  | -381.0 |
| V <sub>SS</sub>  | 143.5  | -412.0 |
| C <sub>OSC</sub> | -790.0 | -157.5 |
| HON              | -785.5 | 402.0  |

### NOTES:

1. Dimensions are in Microns unless otherwise noted.
2. Bonding pads are 125x125 typ.
3. Outside dimensions are maximum, including scribe area.
4. Die thickness is 380 +/- 25 microns (15 mils +/- 1).
5. Pad center coordinates are relative to die center.
6. Die size 74 x 44 mils.

## PIN DESCRIPTION



Pin 1 –  $C_{OSC}$  - Capacitor input 1, connect Capacitor from  $V_{SS}$  to Pin 1 to set  $C_{OSC}$  frequency.

Pin 2 –  $V_{SS}$  - Power supply common, connect to ground.

Pin 3 – Coil- Coil input, connect coil from  $V_{DD}$  to pin 3.

Pin 4 – D1- Diode Cathode connection.

–  $C_{INT}$  - Integrator capacitor, connect capacitor from pin 4 to ground to minimize coil glitch energy.

Pin 5 – Lamp- Lamp driver output2, connect to EL lamp.

Pin 6 – Lamp- Lamp driver output1, connect to EL lamp.

Pin 7 –  $V_{DD}$  - Power supply for driver, connect to system  $V_{DD}$ .

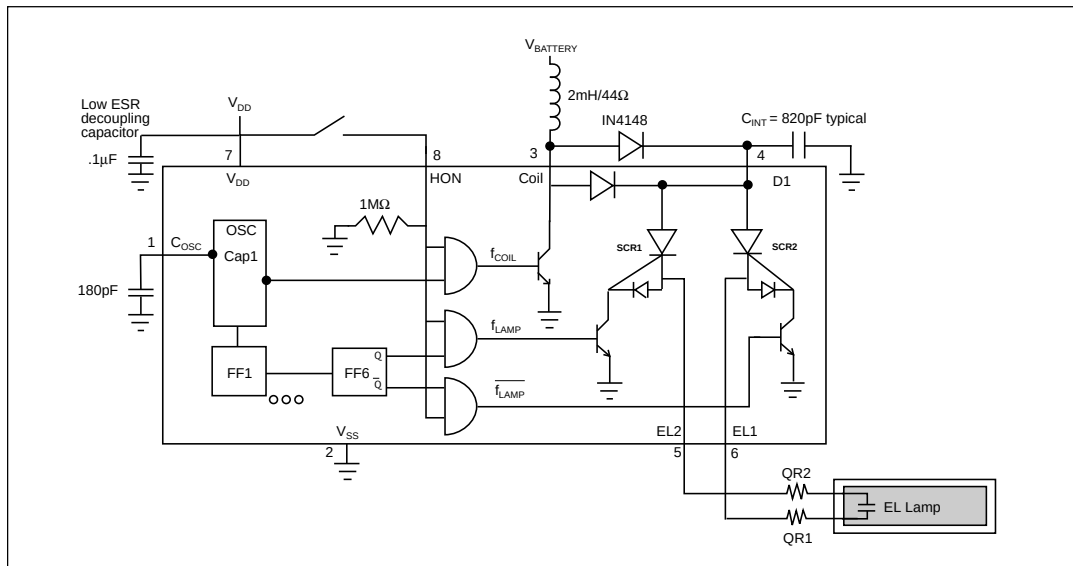
Pin 8 – HON- Enable for driver operation, high = active; low = inactive.

## THEORY OF OPERATION

The **SP4425Q** is made up of three basic circuit elements, an oscillator, coil, and switched H-bridge network. The oscillator provides the device with an on-chip clock source used to control the charge and discharge phases for the coil and lamp. An external capacitor connected between pins 1 and  $V_{SS}$  allows the user to vary the oscillator frequency. For a given choice of coil inductance there will be an optimum  $C_{OSC}$  capacitor value that provides maximum light output.

The suggested oscillator frequency is 28.8kHz ( $C_{OSC}=180\text{pF}$ ). The oscillator output is internally divided to create the control signal for  $f_{LAMP}$ . The oscillator output is internally divided down by 6 flip flops. A 28.8kHz signal will be divided into 6 frequency levels: 14.4kHz, 7.2kHz, 3.6kHz, 1.8kHz, 900kHz, and 450Hz. The oscillator output (28.8kHz) is used to drive the coil (see **figure 2** on **page 6**) and the sixth flip flop output (300Hz) is used to drive the lamp. Although the oscillator frequency can be varied to optimize the lamp output, the ratio of  $f_{COIL}/f_{LAMP}$  will always equal 64.

The coil is an external component connected from  $V_{BATTERY}$  to pin 3 of the **SP4425Q**.  $V_{BATTERY} = 3.0$  VDC with a 2mH/44 $\Omega$  coil are typical conditions. Energy is stored in the coil according to the equation



**SP4425Q Schematic**

$E_L = 1/2LI^2$ , where  $I$  is the peak current flowing in the inductor. The current in the inductor is time dependent and is set by the "ON" time of the coil switch:  $I = (V_L/L)t_{ON}$ , where  $V_L$  is the voltage across the inductor. At the moment the switch closes, the current in the inductor is zero and the entire supply voltage (minus the  $V_{SAT}$  of the switch) is across the inductor. The current in the inductor will then ramp up at a linear rate. As the current in the inductor builds up, the voltage across the inductor will decrease due to the resistance of the coil and the "ON" resistance of the switch:  $V_L = V_{BATTERY} - IR_L - V_{sat}$ . Since the voltage across the inductor is decreasing, the current ramp rate also decreases which reduces the current in the coil at the end of  $t_{ON}$ , the energy stored in the inductor per coil cycle and therefore, the light output. The other important issue is that maximum current (saturation current) in the coil is set by the design and manufacturer of the coil. If the parameters of the application such as  $V_{BATTERY}$ ,  $L$ ,  $R_L$  or  $t_{ON}$  cause the current in the coil to increase beyond its rated  $I_{SAT}$ , excessive heat will be generated and the power efficiency will decrease with no additional light output.

The majority of the current goes through the coil and typically less than 2mA is required for  $V_{DD}$  of the **SP4425Q**.  $V_{DD}$  can range from 2.2V to 3.3V; it is not necessary that  $V_{DD} = V_{BATTERY}$ . Coils are also a function of the core material and winding used. Performance variances may be noticeable from different coil suppliers. The Sipex **SP4425Q** is final tested at 3.0V using a 2mH/44Ω coil from Matsushita. For suggested coil sources see [page 10](#).

The  $f_{COIL}$  signal controls a switch that connects the end of the coil at pin 3 to ground or to open circuit. The  $f_{COIL}$  signal is a 90% duty cycle signal switching at the oscillator frequency. During the time when the  $f_{COIL}$  signal is high, the coil is connected from  $V_{BATTERY}$  to ground and a charged magnetic field is created in the coil. During the low part of  $f_{COIL}$ , the ground connection is switched open, the field collapses and the energy in the inductor is forced to flow toward the lamp.  $f_{COIL}$  will send 32 of these charge pulses (see [figure 2 on page 6](#)) lamp, each pulse increases the voltage drop across the lamp in discrete steps. As the voltage potential approaches its maximum, the steps become smaller (see [figure 1 on page 6](#)).

The H-bridge consists of two SCR structures that act as high voltage switches. These two switches control the polarity of how the lamp is charged. The SCR switches are controlled by the  $f_{LAMP}$  signal which is the oscillator frequency divided by 64. For a 28.8kHz oscillator,  $f_{LAMP} = 450\text{Hz}$ .

When the energy from the coil is released, a high voltage spike is created triggering the SCR switches. The direction of current flow is determined by which SCR is enabled. One full cycle of the H-bridge will create a voltage step from ground to 80V (typical) on pins 5 and 6 which are 180 degrees out of phase with each other (see [figure 3 on page 6](#)). A differential view of the outputs is shown in [figure 4 on page 6](#).

## Layout Considerations

The **SP4425Q** circuit board layout must observe careful analog precautions. For applications with noisy power supply voltages, a 0.1μF low ESR decoupling capacitor must be connected from  $V_{DD}$  to ground. Any high voltage traces should be isolated from any digital clock traces or enable lines. A solid ground plane connection is strongly recommended. All traces to the coil or to the high voltage outputs should be kept as short as possible to minimize capacitive coupling to digital clock lines and to reduce EMI emissions.

## Integrator Capacitor

An integrating capacitor must be placed from pin 4 (D1) to ground in order to minimize glitches associated with switching the coil. A capacitor at this point will collect the high voltage spikes and will maximize the peak to peak voltage output. High resistance EL lamps will produce more pronounced spiking on the EL output waveform; adding the  $C_{INT}$  capacitor will minimize the peaking and increase the voltage output at each coil step. The value of the integrator capacitor is application specific. Typical values can range from 500pF to 0.1μF. No integrator capacitor or very small values (500pF) will have a minor effect on the output, whereas a 0.1μF capacitor will cause the output to charge more rapidly creating a square wave output. For most 3V applications an 820 pF integrator capacitor is suitable.

## Waveshaping

The **SP4425Q** allows the user to "tune" the output waveform for specific application requirements. External resistors, QR1 and QR2 (see SP4425QCU schematic **page 3**) can be adjusted to remove any sharp, high frequency edges present on the EL output waveform. Typical values range from 5k $\Omega$  to 20k $\Omega$ . The waveforms on **page 9** show the effect that the Q resistors have on the output. As the sharp discharge edge is filtered, the available noise from the vibration of the lamp is reduced. The user must balance the noise performance with the light output performance to achieve the desired results.

## Electroluminescent Technology

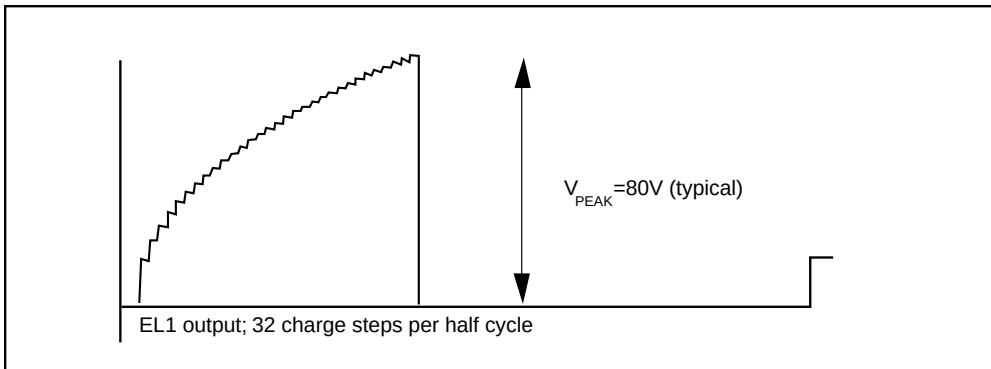
### What is electroluminescence?

An EL lamp is basically a strip of plastic that is coated with a phosphorous material which emits light (fluoresces) when a high voltage (>40V) which was first applied across it, is removed or reversed. Long periods of DC voltages applied to the material tend to breakdown the material and reduce its lifetime. With these considerations in mind, the ideal signal to drive an EL lamp is a high voltage sine wave. Traditional approaches to achieving this type of waveform included discrete circuits incorporating a transformer, transistors, and several resistors and capacitors. This approach is large and bulky, and cannot be implemented in most hand held equipment. **Sipex** now offers low power single chip driver circuits specifically designed to drive small to medium sized electroluminescent panels.

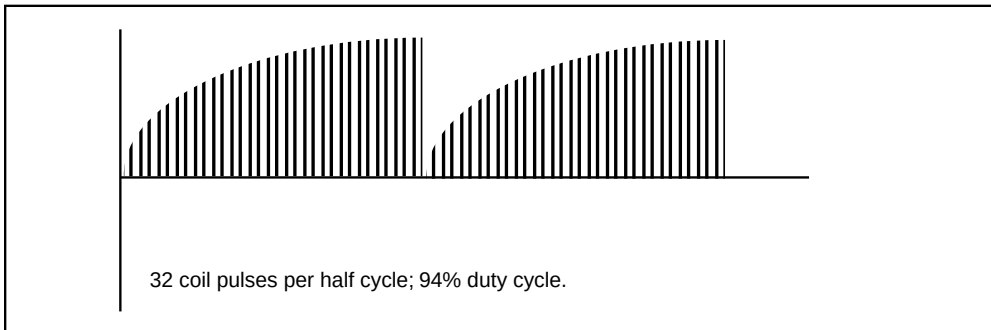
Electroluminescent backlighting is ideal when used with LCD displays, keypads, or other backlit readouts. Its main use is to illuminate displays in dim to dark conditions for momentary periods of time. EL lamps typically consume less power than LEDs or bulbs making them ideal for battery powered products. Also, EL lamps are able to evenly light an area without creating "hot spots" in the display.

The amount of light emitted is a function of the voltage applied to the lamp, the frequency at which it is applied, the lamp material used and its size, and lastly, the inductor used. Both voltage and frequency are directly related to light output. In other words, as the voltage or the frequency of the EL output is increased, the light output will also increase. The voltage has a much larger impact on light output than the frequency does. For example, an output signal of 168V<sub>pp</sub> with a frequency of 500Hz can yield 15Cd/m<sup>2</sup>. In the same application a different EL driver could produce 170V<sub>pp</sub> with a frequency of 450Hz and can also yield 15Cd/m<sup>2</sup>. Variations in peak-to-peak voltage and variations in lamp frequency are to be expected, light output will also vary from device-to-device however typical light output variations are usually not visually noticeable.

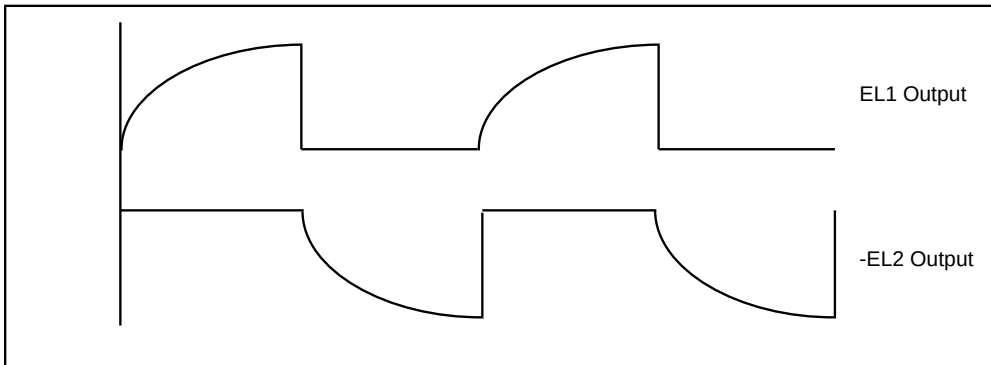
There are many variables which can be optimized for specific applications. **Sipex** supplies characterization charts to aid the designer in selecting the optimum circuit configuration (see **page 7** and **8**).



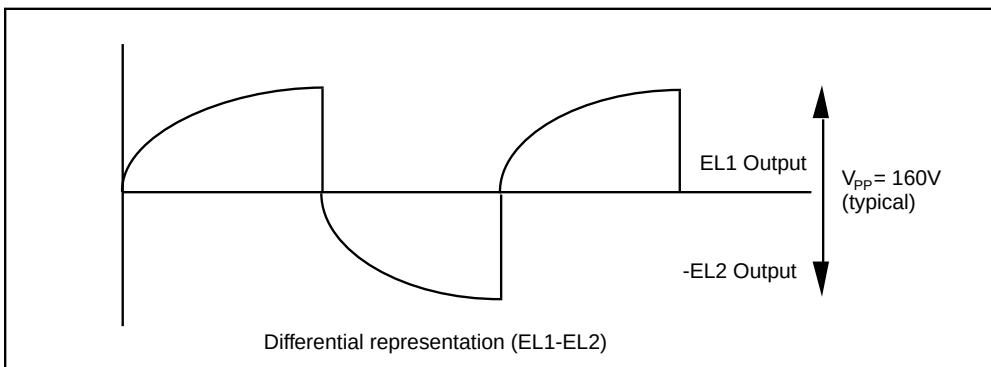
**Figure 1. EL1 Output without QR1 and QR2**



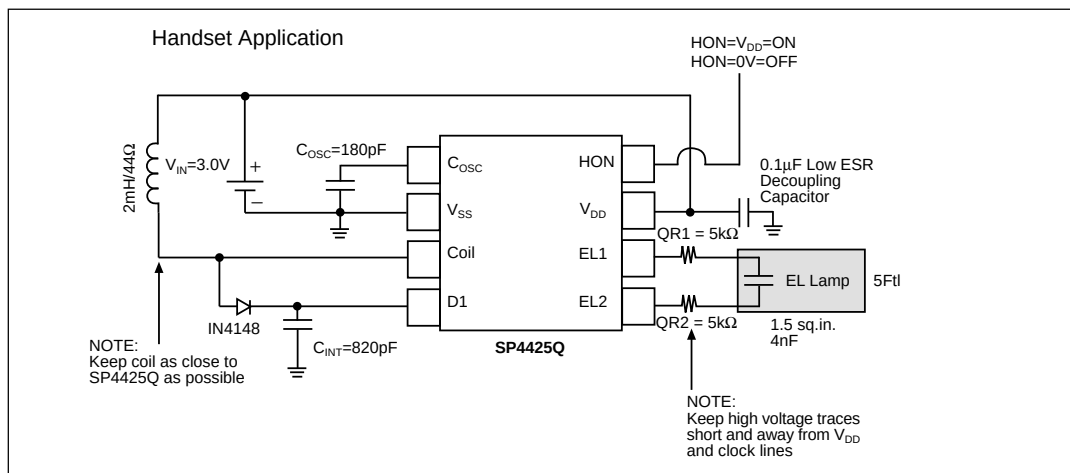
**Figure 2. Voltage pulses released from the coil to the EL driver circuitry**



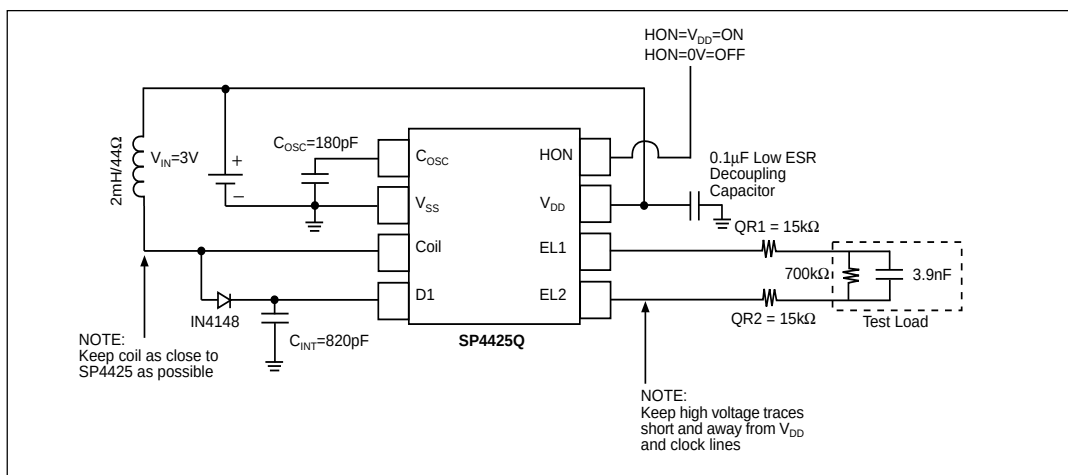
**Figure 3. EL1, EL2 Output without QR1 and QR2**



**Figure 4. Differential Representation of (EL1 - EL2) without QR1 and QR2**

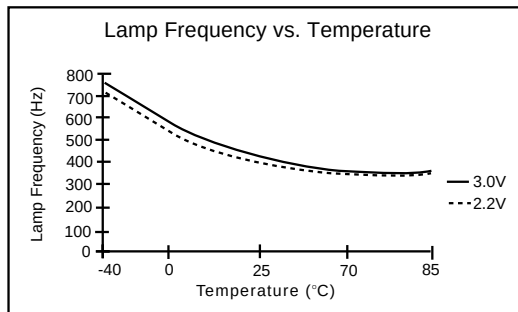


**Figure 5. Typical SP4425Q Application Circuit**

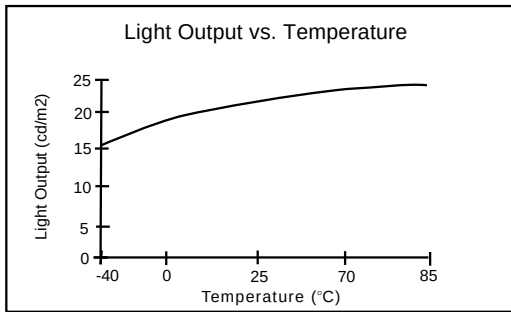


**Figure 6. SP4425Q 3V Test Circuit**

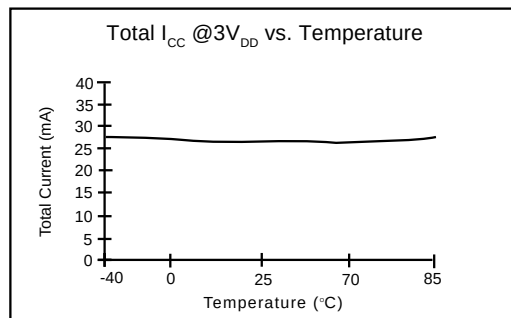
The following performance curves are intended to give the designer a relative scale from which to optimize specific applications. Absolute measurements may vary depending upon the brand of components chosen.



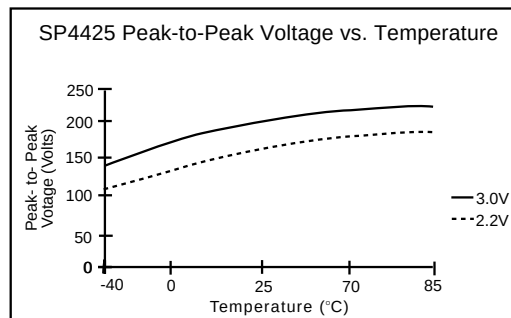
**Figure 7.** Coil=2mH/44Ω;  $C_{OSC}=180\text{pF}$ ;  
 $C_{INT}=470\text{pF}$ ;  $C_{LOAD}=4\text{nF}$



**Figure 8.** Coil=2mH/44Ω;  $C_{OSC}=180\text{pF}$ ;  
 $C_{INT}=470\text{pF}$ ;  $V_{DD}=3.0\text{V}$ ; Load=3 sq.in.



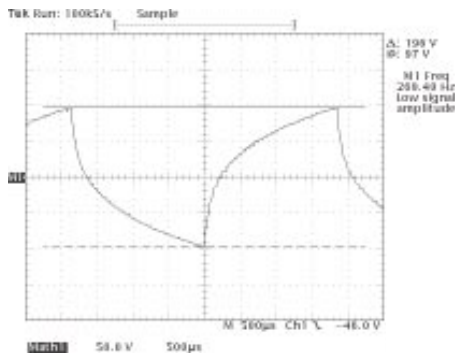
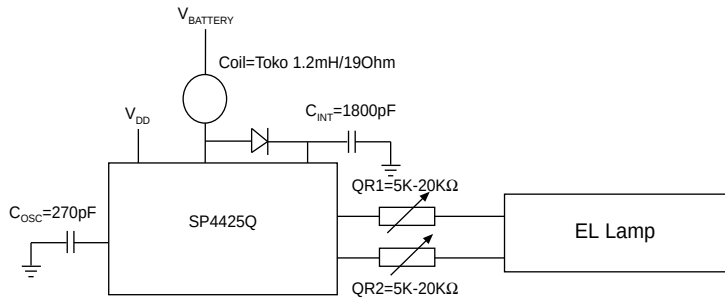
**Figure 9.** Coil=2mH/44Ω;  $C_{OSC}=180\text{pF}$ ;  
 $C_{INT}=470\text{pF}$ ;  $C_{LOAD}=4\text{nF}$



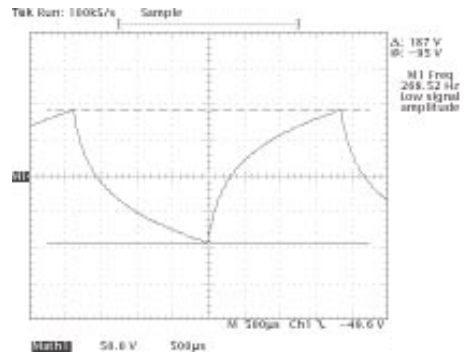
**Figure 10.** Coil=2mH/44Ω;  $C_{OSC}=180\text{pF}$ ;  
 $C_{INT}=470\text{pF}$ ;  $C_{LOAD}=4\text{nF}$



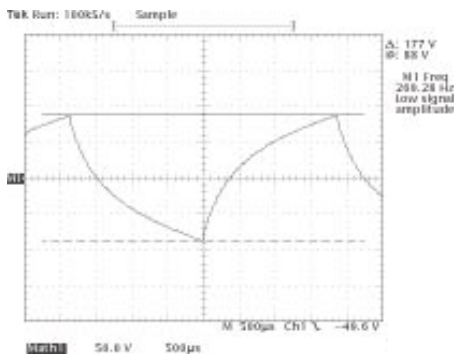
The following scope photos show the affect the tuning resistors (QR1 and QR2) have on the output waveform. Figure 11 implements only 5K $\Omega$  of series resistance introducing only a slight amount of filtering of the discharge edge. Figure 12 shows that if the values are increased to 10K $\Omega$  the discharge edge is reduced even further. A 20K $\Omega$  example is shown in Figure 14 and represents the most amount of filtering needed. Again, the balance in light output and audible noise must be observed for each application.



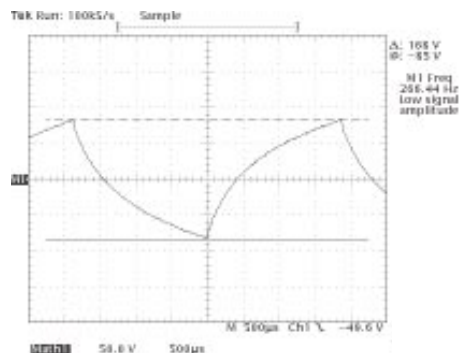
**Figure 11.**  
QR1=QR2=5K $\Omega$   
 $V_{PP}=196V_{PK-PK}$ ,  $F_{LAMP}=269Hz$   
Low noise suppression level



**Figure 12.**  
QR1=QR2=10K $\Omega$   
 $V_{PP}=187V_{PK-PK}$ ,  $F_{LAMP}=268Hz$   
Low noise suppression level



**Figure 13.**  
QR1=QR2=15K $\Omega$   
 $V_{PP}=177V_{PK-PK}$ ,  $F_{LAMP}=269Hz$   
High noise suppression level

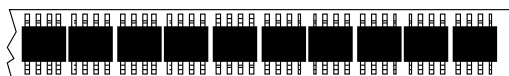
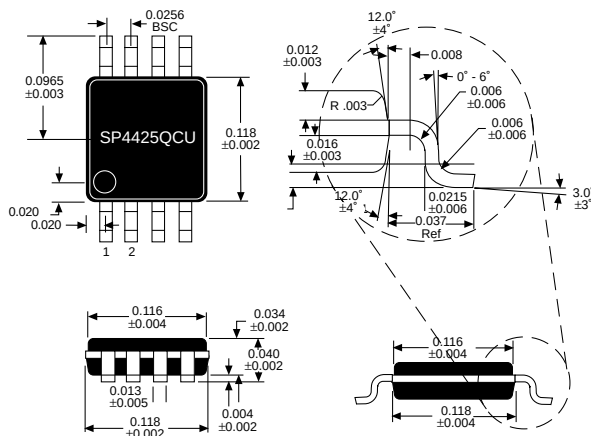


**Figure 14.**  
QR1=QR2=20K $\Omega$   
 $V_{PP}=168V_{PK-PK}$ ,  $F_{LAMP}=266Hz$   
High noise suppression level

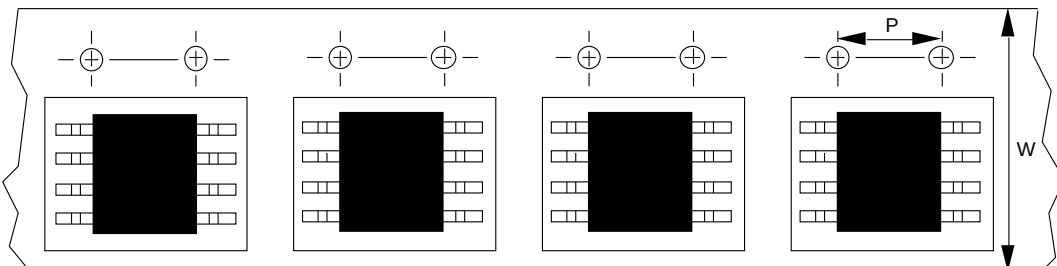


All package dimensions in inches

# 8-pin $\mu$ SOIC



50 SP4425QCU per tube



| $\mu$ SOIC-8 13" reels: P=8mm, W=12mm |                      |                       |                      |
|---------------------------------------|----------------------|-----------------------|----------------------|
| Pkg.                                  | Minimum qty per reel | Standard qty per reel | Maximum qty per reel |
| CU                                    | 500                  | 2500                  | 3000                 |

## ORDERING INFORMATION

| Model                 | Operating Temperature Range | Package Type                |
|-----------------------|-----------------------------|-----------------------------|
| SP4425QCU . . . . .   | -40°C to +85°C . . . . .    | 8-Pin $\mu$ SOIC            |
| SP4425QCUEB . . . . . | N/A . . . . .               | $\mu$ SOIC Evaluation Board |

Please consult the factory for pricing and availability on a Tape-On-Reel option.



### SIGNAL PROCESSING EXCELLENCE

#### Sipex Corporation

##### Headquarters and Sales Office

22 Linnell Circle  
Billerica, MA 01821  
TEL: (978) 667-8700  
FAX: (978) 670-9001  
e-mail: sales@sipex.com

##### Sales Office

233 South Hillview Drive  
Milpitas, CA 95035  
TEL: (978) 934-7500  
FAX: (978) 935-7600

#### European Sales Offices:

##### ENGLAND:

Sipex Corporation  
2 Linden House  
Turk Street  
Alton Hampshire GU34 IAN  
England  
TEL: 44-1420-549527  
FAX: 44-1420-542700  
e-mail: mikeb@sipex.co.uk

##### GERMANY:

Sipex GmbH  
Gautinger Strasse 10  
82319 Starnberg  
TEL: 49.81.51.89810  
FAX: 49.81.51.29598  
e-mail: sipex-starnberg@t-online.de

#### Far East:

##### JAPAN:

Nippon Sipex Corporation  
Yahagi No. 2 Building  
3-5-3 Uchikanda, Chiyoda-ku  
Tokyo 101  
TEL: 81.3.3256.0577  
FAX: 81.3.3256.0621

Sipex Corporation reserves the right to make changes to any products described herein. Sipex does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights nor the rights of others.