TOSHIBA Photocoupler GaAlAs Ired & Photo-Triac

TLP668J(S)

Office Machine Household Use Equipment Triac Driver Solid State Relay

The TOSHIBA TLP668J(S) consists of a zero voltage crossing turn-on photo-triac optically coupled to a GaAlAs infrared emitting diode in a six lead plastic DIP package.

- Peak off-state voltage: 600V (Min.)
- Trigger LED current: 3mA (Max.)
- On-state current: 100mA (Max.)
- Isolation voltage: 5000Vrms (Min.)
- UL recognized: UL1577, file No. E67349
- BSI approved: BS EN60065:2002, file No. 8385
- BS EN60950-1:2002, file No. 8386 SEMCO approved: EN60065, EN60950-1, EN60335-1

Certificate no.708960

Option(D4) type

VDE approved: DIN EN 60747-5-2

Certificate No. 40009302 Maximum operating insulation voltage : 890Vpk

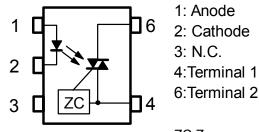
Highest permissible over voltage : 8000 Vpk

(Note) When an EN60747-5-2 approved type is needed, please designate the "Option(D4)".

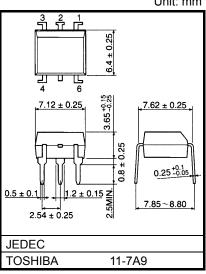
Construction mechanical rating

	7.62 mm pich standard type	10.16 mm pich TLPXXXF type
Creepage distance	7.0 mm (Min.)	8.0 mm (Min.)
Clearance	7.0 mm (Min.)	8.0 mm (Min.)
Insulation thickness	0.5 mm (Min.)	0.5 mm (Min.)

Pin configuration (top view)



ZC:Zero-cross circuit



Weight: 0.39 g (Typ.)

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Unit: mm

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit		
	Forward current	١ _F	30	mA		
	Forward current derating (Ta≥25°C)	∆l _F /°C	-0.3	mA /°C		
LED	Peak forward current (100µs pulse, 100pps)	I _{FP}	1	А		
	Reverse voltage	V _R	5	V		
	Junction temperature	Tj	125	°C		
	Off-state output terminal voltage	V _{DRM}	600	V		
	On-state RMS current	Ta=25°C	I _{T(RMS)}	100	mA	
ъ		Ta=70°C	·1(RIVIS)	50		
Detector	On-state current derating (Ta≥25°C)	∆l _T /°C	-1.1	mA /°C		
Δ	Peak on-state current (100µs pulse, 120pps)	I _{TP}	2	А		
	Peak nonrepetitive surge current (Pw=10ms,DC=10%	I _{TSM}	1.2	А		
	Junction temperature	Tj	110	°C		
Ope	erating temperature range	T _{opr}	-40~100	°C		
Stor	Storage temperature range			-55~125	°C	
Lea	d soldering temperature (10s)	T _{sol} 260		°C		
Isola	ation voltage (AC,1min. , R.H. ≤60%)	BV _S 5000		Vrms		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2) Device considered a two terminal device: Pins1,2 and 3 shorted together and pin4 and pin6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{AC}	—	_	240	Vac
Forward current	١ _F	4.5	6	7.5	mA
Peak on-state current	I _{TP}	_		1	А
Operating temperature	T _{opr}	-10	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

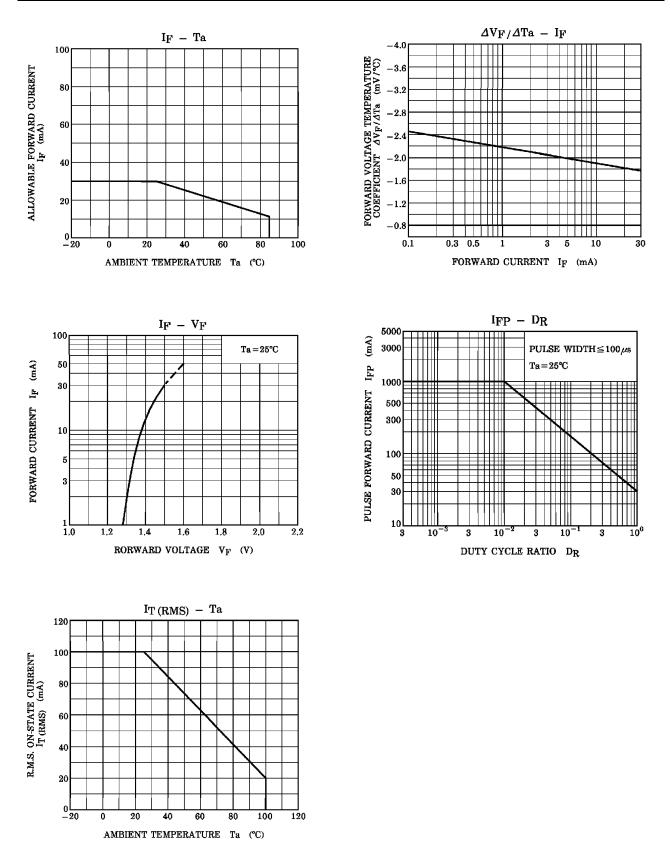
Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	VF	I _F = 10 mA	1.2	1.4	1.7	V
LED	Reverse current	I _R	V _R = 3 V	_	_	10	μA
	Capacitance	CT	V = 0, f=1MHz	_	30	_	pF
	Peak off-state current	I _{DRM}	V _{DRM} =600V	_	10	1000	nA
<u>ـ</u>	Peak on-state voltage	V _{TM}	I _{TM} =100mA	_	_	3.0	V
Detector	Holding current	Ι _Η	—	_	0.6	_	mA
Det	Critical rate of rise of off-state voltage	dv/dt	Vin=240Vrms , Ta=85°C	200	500	_	V/µs
	Critical rate of rise of commutating voltage	dv/dt(c)	Vin=60Vrms , IT=15mA	_	0.2	_	V/µs

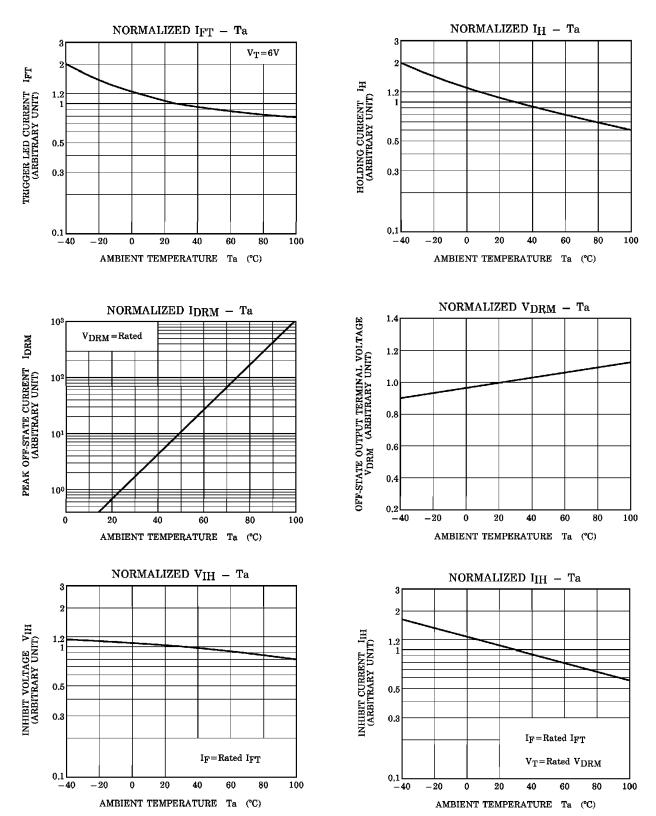
Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	V _T =6V ,Resistive load	—	_	3	mA
Inhibit voltage	VIH	I _F =Rated I _{FT}	—	—	50	V
Leakage in inhibited state	IIH	I_F =Rated I_{FT} , V_T =rated V_{DRM}	—	200	600	μA
Capacitance (input to output)	CS	V _S =0 , f=1MHz	—	0.8	—	pF
Isolation resistance	R _S	V _S =500V	1×10 ¹²	10 ¹⁴	—	Ω
	BVs	AC, 1minute	5000	_	_	Vrms
Isolation voltage		AC, 1second,in oil	—	10000	—	
		DC, 1minute,in oil	_	10000	_	Vdc

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