

TOSHIBA Photocoupler Photorelay

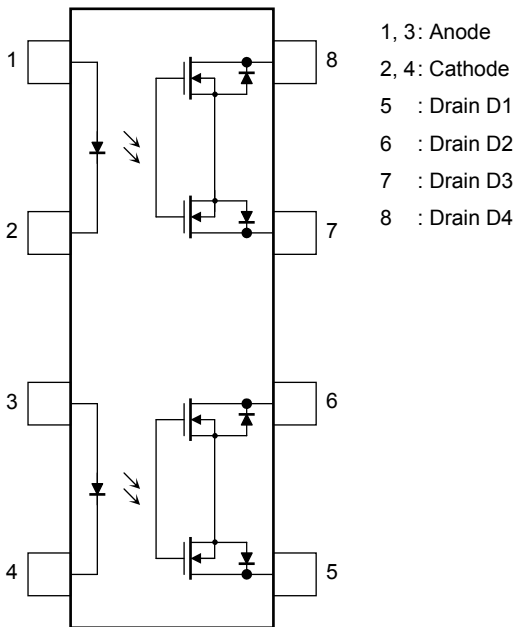
TLP4202G

Telecommunication
Measurement Equipment
Security Equipment
FA

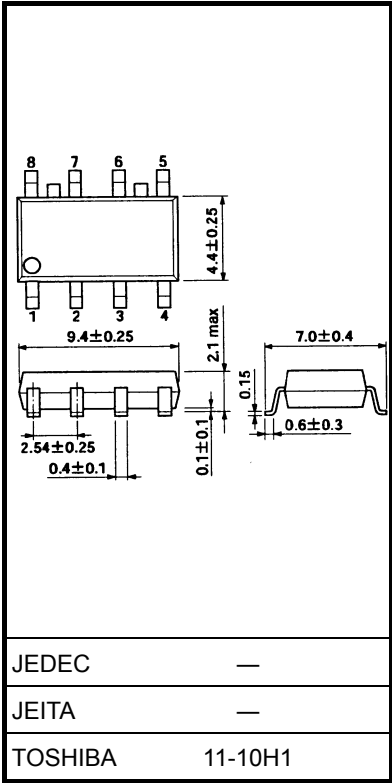
The Toshiba TLP4202G consists of an aluminum gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in a SOP package. This 2-form-B (NC) photorelay features a withstanding voltage of 350 V.

- 8-pin SOP (2.54SOP8): Height = 2.1 mm, pitch = 2.54 mm
- Normally closed (2-form-B) device
- Peak off-state voltage: 350 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 90 mA (max)
- On-state resistance: 50 Ω (max)
- Isolation voltage: 1500 Vrms (min)
- UL Recognized: UL1577, File No. E67349

Pin Configuration (top view)



Unit: mm



Weight: 0.2 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
LED	Forward current	I _F	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	−0.5	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	I _{FP}	1	A
	Reverse voltage	V _R	5	V
	Junction temperature	T _j	125	°C
Detector	Off-state output terminal voltage	V _{OFF}	350	V
	On-state current	I _{ON}	90	mA
	On-state current derating (Ta ≥ 25°C)	ΔI _{ON} /°C	−0.9	mA/°C
	Junction temperature	T _j	125	°C
Storage temperature range		T _{stg}	−55 to 125	°C
Operating temperature range		T _{opr}	−40 to 85	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isolation voltage (AC, 1 min, R.H. ≤ 60%) (Note 1)		BV _S	1500	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pins 1, 2, 3 and 4 are shorted together, and pins 5, 6, 7 and 8 are shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	V _{DD}	—	—	280	V
Forward current	I _F	5	—	25	mA
On-state current	I _{ON}	—	—	90	mA
Operating temperature	T _{opr}	−20	—	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V = 0, f = 1 MHz	—	30	—	pF
Detector	Off-state current	I _{OFF}	V _{OFF} = 350 V, I _F = 5 mA	—	—	1	μA
	Capacitance	C _{OFF}	V = 0, f = 1 MHz, I _F = 5 mA	—	30	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	I_{FC}	$I_{OFF} = 10 \mu A$	—	1	3	mA
Return LED current	I_{FT}	$I_{ON} = 90 \text{ mA}$	0.1	—	—	mA
On-state resistance	R_{ON}	$I_{ON} = 90 \text{ mA}$	—	27	50	Ω

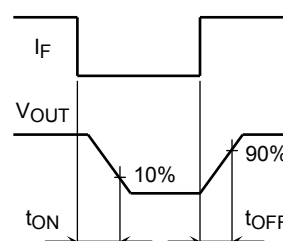
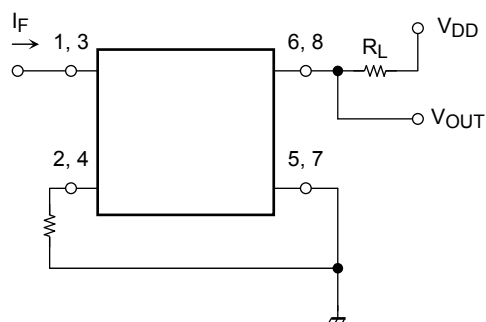
Isolation Characteristics (Ta = 25°C)

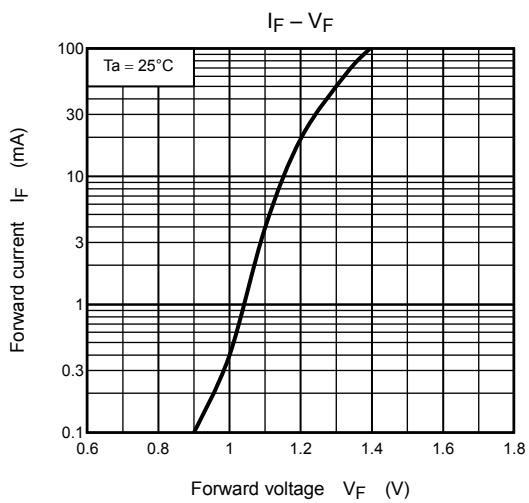
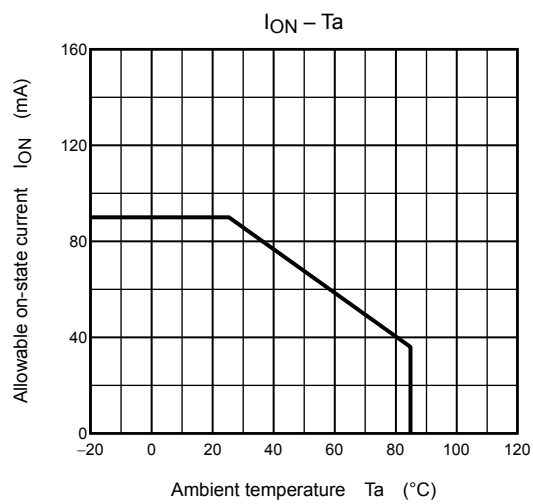
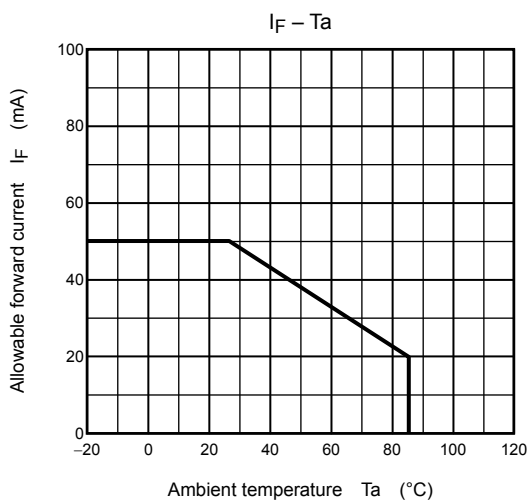
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	C_S	$V_S = 0, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance	R_S	$V_S = 500 \text{ V}, \text{R.H.} \leq 60\%$	5×10^{10}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 1 min	1500	—	—	Vrms
		AC, 1 s, in oil	—	3000	—	
		DC, 1 min, in oil	—	3000	—	Vdc

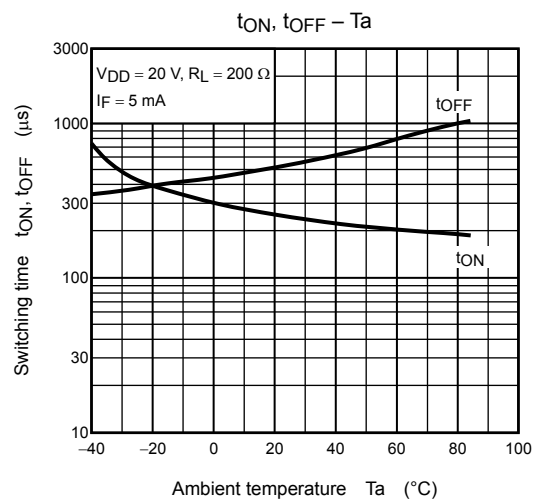
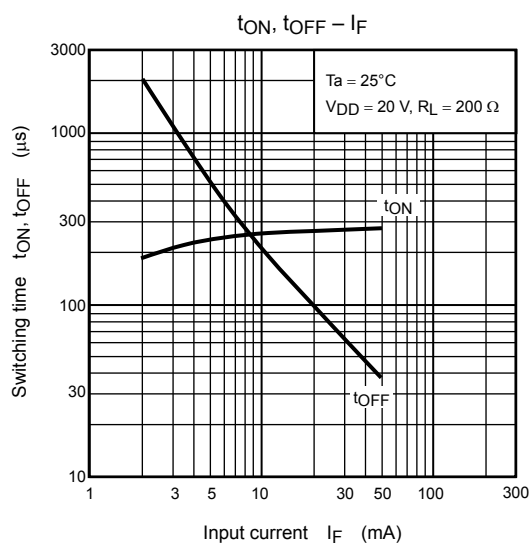
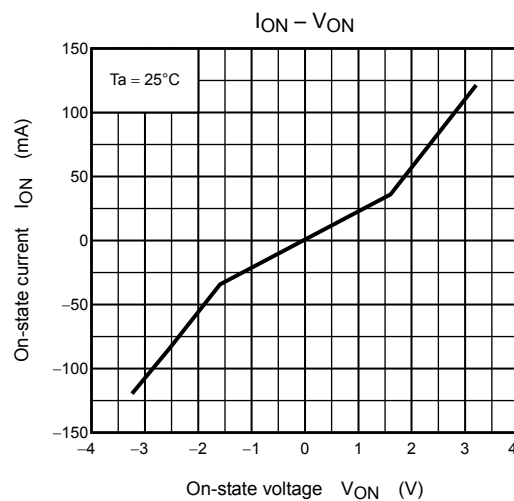
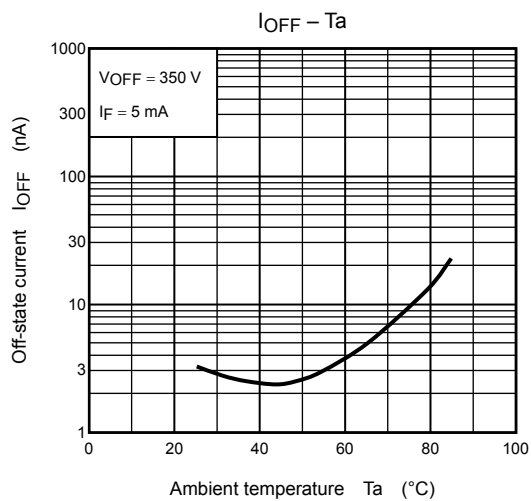
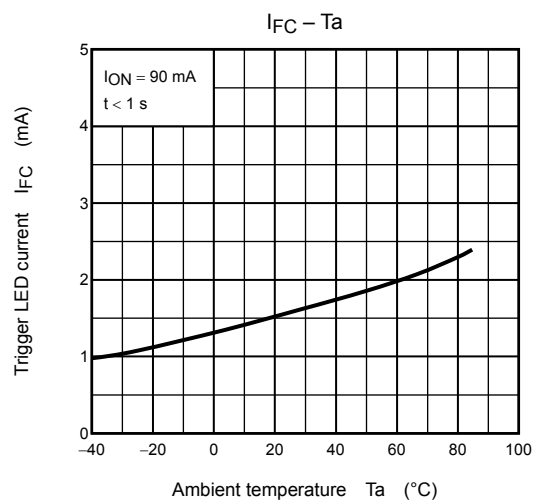
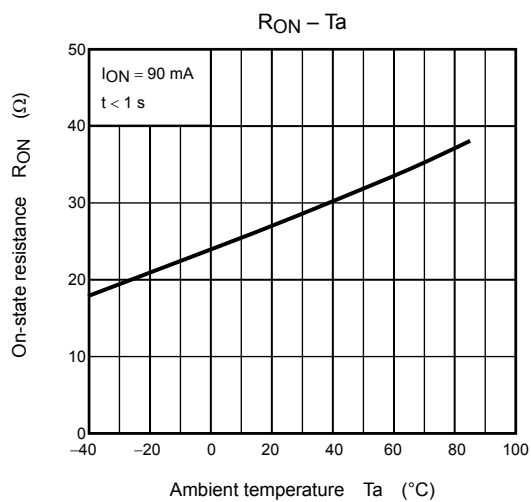
Switching Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Turn-on time	t_{ON}	$R_L = 200 \Omega$ $V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note 2)	—	0.25	0.5	ms
Turn-off time	t_{OFF}		—	0.5	1	ms

Note 2: Switching time test circuit







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