# <u>TOSHIBA</u>

### TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA76432AFT,TA76432AF,TA76432AFR,TA76432AS

### 1.26-V Adjustable High-Precision Shunt Regulators

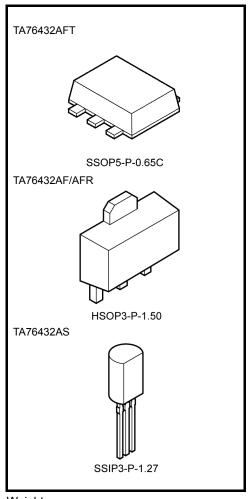
The TA76432 series consists of adjustable high-precision shunt regulators whose output voltage ( $V_{\rm KA}$ ) can be set arbitrarily using two external resistors.

These devices have a precise internal reference voltage of 1.26 V, enabling them to operate at low voltage.

The devices are ideal for use as error amplifiers in 3-V switching-regulator systems. In addition, they can be used as zener diodes to perform temperature compensation.

### Features

- Precision reference voltage:  $V_{REF} = 1.26 \text{ V} \pm 1\%$  (Ta = 25°C)
- Small temperature coefficient:  $|\alpha V_{REF}| = 30 \text{ ppm/}^{\circ}C$  (typ.)
- Adjustable output voltage:  $V_{REF} \le V_{OUT} \le 19 \text{ V}$
- Minimum cathode current for regulation: I<sub>kmin</sub> = 0.5 mA (max.)
- Operating temperature: Ta = -40~85°C
- Packages: UFV (TA76432AFT), PW-MINI (TA76432AF/AFR) and TO-92MOD (TA76432AS)
- The TA76432AFT is housed in an ultra-thin UFV package. (thickness: 0.7 mm typ.)

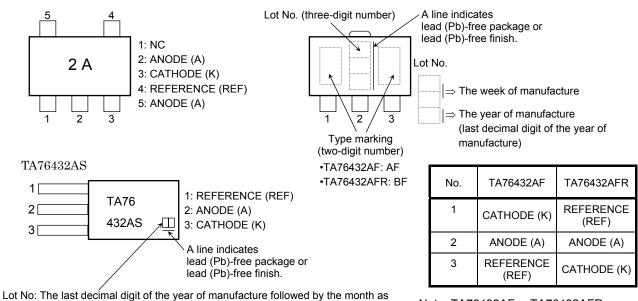


Weight SSOP5-P-0.65C : 0.007 g (typ.) HSOP3-P-1.50 : 0.05 g (typ.) SSIP3-P-1.27 : 0.36 g (typ.)

### Pin Assignment/Marking

### TA76432AFT

#### TA76432AF/AFR



Lot NO: The last decimal digit of the year of manufacture followed by the mon letters A to L of the alphabet.

For example: Jan-2004 is coded as "4A"

Note: TA76432AFvs.TA76432AFR: reverse pin connection.

### How to Order

Product No.	Package Type	Packing Type and Capacity	Minimum Order	
TA76432AFT (TE85L)	UFV (surface-mount type)	Embossed tape: 3000/tape	1 tape	
TA76432AF/AFR	PW-MINI (SOT-89)	On cut tape (TE12L): 100/tape section	100	
TA76432AF/AFR (TE12L)	(surface-mount type)	Embossed tape: 1000/tape	1 tape	
TA76432AS	TO-92MOD	Loose in bag: 200/bag	1 bag	
TA76432AS (TPE6)	(lead type)	Radial tape: 2000/tape	1 tape	

Note: The lead pitch for the TA76432AS and TA76432AS (TPE6) may vary.

Anode (A)

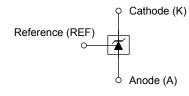
### **Functional Block Diagram**

Reference (REF)

1.26 V

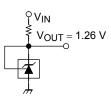


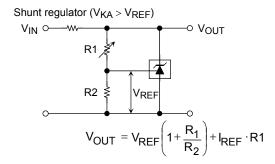




## **Typical Application Circuits**

1.26 V Reference ( $V_{KA} = V_{REF}$ )





### **Precautions during Use**

- TA76432AFT, TA76432AF/AFR, TA76432AS These products contain MOS elements. Please take care to avoid generating static electricity when handling these devices.
- (2) TA76432AFT, TA76432AF/AFR, TA76432AS

The oscillation frequency of these devices is determined by the value of the capacitor connected between the anode and the cathode.

When establishing maximum operating condition parameters, please derate the maximum rating values specified in these datasheets so as to allow an operational safety margin. Use of a laminated ceramic capacitor is recommended.

(3) Precautions when handling anode pin of TA76432AFT Pin 2 and pin 5 should normally be shorted together. If only pin 5 is used, pin 2 should either be left open or always kept at a lower potential than pin 5. Do not leave pin 5 open and use pin 2 only.

Characteristics		Symbol	Rating	Unit	
Cathode voltage		V <sub>KA</sub>	20	V	
Cathode current		١ĸ	20	mA	
Cathode-anode reverse current		-I <sub>K</sub>	10	mA	
Reference voltage		V <sub>REF</sub>	7	V	
Reference current		I <sub>REF</sub>	50	μA	
Reference-anode reverse current		-I <sub>REF</sub>	10	mA	
Power dissipation	TA76432AFT	PD	0.45 (Note 1)		
	TA76432AF/AFR		0.5	W	
	TA76432AS		0.8		
Thermal resistance	TA76432AFT		277 (Note 1)	°C/W	
	TA76432AF/AFR	R <sub>th</sub>	250		
	TA76432AS		156		
Operating temperature		T <sub>opr</sub>	-40~85	°C	
Junction temperature		Тj	150	°C	
Storage temperature		T <sub>stg</sub>	-55~150	°C	

## Absolute Maximum Ratings (Ta = 25°C)

Note 1: Glass epoxy board mounting:  $30 \text{ mm} \times 30 \text{ mm} \times 0.8 \text{ mmt}$  (Cu pad area  $35 \text{ mm}^2$ )

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

**Recommended Operating Conditions** 

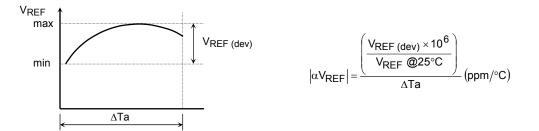
Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	V <sub>KA</sub>	V <sub>REF</sub>	_	19	V
Cathode current	١ <sub>K</sub>	0.5		15	mA
Operating temperature	T <sub>opr</sub>	-40		85	°C

# Electrical Characteristics (Unless otherwise specified, Ta = $25^{\circ}$ C, I<sub>K</sub> = 5 mA)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	V <sub>REF</sub>	V <sub>KA</sub> = V <sub>REF</sub>	1.247	1.26	1.273	V
Deviation of reference input voltage over temperature	V <sub>REF (dev)</sub>	$0^{\circ}C \leq Ta \leq 85^{\circ}C, V_{KA} = V_{REF}$	_	3	15	mV
Ratio of change in reference input voltage to the change in cathode voltage	$\Delta V_{REF} / \Delta V$	$V_{REF} \leq V_{KA} \leq 5 V$	_	0.5	2.5	mV/V
		$5 \text{ V} \leq \text{V}_{\text{KA}} \leq 19 \text{ V}$	_	0.3	2.0	
Reference input current	I <sub>REF</sub>	V <sub>KA</sub> = V <sub>REF</sub>		2	4	μA
Deviation of reference input current over temperature	IREF (dev)	$\begin{array}{l} 0^{\circ}C \leq Ta \leq 85^{\circ}C, \ V_{KA} = V_{REF}, \\ R_{1} = 10 \ k\Omega, \ R_{2} = \infty \end{array}$	_	0.3	1.2	μA
Minimum cathode current for regulation	I <sub>Kmin</sub>	V <sub>KA</sub> = V <sub>REF</sub>	_	0.2	0.5	mA
Off-state cathode current	I <sub>Koff</sub>	V <sub>KA</sub> = 19 V, V <sub>REF</sub> = 0 V			1.0	μA
Dynamic impedance	Z <sub>KA</sub>	$V_{KA} = V_{REF}, f \le 1 \text{ kHz},$ 0.5 mA $\le I_K \le 15 \text{ mA}$	_	0.2	0.5	Ω

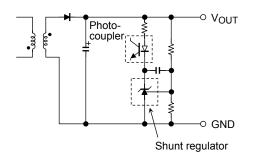
The deviation parameters  $V_{REF (dev)}$  and  $I_{REF (dev)}$  are defined as the maximum variation of the  $V_{REF}$  and  $I_{REF}$  over the rated temperature range.

The average temperature coefficient of the  $\ensuremath{V_{\mathrm{REF}}}$  is defined as:

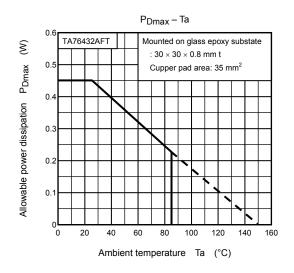


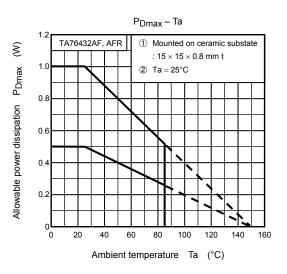
## **Application Circuit Example**

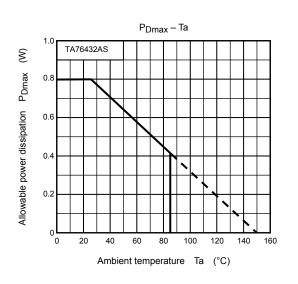
Error amplification circuit for switching power supply

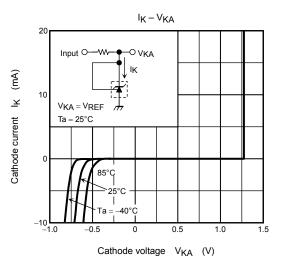


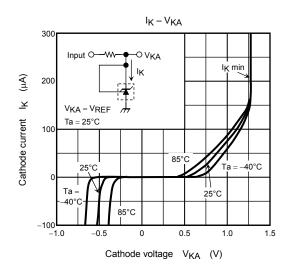
This circuit amplifies the difference between the switching power supply's secondary output voltage and the shunt regulator's reference voltage. It then feeds the amplified voltage back to the primary input voltage via the photocoupler.

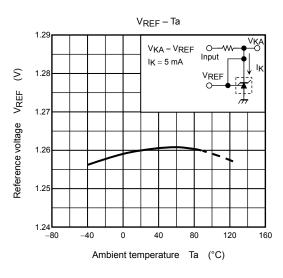


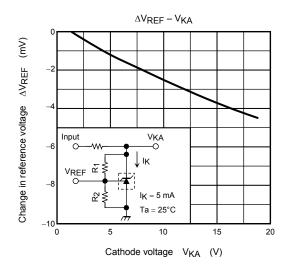


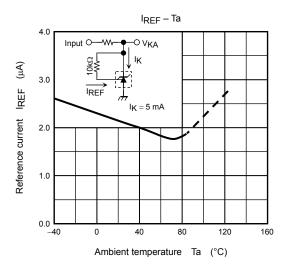


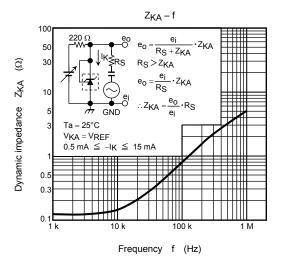


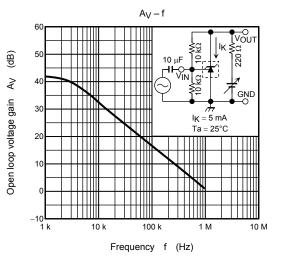


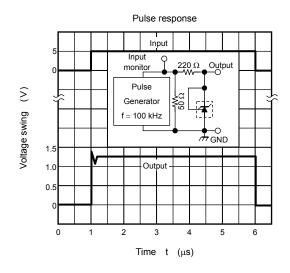


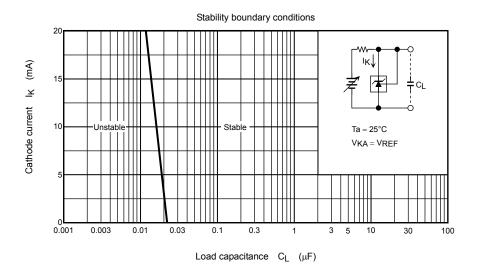








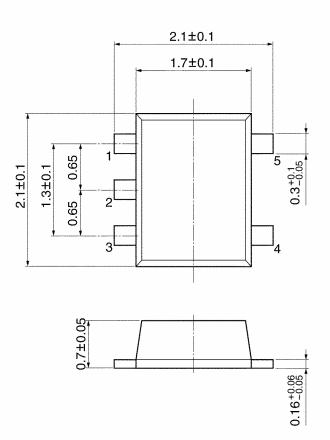




### **Package Dimensions**

### SSOP5-P-0.65C

Unit: mm

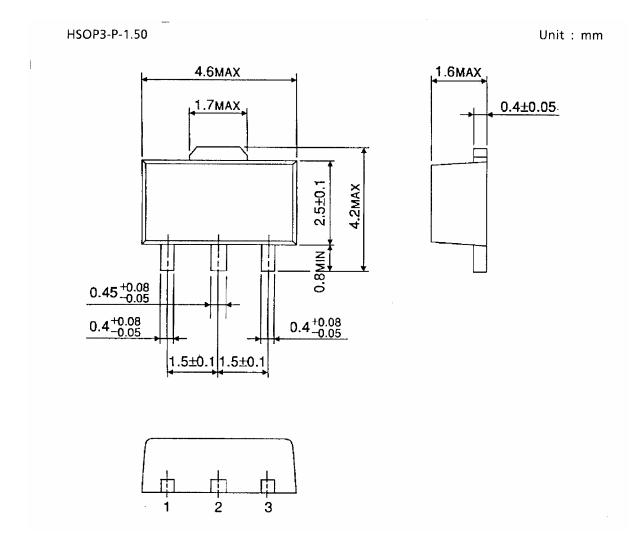


TA76432AFT (UFV)

Weight: 0.007 g (typ.)

# <u>TOSHIBA</u>

## Package Dimensions

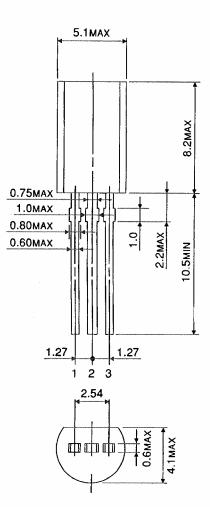


TA76432AF/AFR (PW-MINI)

Weight: 0.05 g (typ.)

## Package Dimensions

SSIP3-P-1.27



TA76432AS (TO-92MOD)

Weight: 0.36 g (typ.)

### **RESTRICTIONS ON PRODUCT USE**

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