

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIERS		REVERSE VOLTAGE - 20 to 100 Volts FORWARD CURRENT - 1.0 Amperes								
FEATURES ●For surface mounted applications ●Metal-Semiconductor junction with guarding ●Epitaxial construction ●Very low forward votage drop ●High current capability ●Plastic material has UL flammability classification 94V-0 ●For use in lowvoltage, high frequency inverters, free wheeling, and polarity protection applications. MECHANICAL DATA ●Case: Molded Plastic ●Polarity:Color band denotes cathode ●Weight: 0.003 ounces,0.093 grams ●Mounting position: Any		SMB Top View Dimensions: Width: .185(4.70) / .160(4.06) Height: .155(3.94) / .130(3.30) Lead Width: .083(2.11) / .075(1.91) Side View Dimensions: Total Height: .096(2.44) / .084(2.13) Lead Height: .012(.305) / .006(.152) Lead Width: .060(1.52) / .030(0.76) Foot Length: .220(5.59) / .200(5.08) Foot Width: .008(.203) / .002(.051) Dimensions in inches and (millimeters)								
MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave ,60Hz, resistive or inductive load. For capacitive load, derate current by 20%										
CHARACTERISTICS		SYMBOL	SS12B	SS13B	SS14B	SS15B	SS16B	SS18B	SS110B	UNIT
Maximum Recurrent Peak Reverse Voltage		V _{RRM}	20	30	40	50	60	80	100	V
Maximum RMS Voltage		V _{RMS}	14	21	28	35	42	56	70	V
Maximum DC Blocking Voltage		V _{DC}	20	30	40	50	60	80	100	V
Maximum Average Forward Rectified Current @T _L =100 °C		I _(AV)	1.0							A
Peak Forward Surage Current 8.3ms Single Half Sine-Wave Super Imposed On Rated Load (JEDEC Method)		I _{FSM}	40							A
Maximum Forward Voltage at 1.0A DC		V _F	0.45	0.55	0.6	0.70		0.85		V
Maximum DC Reverse Current @T _J =25°C at Rated DC Blocking Voltage @T _J =100°C		I _R	1.0 10							mA
Typical Junction Capacitance (Note1)		C _J	110							pF
Typical Thermal Resistance (Note2)		R _{θJL}	20							°C/W
Operating Temperature Range		T _J	-55 to + 150							°C
Storage Temperature Range		T _{STG}	-55 to + 150							°C
NOTES:1.Measured at 1.0 MHz and applied reverse voltage of 4.0V DC. 2.Thermal resistance junction to lead.										

FIG. 1 - FORWARD CURRENT DERATING CURVE

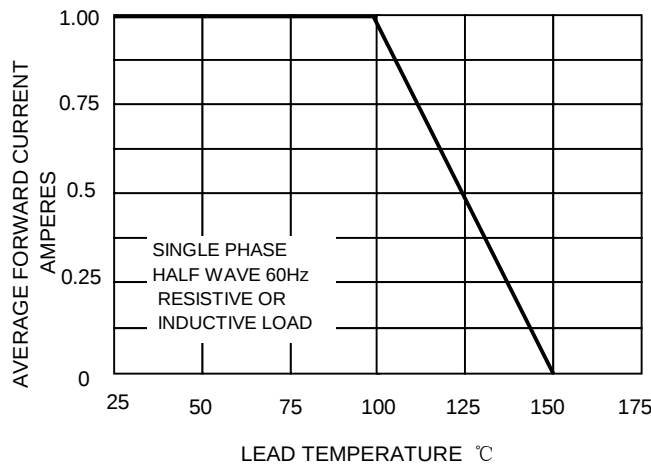


FIG. 2 – MAXIMUM NON-REPETITIVE SURGE CURRENT

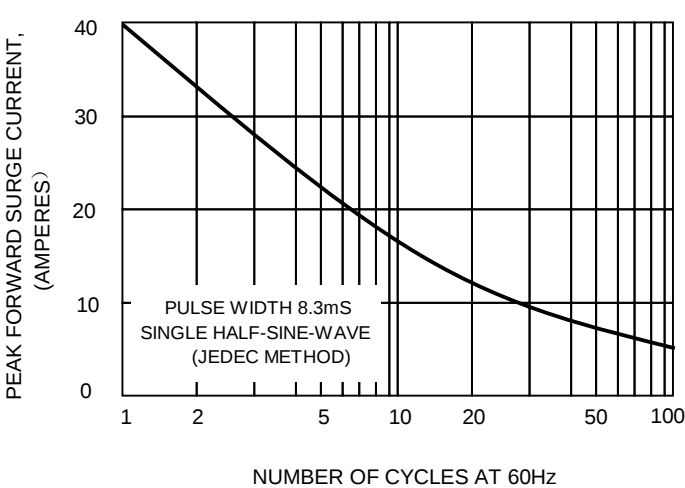


FIG.4-TYPICAL FORWARD CHARACTERISTICS

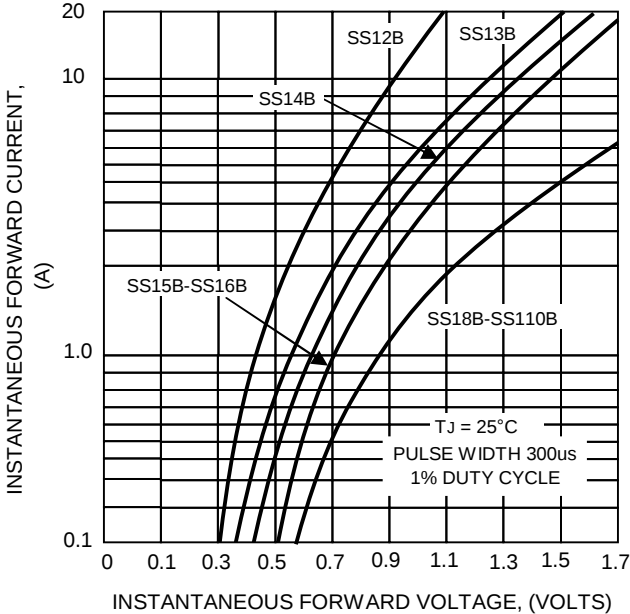


FIG.4-TYPICAL JUNCTION CAPACITANCE

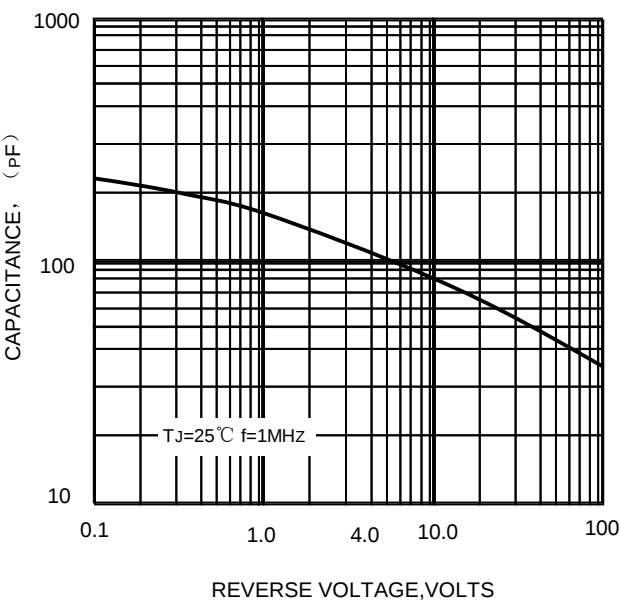


FIG.5-TYPICAL REVERSE CHARACTERISTICS

