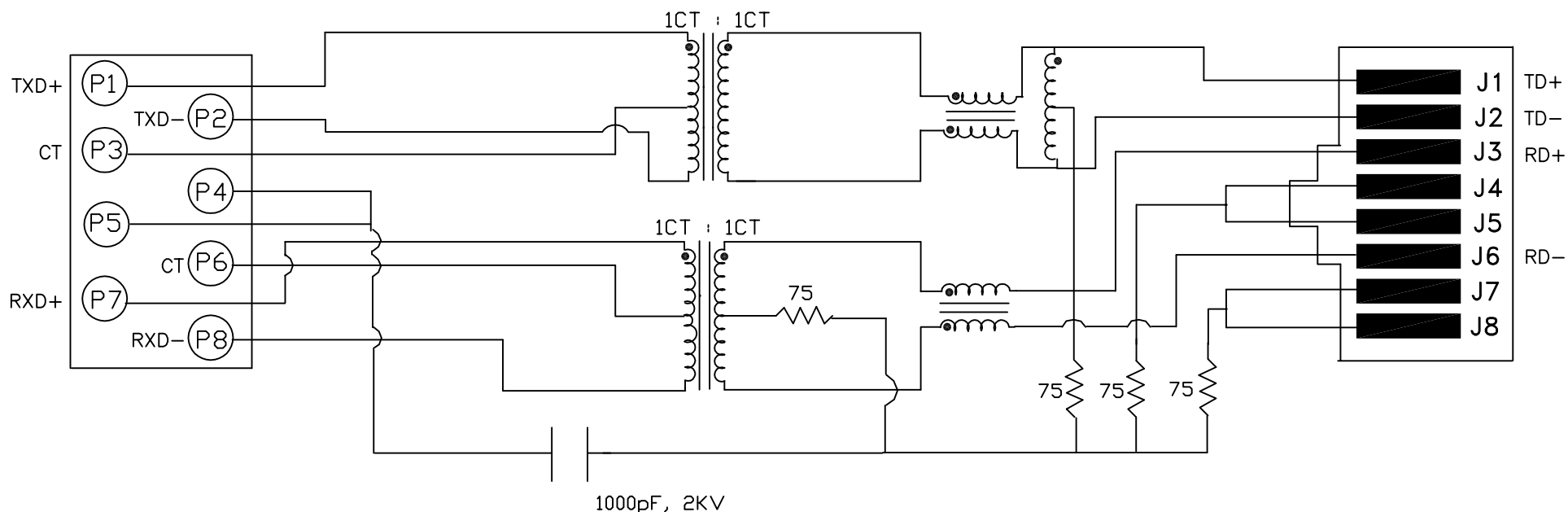




ELECTRICAL SPECIFICATIONS:

- 1.0 TURNS RATIO: $\begin{matrix} (P7-P6-P8) \\ (P1-P3-P2) \end{matrix} : \begin{matrix} (J3-J6) \\ (J1-J2) \end{matrix}$: $1CT : 1CT \pm 3\%$
: $1CT : 1CT \pm 3\%$
- 2.0 INDUCTANCE: $\begin{matrix} (P7-P8) \\ (P1-P2) \end{matrix}$: $350\mu H$ MIN. @ 0.1V, 100KHz, 8mA DC Bias
: $350\mu H$ MIN. @ 0.1V, 100KHz, 8mA DC Bias
- 3.0 LEAKAGE INDUCTANCE: $\begin{matrix} P8-P6-P7 \\ P2-P3-P1 \end{matrix}$ (WITH J6 AND J3 SHORT) : $0.3\mu H$ MAX. @ 1MHz
(WITH J2 AND J1 SHORT) : $0.3\mu H$ MAX. @ 1MHz
- 4.0 INTERWINDING CAPACITANCE: $\begin{matrix} (P8,P6,P7) \\ (P2,P3,P1) \end{matrix}$ TO $\begin{matrix} (J6,J3) \\ (J2,J1) \end{matrix}$: $30pf$ MAX @ 1MHz
: $30pf$ MAX @ 1MHz
- 5.0 DC RESISTANCE: $(J6-J3)=(J2-J1)$: 1.2 ohms Max.

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

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717.234.7512

MagJack

<http://www.stewartconnector.com>

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RECEIVE

6.0 RETURN LOSS: (P7-P8)=100 OHMS AND (P1-P2)=100 OHM REF.
1MHz TO 30MHz : 18dB MIN.
30MHz TO 80MHz : 12dB MIN.

NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).

7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P2) : 1500 VAC
(J3, J6) TO (P7,P8) : 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms
100KHz TO 100MHz : 1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX
PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1-100 MHz : 30 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : 35dB TYP

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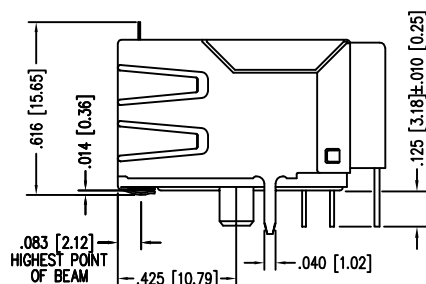
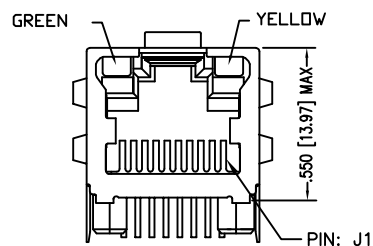
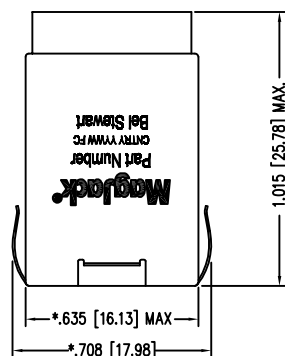
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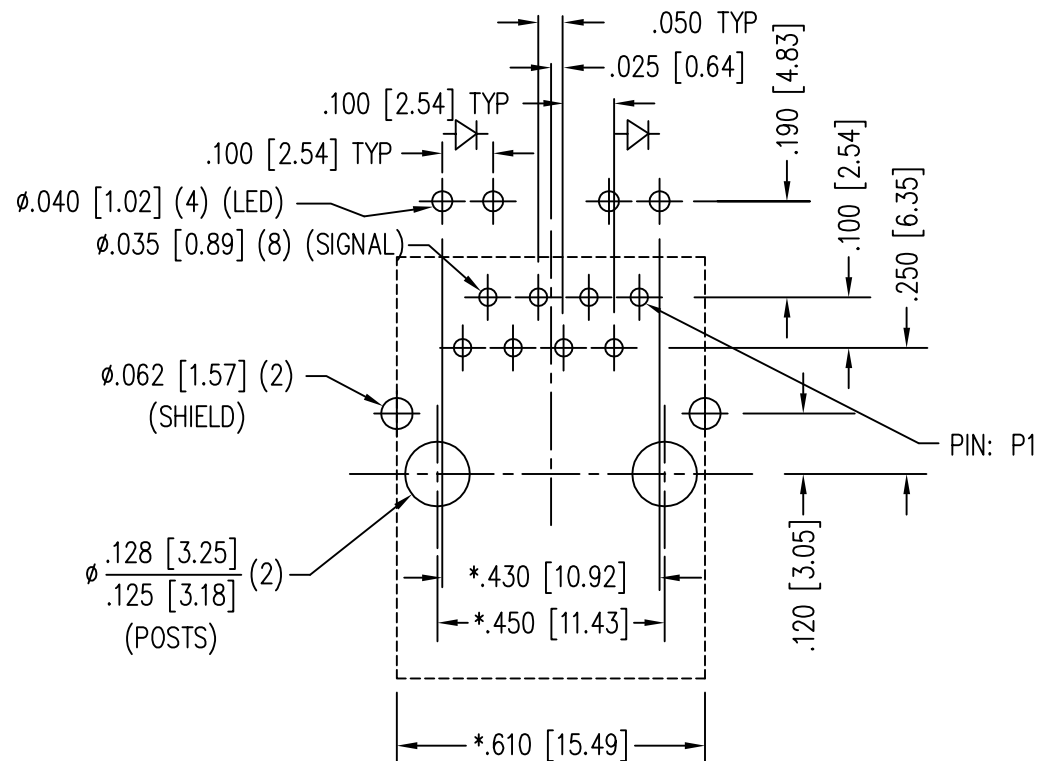
STANDARD LED	WAVELENGTH	* Forward V (MAX)	(TYP)
YELLOW	590 nm	2.5 V	2.1 V
GREEN	565 nm	2.5 V	2.2 V

* WITH A FORWARD CURRENT OF 20 mA

NOTES:

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING
- HIGH TEMPERATURE REFLOW COMPATIBLE - 230°C/90 SEC MAX.
- ALL POLYMERS FLAMMABILITY - UL94V0

CT750006



P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE
TOLERANCE ± 0.003 [0.08] UNLESS OTHERWISE SPECIFIED

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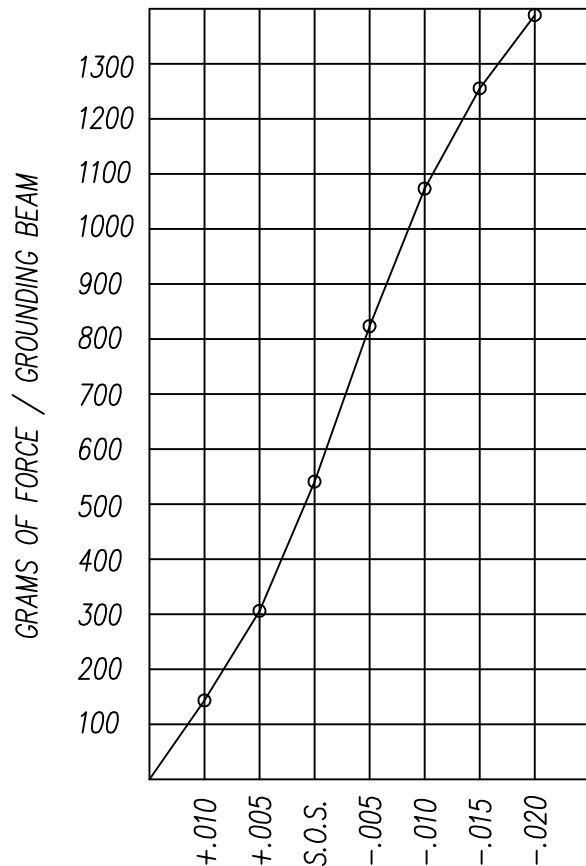
<http://www.stewartconnector.com>

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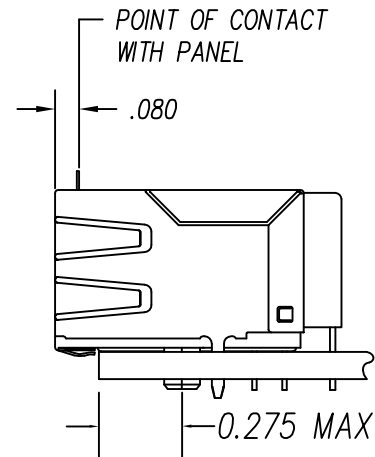
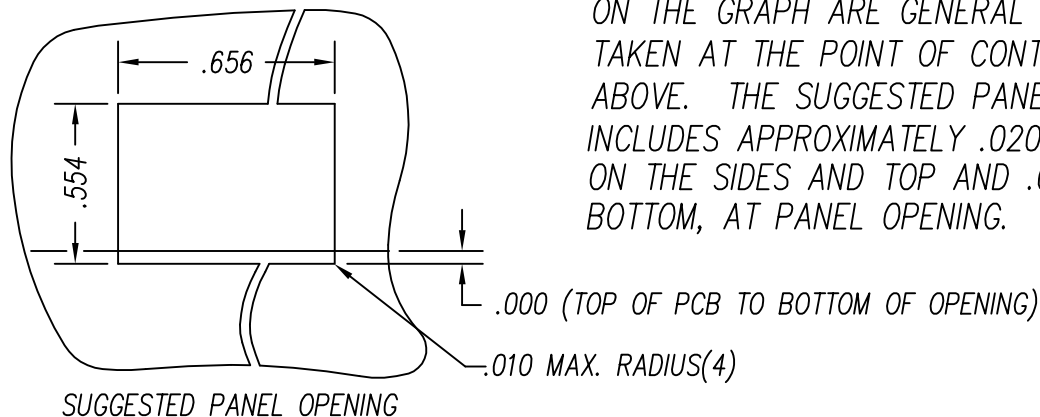
DRAWING NO.

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PANEL GROUNDING BEAM DEFLECTION
S.O.S. = SUGGESTED OPENING SIZE



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.

CT720034X1/24-001302

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