Preferred Device

Switching Diode

• High-Speed Switching Applications

• Lead Finish: 100% Matte Sn (Tin)

• Qualified Reflow Temperature: 260°C

• Extremely Small SOD-523 Package

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V_R	75	Vdc
Peak Forward Current	I _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C Derate above 25°C	P _D	200 1.57	mW mW/°C
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	635	°C/W
Junction and Storage Temperature	T _J , T _{stg}	–55 to 150	°C

- 1. FR-4 Minimum Pad.
- 2. 300 mW for 1 in. copper.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

				-	
Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Reverse Voltage Leakage Current $(V_R = 75 \text{ Vdc})$ $(V_R = 75 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ $(V_R = 25 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	I _R	1 1 1	1.0 50 30	μAdc	
Reverse Breakdown Voltage (I _{BR} = 100 μAdc)	V _(BR)	75	-	Vdc	
Forward Voltage ($I_F = 1.0 \text{ mAdc}$) ($I_F = 10 \text{ mAdc}$) ($I_F = 50 \text{ mAdc}$) ($I_F = 150 \text{ mAdc}$)	V _F		715 855 1000 1250	mV	
Diode Capacitance ($V_R = 0$, $f = 1.0 \text{ MHz}$)	C _D	-	2.0	pF	
Forward Recovery Voltage (I _F = 10 mAdc, t _r = 20 ns)	V _{FR}	ı	1.75	Vdc	
Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc}, R_L = 50 \Omega)$	t _{rr}	_	6.0	ns	
Stored Charge (I _F = 10 mAdc to V_R = 5.0 Vdc, R_L = 500 Ω)	Q _S	-	45	pC	



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MARKING DIAGRAM

SOD-523 CASE 502 PLASTIC



A6 = Specific Device Code

M = Month Code

= Pb–Free Package

(Note: Microdot may be in either location)

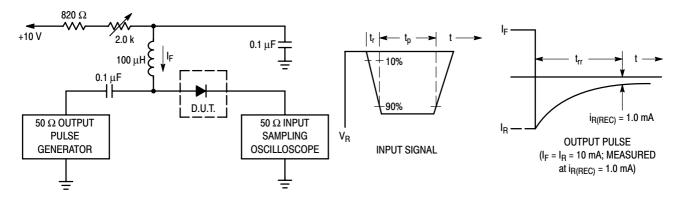
ORDERING INFORMATION

Device	Package	Shipping†
BAS16XV2T1	SOD-523	3000/Tape & Reel
BAS16XV2T1G	SOD-523 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

1



Notes: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

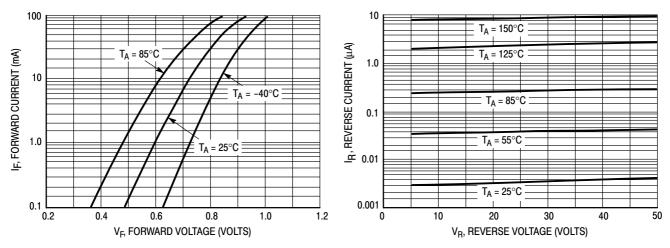


Figure 2. Forward Voltage

Figure 3. Leakage Current

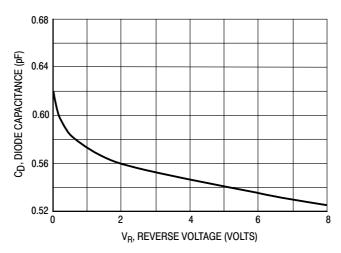
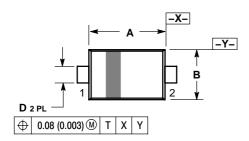
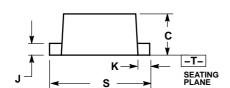


Figure 4. Capacitance

PACKAGE DIMENSIONS

SOD-523 CASE 502-01 **ISSUE B**





NOTES:

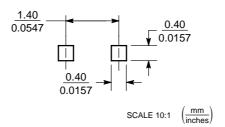
- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 1982.

 2. CONTROLLING DIMENSION: MILLIMETER.

 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

		MILLIMETERS			INCHES		
D	IM	MIN	NOM	MAX	MIN	NOM	MAX
	Α	1.10	1.20	1.30	0.043	0.047	0.051
	В	0.70	0.80	0.90	0.028	0.032	0.035
-	С	0.50	0.60	0.70	0.020	0.024	0.028
	D	0.25	0.30	0.35	0.010	0.012	0.014
	J	0.07	0.14	0.20	0.0028	0.0055	0.0079
	ĸ	0.15	0.20	0.25	0.006	0.008	0.010
	S	1.50	1.60	1.70	0.059	0.063	0.067

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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