

GENERAL DESCRIPTION

The CM3706 is a 1.5MHz constant frequency, slope compensated current mode PWM step-down converter. The device integrates a main switch and a synchronous rectifier for high efficiency without an external Schottky diode. It is ideal for powering portable equipment that runs from a single cell lithium-lon (Li+) battery. The CM3706 can supply 600mA of load current from a 2.5V to 5.5V input voltage. The output voltage can be regulated as low as 0.6V. The CM3706 can also run at 100% duty cycle for low dropout operation, extending battery life in portable system. Idle mode operation at light loads provides very low output ripple voltage for noise sensitive applications. The CM3706 is offered in a low profile (1mm) 5-pin, thin TSOT package, and is available in an adjustable version.

FEATURES

- High Efficiency: Up to 96%
- 1.5Mhz Constant Switching Frequency
- ♦ 600mA Output Current at V_{IN}=3V
- Integrated Main switch and synchronous rectifier. No Schottky Diode Required
- 2.5V to 5.5V Input Voltage Range
- Output Voltage as Low as 0.6V
- ◆ 100% Duty Cycle in Dropout
- Low Quiescent Current: 300μA
- Slope Compensated Current Mode Control for Excellent Line and Load Transient Response
- Short Circuit Protection
- ◆ Thermal Fault Protection
- <1uA Shutdown Current</p>
- Space Saving 5-Pin Thin TSOT23 package

APPLICATIONS

- Cellular and Smart Phones
- Microprocessors and DSP Core Supplies
- Wireless and DSL Modems
- ♦ PDAs

- ♦ MP3 Players
- Portable Instruments
- Digital Still and Video Cameras

TYPICAL APPLICATIONS

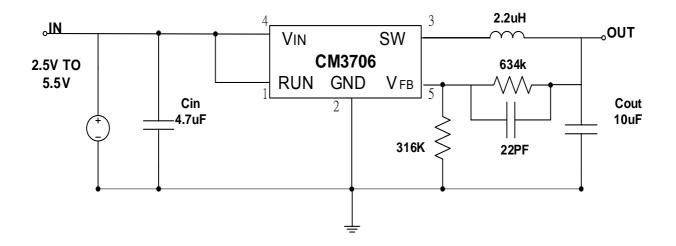
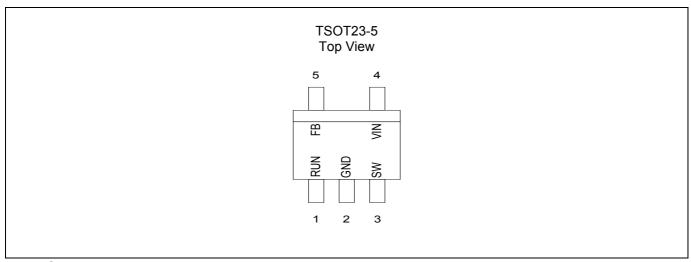


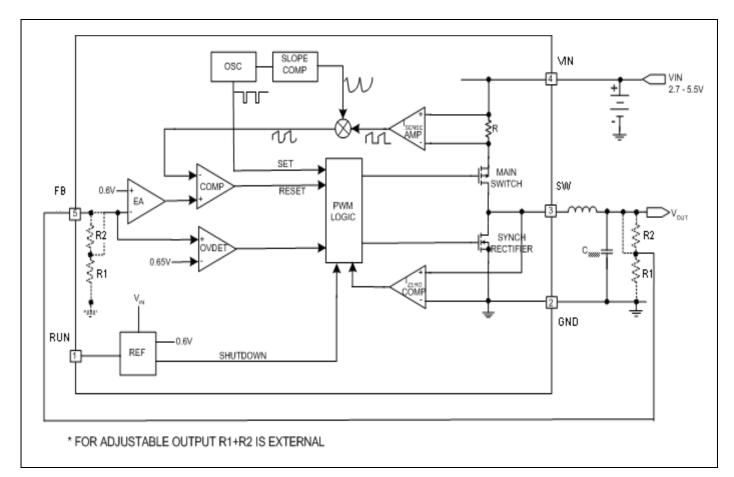
Figure 1. Basic Application Circuit with CM3706 adjustable version



PIN CONFIGURATION



BLOCK DIAGRAM



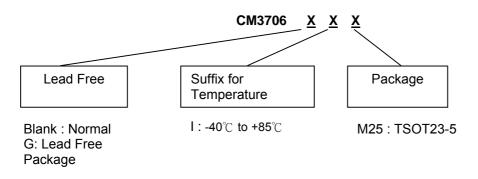


PIN DESCRIPTION

Pin No.	Symbol	Description
1	RUN	Regulator Enable control input. Drive RUN above 1.5V to turn on the part. Drive RUN below 0.3V to turn it off. In shutdown, all functions are disabled drawing <1µA supply current. Do not leave RUN floating.
2	GND	Ground Pin.
3	sw	Power Switch Output. It is the Switch note connection to Inductor. This pin connects to the drains of the internal P-CH and N-CH MOSFET switches.
4	VIN	Supply Input Pin. Must be closely decoupled to GND, Pin 2, with a 2.2µF or greater ceramic capacitor.
5	FB	VFB (CM3706): Feedback Input Pin. Connect FB to the center point of the external resistor divider. The feedback threshold voltage is 0.6V.

ORDERING INFORMATION

Part Number	Voltage	Temperature Range	Package
CM3706IM25	Adj	-40℃ to +85℃	TSOT23-5
CM3706GIM25	Adj	-40°C to +85°C	TSOT23-5



ABSOLUTE MAXIMUM RATINGS(Note 1)

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Input Supply Voltage	–0.3V to 6.0V	Peak SW Sink and Source Current	1.5A
RUN, VFB Voltages	0.3V to (VIN + 0.3V)	Junction Temperature (Note 2)	125°C
SW Voltage	– 0.3V to (VIN + 0.3V)	Storage Temperature Range	– 65°C to 150°C
Operating Temperature Range	40°C to +85°C	Lead Temperature (Soldering, 10 sec)	300°C

OPERATING CONDITIONS

Temperature Range	40°C to 85°C
Vin Operating Range	2.5V to 5.5V



Thermal Resistance (Note 3):

Package	Θ_{JA}	Θ_{JC}
TSOT23-5	250°C/W	110°C/W

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: TJ is calculated from the ambient temperature TA and power dissipation PD according to the following formula: CM3706: TJ = TA + (PD) \times (Θ_{JA})

Note 3: Thermal Resistance is specified with approximately 1 square of 1 oz copper.

ELECTRICAL CHARACTERISTICS (Unless otherwise stated, these specifications apply T_A=25°C; VIN=+3.6V) maximum ratings are stress ratings only and functional device operation is not implied.

Parameter	Conditions	MIN	TYP	MAX	unit
Input Voltage Range		2.5		5.5	V
Input DC Supply Current Active Mode	V _{FB} =0.5V		270	400	μΑ
Shutdown Mode	V_{FB} =0V, V_{IN} =4.2V		0.08	1.0	μΑ
	T _A = +25°C	0.5880	0.6000	0.6120	V
Regulated Feedback Voltage	$T_A = 0$ °C $\leq T_A \leq 85$ °C	0.5865	0.6000	0.6135	V
	$T_A = -40$ °C $\leq T_A \leq 85$ °C	0.5850	0.6000	0.6150	V
V _{FB} Input Bias Current	V _{FB} = 0.65V			±30	nA
Reference Voltage Line Regulation	$V_{IN} = 2.5V \text{ to } 5.5V,$		0.11	0.4	%/V
	CM3706-1.2, -40° C $\leq T_A \leq 85^{\circ}$ C	1.164	1.200	1.236	V
Regulated Output Voltage	CM3706-1.5, -40° C $\leq T_A \leq 85^{\circ}$ C	1.455	1.500	1.545	V
	CM3706-1.8, -40° C $\leq T_A \leq 85^{\circ}$ C	1.746	1.800	1.854	V
Output Voltage Line Regulation	VIN = 2.5V to 5.5V,lout=10mA		0.11	0.40	%/V
Output Voltage Load Regulation	louτ from 10 to 600mA		0.0015		%/mA
Peak Inductor Current	V_{IN} =3V, V_{FB} =0.5V or V_{OUT} =90% Duty Cycle <35%		1.2		Α
Oscillator Frequency	V _{FB} =0.6V or V _{OUT} =100%	1.2	1.5	1.8	MHz
R _{DS(ON)} of P-CH MOSFET	I _{SW} = 300mA		0.3	0.50	Ω
R _{DS(ON)} of N-CH MOSFET	I _{SW} = -300mA		0.2	0.45	Ω
SW Leakage	V _{RUN} = 0V, V _{SW} = 0V or 5V, V _{IN} = 5V		±0.01	±1	μΑ
RUN Threshold	-40°C ≤ T _A ≤ 85°C	0.3	0.45	1.5	V
RUN Leakage Current			±0.1	±1	μΑ
Thermal Shutdown Trip Threshold			150		$^{\circ}\!\mathbb{C}$
Output over voltage lockout	ΔVovl = Vovl – Vfb		60		mV

Note 4: 100% production test at +25°C. Specifications over the temperature range are guaranteed by design and characterization.



FUNCTIONAL DESCRIPTION

Operation

CM3706 is a monolithic switching mode Step-Down DC-DC converter. It utilizes internal MOSFETs to achieve high efficiency and can generate very low output voltage by using internal reference at 0.6V. It operates at a fixed switching frequency, and uses the slope compensated current mode architecture. This Step-Down DC-DC Converter supplies 600mA output current at VIN = 3V with input voltage range from 2.5V to 5.5V.

Current Mode PWM Control

Slope compensated current mode PWM control provides stable switching and cycle-by-cycle current limit for excellent load and line responses and protection of the internal main switch (P-Ch MOSFET) and synchronous rectifier (N-CH MOSFET). During normal operation, the internal P-Ch MOSFET is turned on for a certain time to ramp the inductor current at each rising edge of the internal oscillator, and switched off when the peak inductor current

is above the error voltage. The current comparator, $\ensuremath{\mathsf{ICOMP}},$ $\ensuremath{\mathsf{limits}}$

the peak inductor current. When the main switch is off, the synchronous rectifier will be turned on immediately and stay on until either the inductor current starts to reverse, as indicated by the current reversal comparator, IZERO, or the beginning of the next clock cycle. The OVDET comparator controls output transient overshoots by turning the main switch off and keeping it off until the fault is no longer present.

Dropout Operation

When the input voltage decreases toward the value of the output voltage, the CM3706 allows the main switch to remain on for more than one switching cycle and increases the duty cycle (Note 5) until it reaches 100%. The output voltage then is the input voltage

minus the voltage drop across the main switch and the inductor. At low input supply voltage, the RDS(ON) of the P-Channel MOSFET increases, and the efficiency of the converter decreases. Caution must be exercised to ensure the heat dissipated not to exceed the maximum junction temperature of the IC.

Idle Mode Operation

At very light loads, the CM3706 automatically enters Idle Mode. In the Idle Mode, the inductor current may reach zero or reverse on each pulse. The PWM control loop will automatically skip pulses to

maintain output regulation. The bottom MOSFET is turned off by the current reversal comparator, IZERO, and the switch voltage will ring. This is discontinuous mode operation, and is normal behavior for the switching regulator.

Maximum Load Current

The CM3706 will operate with input supply voltage as low as 2.5V, however, the maximum load current decreases at lower input due to large IR drop on the main switch and synchronous rectifier. The slope compensation signal reduces the peak inductor current as a function of the duty cycle to prevent sub-harmonic oscillations at duty cycles greater than 50%. Conversely the current limit increases as the duty cycle decreases.

Note 5: The duty cycle D of a step-down converter is defined as:

$$D = T_{ON} \times f_{OSC} \times 100\% \approx \frac{V_{OUT}}{V_{IN}} \times 100\%$$

where T_{ON} is the main switch on time, and f_{OSC} is the oscillator frequency (1.5Mhz).

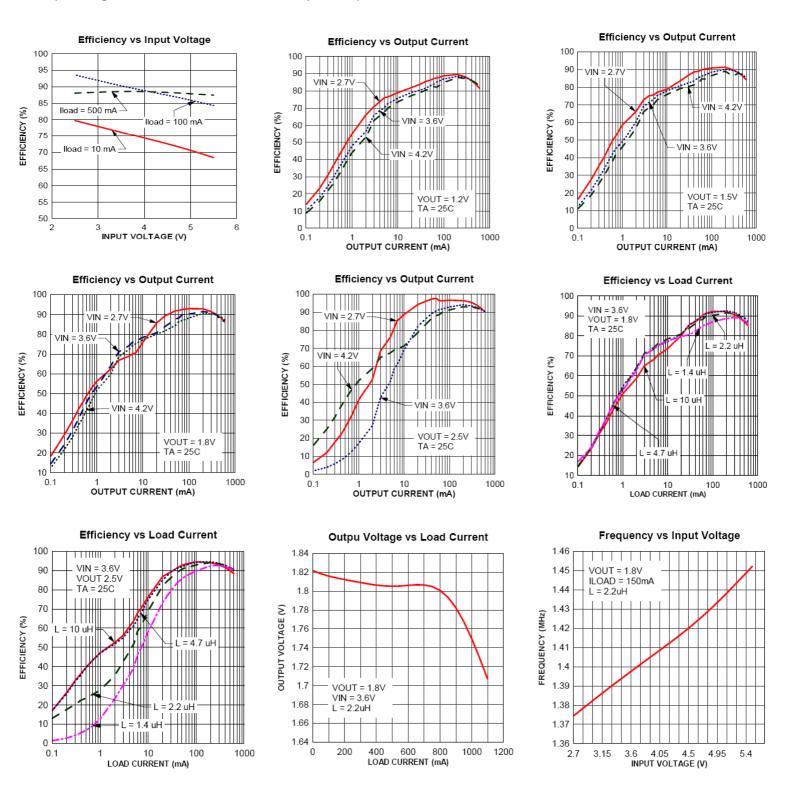


CM3706

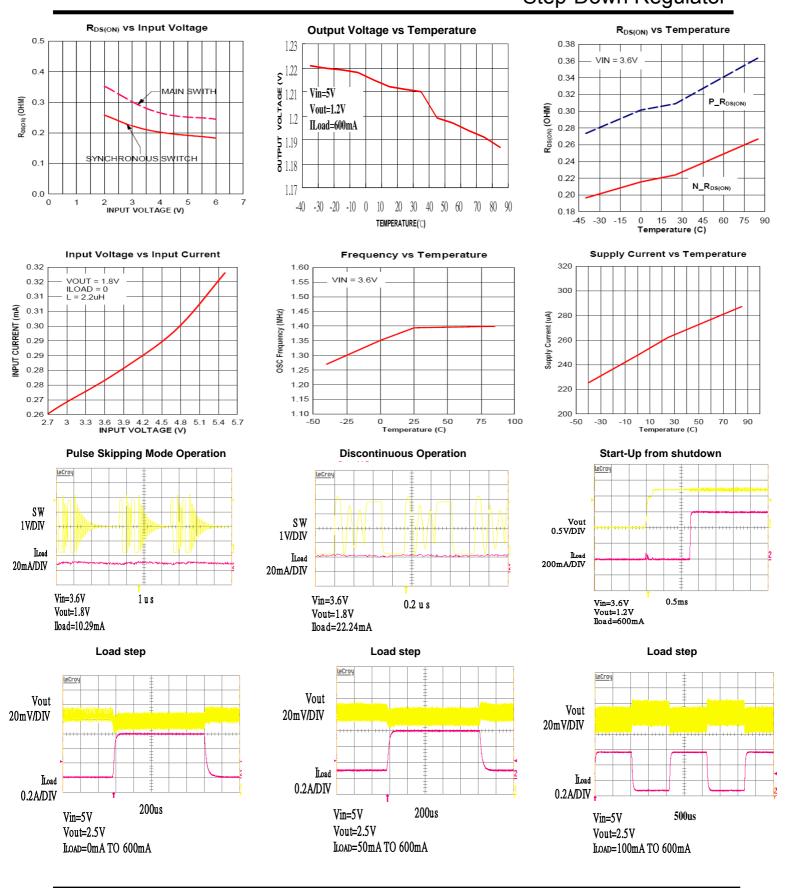
1.5MHz, 600mA Synchronous Step-Down Regulator

Typical Performance Characteristics

(Test Figure 1 above unless otherwise specified)









APPLICATIONS INFORMATION

Setting the Output Voltage

Figure 2 above shows the basic application circuit with CM3706 adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.6V \left(1 + \frac{R2}{R1}\right)$$

$$\begin{split} &\text{R1=316K}\Omega\;;\;\text{R2=316K}\Omega\;;\;\text{V}_{\text{OUT}}\text{=}1.2\text{V}\\ &\text{R1=316K}\Omega\;;\;\text{R2=470K}\Omega\;;\;\text{V}_{\text{OUT}}\text{=}1.5\text{V}\\ &\text{R1=316K}\Omega\;;\;\text{R2=630K}\Omega\;;\;\text{V}_{\text{OUT}}\text{=}1.8\text{V}\\ &\text{R1=316K}\Omega\;;\;\text{R2=1M}\Omega\;;\;\text{V}_{\text{OUT}}\text{=}2.5\text{V}\\ &\text{R1=316K}\Omega\;;\;\text{R2=1.42M}\Omega\;;\;\text{V}_{\text{OUT}}\text{=}3.3\text{V}. \end{split}$$

Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency shall be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A 4.7µF ceramic capacitor for most applications is sufficient.

Inductor Selection

For most designs, the CM3706 operates with inductors of $1\mu H$ to $4.7\mu H$. Low inductance values are physically smaller but require faster switching, which results in some efficiency loss. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 35% of the maximum load current 600mA, or ΔI_L =210mA.

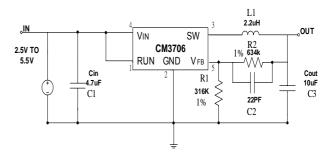


Figure 2 Application Circuit with adjustable output versions

For output voltages above 2.0V, when light-load efficiency is important, the minimum recommended inductor is $2.2\mu H$.

For optimum voltage-positioning load transients, choose an inductor with DC series resistance in the $50m\Omega$ to $150m\Omega$ range. For higher efficiency at heavy loads, or minimal load regulation (but some transient overshoot), the resistance should be kept below $100m\Omega$. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (600mA+105mA).

Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current. The output ripple V_{OUT} is determined by:

$$\Delta V_{OUT} \leq \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times f_{OSC} \times L} \times \left(ESR + \frac{1}{8 \times f_{osc} \times C3}\right)$$

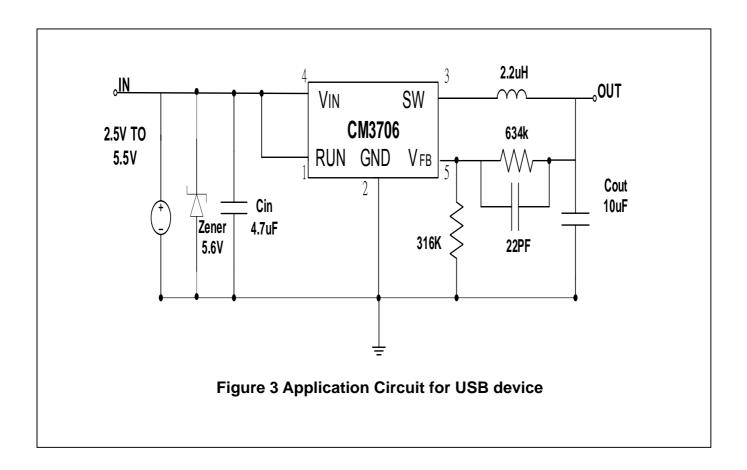
Input Zener Selection

In USB device application, we will add zener. The zener can be reduced the surge voltage when device plug in and plug out. Application circuit is show in Figure 3.



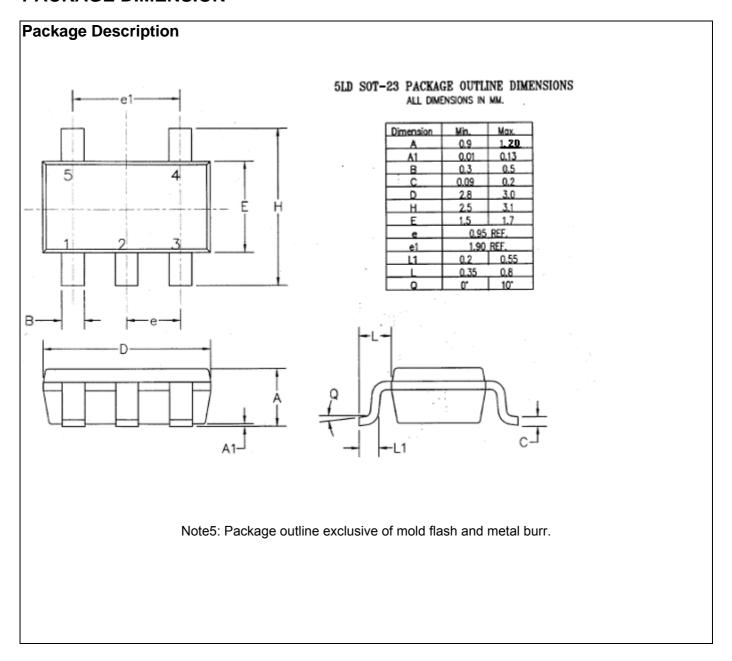


APPLICATION CIRCUIT Application Circuit for USB device





PACKAGE DIMENSION





CM3706

1.5MHz, 600mA Synchronous Step-Down Regulator

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