



Dual N-Channel 25-V (D-S) MOSFET

PRODUCT SUMMARY

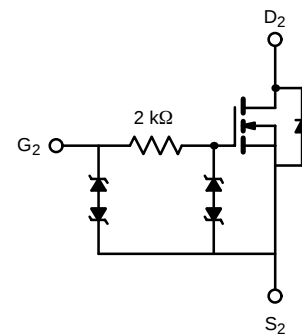
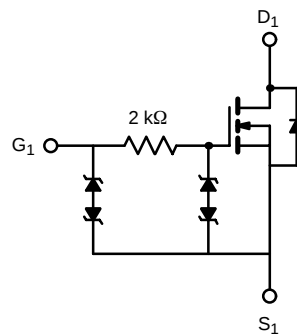
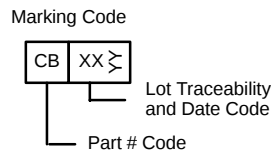
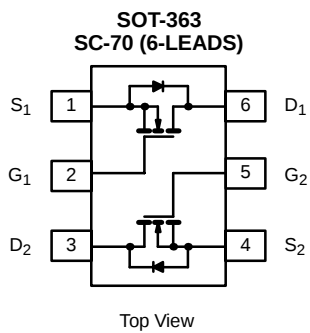
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
25	0.810 @ $V_{GS} = 4.5$ V	0.73
	1.04 @ $V_{GS} = 2.5$ V	0.65

FEATURES

- TrenchFET® Power MOSFETS: 2.5-V Rated
- ESD Protected: 1800 V
- Thermally Enhanced SC-70 Package

APPLICATIONS

- Load Switching
- PA Switch
- Level Switch

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	5 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	25		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current (T _J = 150°C) ^a	T _A = 25°C	I _D	0.73	0.64	A
	T _A = 85°C		0.53	0.46	
Pulsed Drain Current		I _{DM}	2		
Continuous Diode Current (Diode Conduction) ^a		I _S	0.61	0.48	
Maximum Power Dissipation ^a	T _A = 25°C	P _D	0.74	0.57	W
	T _A = 85°C		0.38	0.30	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	−55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 5$ sec	R_{thJA}	130	170	$^\circ\text{C/W}$
	Steady State		170	220	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	80	100	

Notes

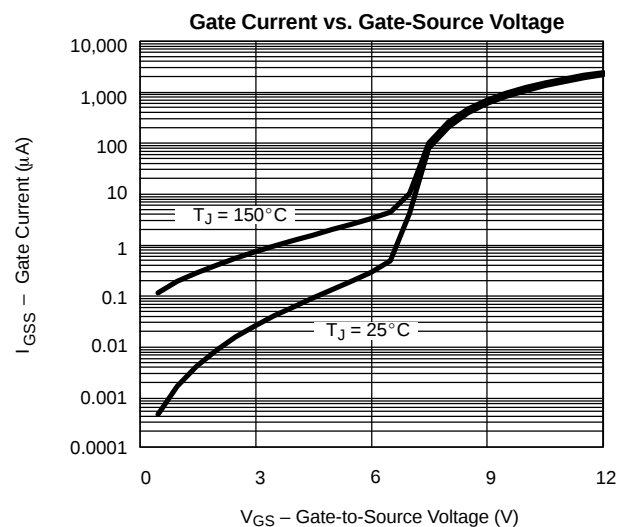
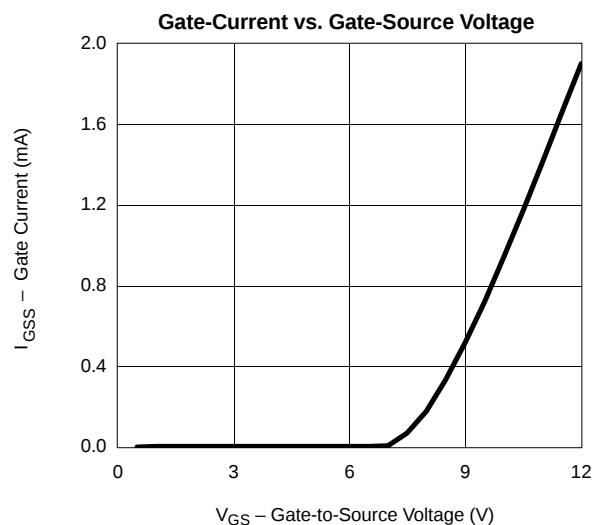
a. Surface Mounted on 1" x 1" FR4 Board.

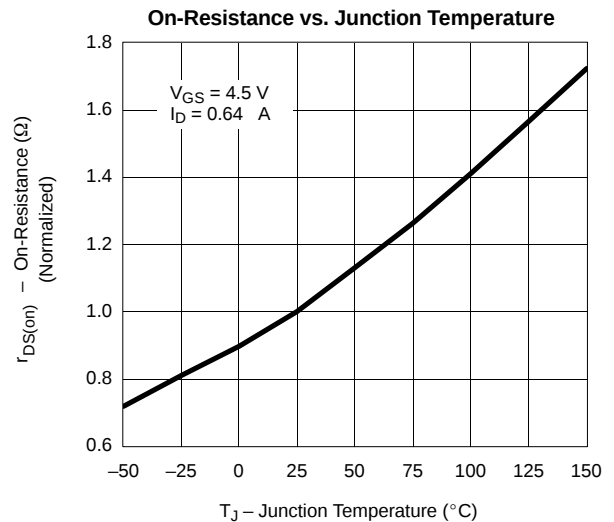
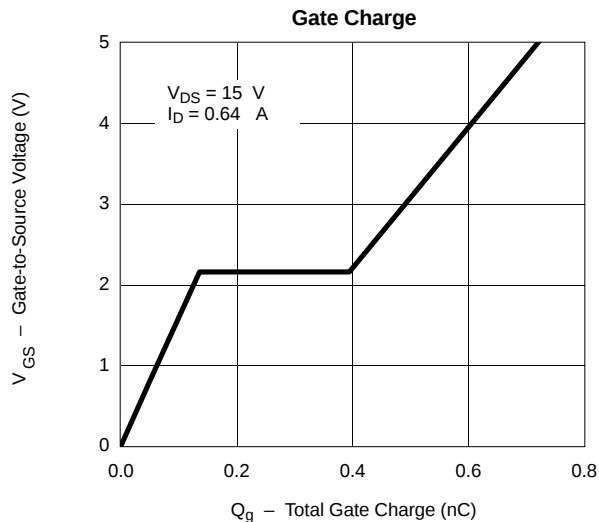
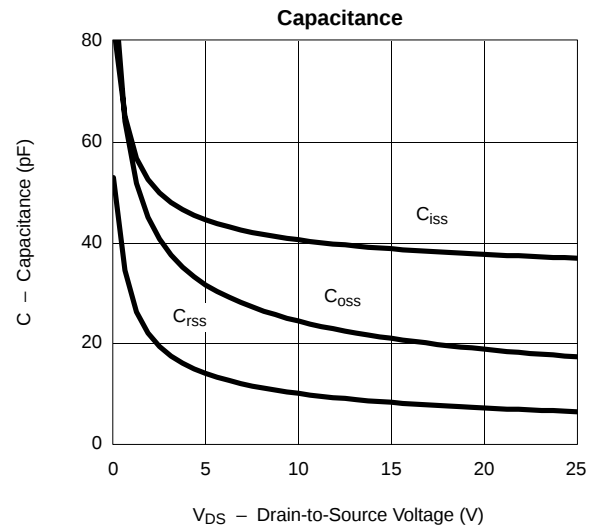
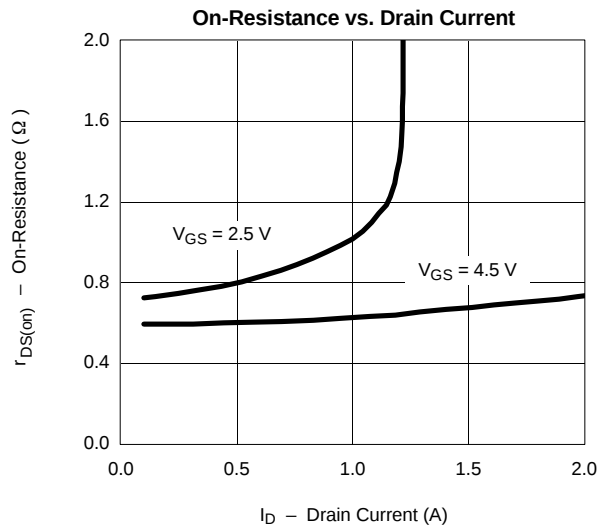
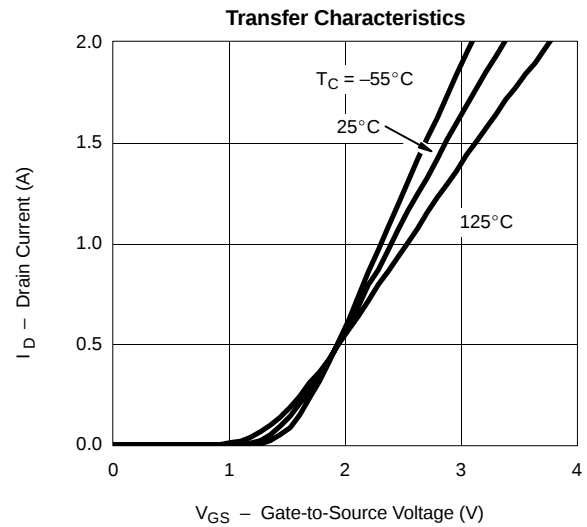
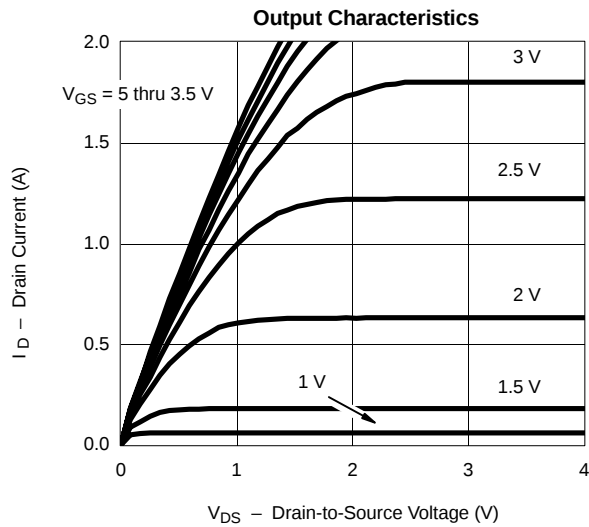
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

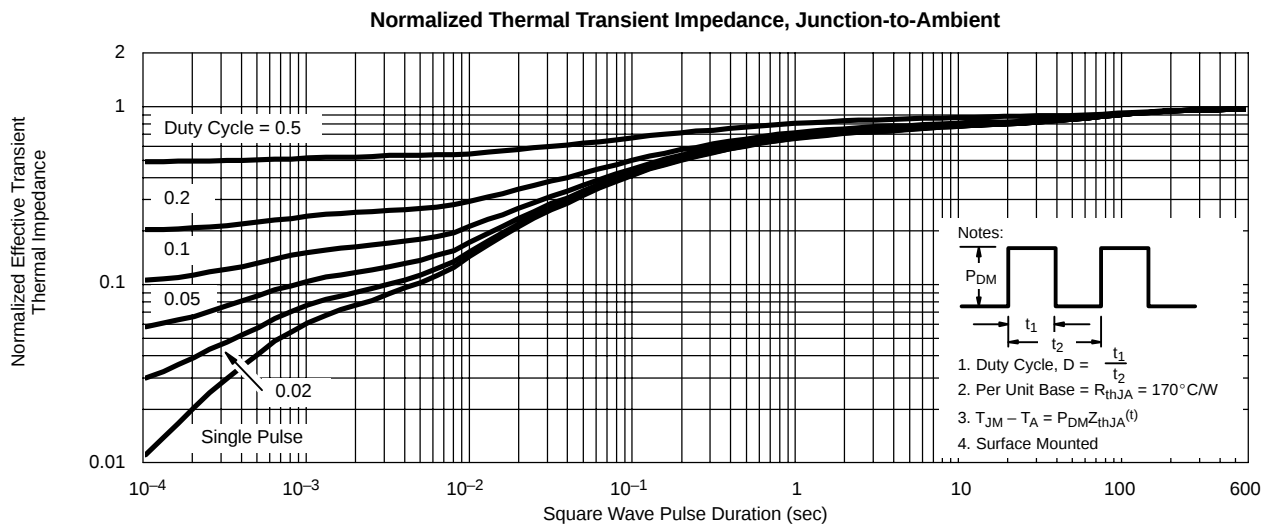
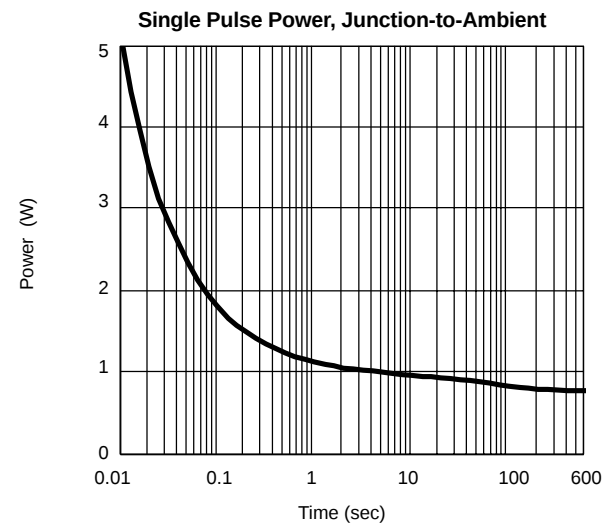
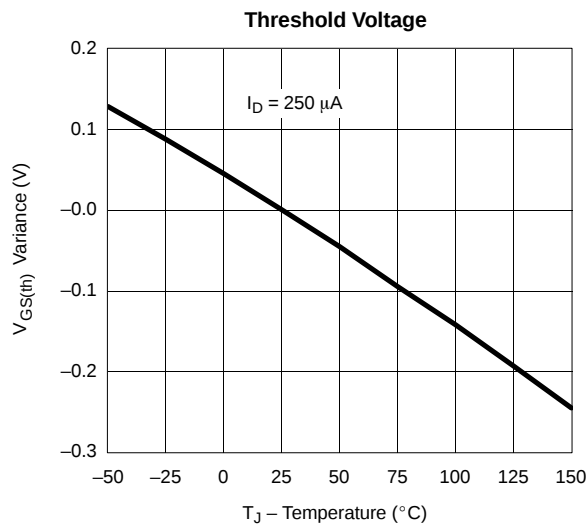
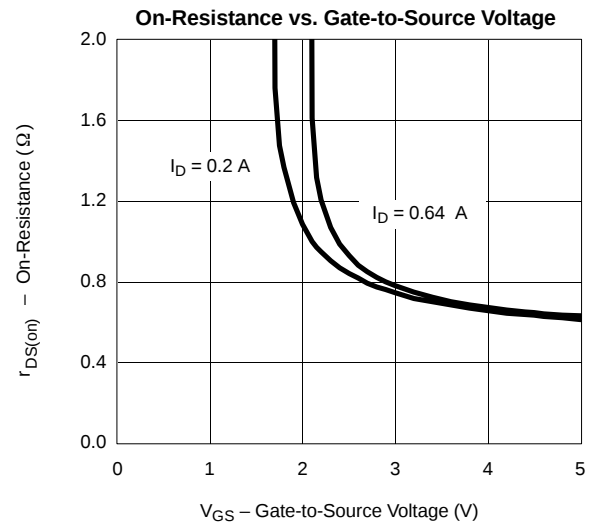
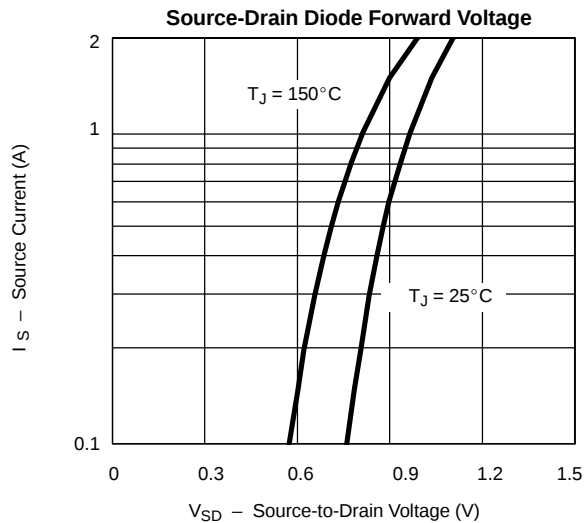
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	0.6			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\ \text{V}$, $V_{GS} = \pm 4.5\ \text{V}$			± 1	μA
		$V_{DS} = 0\ \text{V}$, $V_{GS} = \pm 8\ \text{V}$			± 1	mA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\ \text{V}$, $V_{GS} = 0\ \text{V}$			1	μA
		$V_{DS} = 20\ \text{V}$, $V_{GS} = 0\ \text{V}$, $T_J = 85^\circ\text{C}$			5	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5\ \text{V}$, $V_{GS} = 4.5\ \text{V}$	2			A
Drain-Source On-State Resistance ^a	$r_{DS(on)}$	$V_{GS} = 4.5\ \text{V}$, $I_D = 0.64\ \text{A}$		0.630	0.810	Ω
		$V_{GS} = 2.5\ \text{V}$, $I_D = 0.2\ \text{A}$		0.830	1.04	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\ \text{V}$, $I_D = 0.64\ \text{A}$		1.1		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 0.48\ \text{A}$, $V_{GS} = 0\ \text{V}$		0.80	1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 15\ \text{V}$, $V_{GS} = 4.5\ \text{V}$, $I_D = 0.64\ \text{A}$		0.66	1.0	nC
Gate-Source Charge	Q_{gs}			0.14		
Gate-Drain Charge	Q_{gd}			0.26		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\ \text{V}$, $R_L = 30\ \Omega$ $I_D \approx 0.5\ \text{A}$, $V_{GEN} = 4.5\ \text{V}$, $R_G = 6\ \Omega$		42	65	ns
Rise Time	t_r			85	130	
Turn-Off Delay Time	$t_{d(off)}$			200	300	
Fall Time	t_f			160	240	

Notes

- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

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