TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK2551

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance : $RDS(ON) = 7.2 \text{ m}\Omega \text{ (typ.)}$

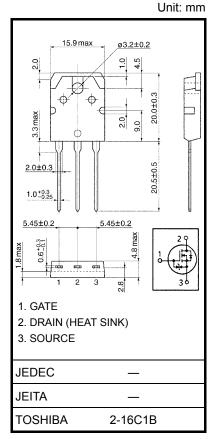
• High forward transfer admittance : $|Y_{fs}| = 50 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = 50 \,\text{V})$

• Enhancement mode : $V_{th} = 1.5 \sim 3.0 \text{ V (Vps} = 10 \text{ V, Ip} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	50	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	50	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	ΙD	50	Α	
	Pulse (Note 1)	I _{DP}	200	Α	
Drain power dissipation	n (Tc = 25°C)	P_{D}	150	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	894	mJ	
Avalanche current		I _{AR}	50	Α	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 440 \mu\text{H}$, $R_G = 25 \Omega$, $I_{AR} = 50 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



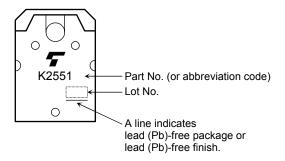
Electrical Characteristics (Ta = 25°C)

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	50	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 25 A	_	7.2	11	mΩ
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 25 A	30	50	_	S
Input capacitano	е	C _{iss}			4000	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		800	_	pF
Output capacita	put capacitance C _{oss}		_	2000	_		
Switching time	Rise time	t _r	V_{GS} V_{OV} V_{OUT}	_	25	_	- ns
	Turn-on time	t _{on}		_	40	_	
	Fall time	t _f		_	120	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\rm w} = 10 \mu \rm s$	_	360	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	130	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		90	_	nC
Gate-drain ("miller") charge		Q _{gd}			40	_	

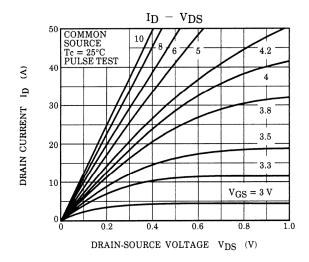
Source-Drain Ratings and Characteristics (Ta = 25°C)

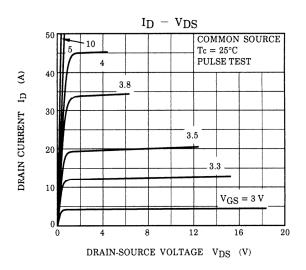
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	50	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	200	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 50 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 50 A, V _{GS} = 0 V		140	_	ns
Reverse recovered charge	Q _{rr}	dI _{DR} / dt = 50 Å / μs		77	_	μC

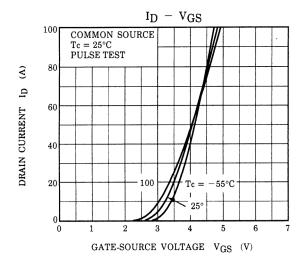
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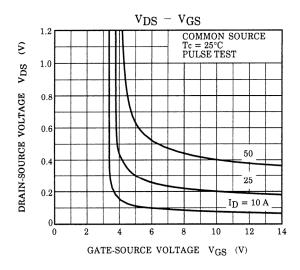


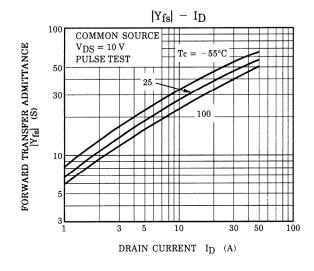
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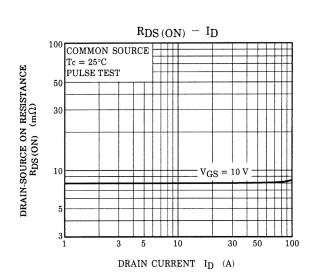




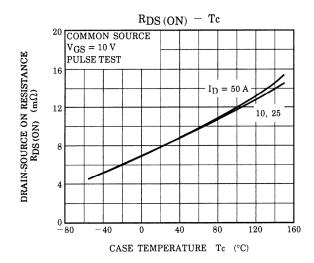


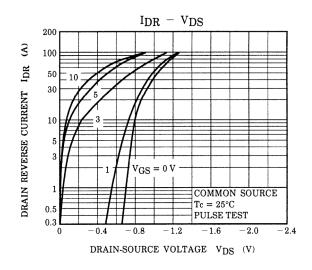


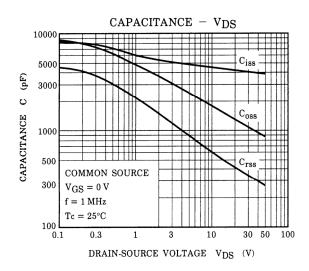


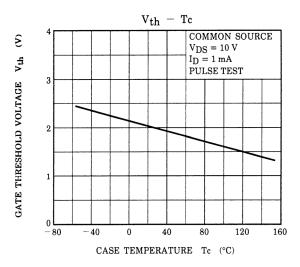


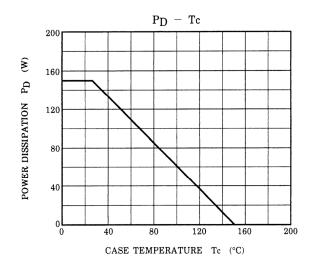
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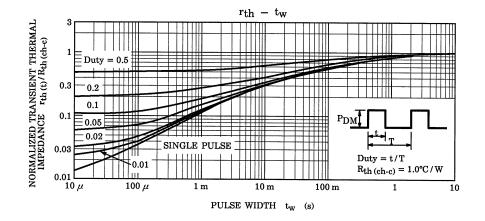


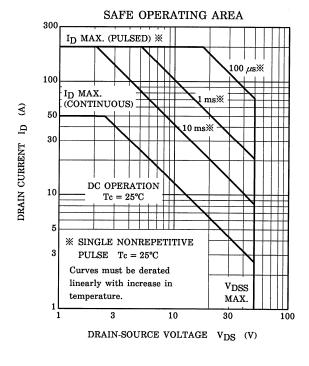


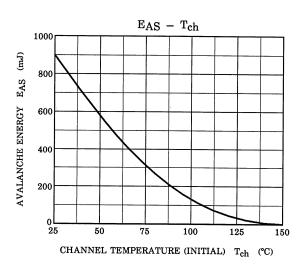


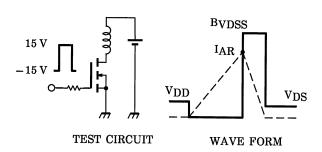


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$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 25~V,~L = 440~\mu H \end{aligned} \qquad EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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