TRAFFIC CONTROL MESSAGES AND WARNING TONE CIRCUIT

The TDA1589 is for evaluation of operating signals and logic control signals of a traffic control (TC) message decoder.

Features

- mute of non-traffic control stations
- · restriction to traffic-control message reception
- LED display driver for MUTE indication
- · control output for TC messages minimum volume
- delayed start of warning tone signal on failure of TC transmission. Also to be used to control a start
 of search tuning
- warning tone generator with automatic level control increasing volume in five steps
- interruption of cassette playback with motor stop during TC messages
- warning tone indicating failure of TC transmission also during cassette playback

QUICK REFERENCE DATA

Supply voltage (pin 10)	V-	7,5	7,5 to 16 V			
	Vp	typ.	8,5 V			
Supply current	lp	typ.	4,5 mA			
Warning tone maximum voltage	$V_{o(p-p)}$	typ.	4,3 V			
Output LED driver current (pin 3) motor stop current (pin 5) motor stop current (pin 6) MUTE display current (pin 8) start warning tone current (pin 13)	13 15 16 18	typ. typ. typ. typ. typ.	30 mA 30 mA 2 mA 2 mA 2 mA			
Saturation voltage at output for minimum volume-on (pin 7)	V _{7 sat}	<	0,1 V			

PACKAGE OUTLINE

18-lead DIL; plastic (SOT102).

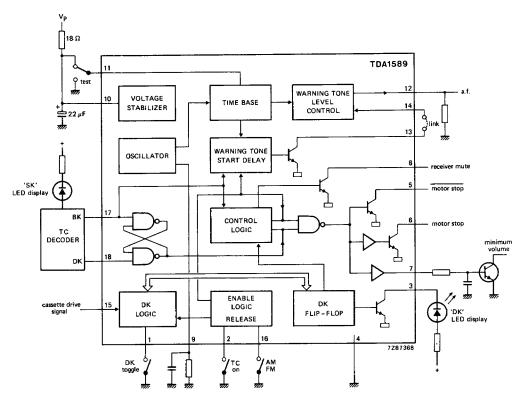
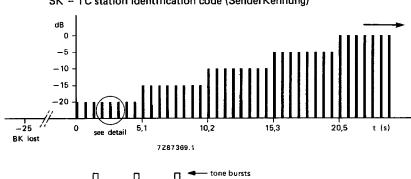


Fig. 1 Block diagram with external components; used as test circuit.

BK = TC area identification code (BereichsKennung)

DK = TC message identification code (DurchsageKennung)

SK = TC station identification code (SenderKennung)



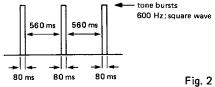


Fig. 2 Intermittent step pulse warning signal.

DETAIL

FUNCTIONAL DESCRIPTION

The automatic evaluation of traffic control signals is only possible during FM reception. The enable circuit will be active when pin 16 (FM on) is LOW. If traffic control messages are desired, pin 2 (TC on) must be switched to LOW.

FM radio mode

By operating the DK-toggle switch at pin 1 the DK flip-flop is set. This is displayed by a LED connected to pin 3. In the position "TC off" (pin 2 HIGH) it is not possible to set the DK flip-flop. Non-traffic control stations are muted. If a message is transmitted on the tuned TC station the minimum volume at pin 7 is exposed.

In case of BK-signal failure pin 13 changes to LOW after a delay of about 25 seconds. If pins 13 and 14 are then connected, an intermittent warning signal will be supplied at pin 12. The level increases automatically from -20 dB to 0 dB in 5 steps. (See Fig. 2.)

Cassette mode

If a TC message is delivered when TC is switched on (pin 16 and pin 2 LOW) and the DK flip-flop is set, the motor of the cassette player is stopped and the receiver automatically cuts in. The minimum volume is also set at the same time.

In case of BK-signal failure, the warning tone will be mixed into the cassette playback.

Protection

To avoid faulty switching, an internal latch will be set only if both DK (pin 18) and BK (pin 17) are HIGH. The latch will be reset if DK is changed to LOW independent of BK.

Reset of the DK-toggle flip-flop:

- by operating the DK-toggle (pin 1) twice
- by opening the TC-switch (pin 2)
- by switching to AM reception (pin 16)
- by switching off the cassette-player (pin 15)
- by switching power off or on.

Transmission monitoring

At reception failure of a TC-station BK at pin 17 will become LOW. After about 25 seconds the output (pin 13) will be set LOW to start the warning signal via the jumper between pins 13 and 14. In the meantime the search-tuning can also be started. The warning tone stage gives a graduated signal with a level increasing in five steps from -20 dB to 0 dB in about 20 seconds. The frequency of the warning signal is about 600 Hz; tone period $\approx 80 \text{ ms}$; pause $\approx 560 \text{ ms}$.

If now another TC-message transmitter has been tuned the input BK (pin 17) becomes HIGH and the warning tone is stopped. Also when switching TC-off (pin 2 HIGH) or switching to AM reception (pin 16 HIGH) the warning tone will stop. The BK-signal has to be stable for more than 1 second to reset the just started 25-second-timer.

	inputs pin numbers outputs pin numbers											
mode	16	2	15	1	17	18	7	5	6	8	13	3
AM RADIO	Н	Х	Х	X	Х	x	н	Н	Ľ	Н	Н	Н
FM RADIO TC off	L	Н	X	Х	Х	Х	Н	н	L	Н	Н	Н
FM-TC on station without TC	L	L	х	х	L	х	L	L	Н	L	L*	L
FM-TC on station with BK	L	L	×	н	Н	L	н	н	L	н	н	н
FM-TC on station with BK, DK	L	L	х	Н	Н	н	L	L	Н	н	н	н
FM-TC on station with BK DK-toggle operated MUTE	L	L	x		Н	L	н	н	L	L	н	~
FM-TC on station with BK, DK incoming message	L	L	x	н	н	<u></u>	~	~	_	<i></i>	н	L
FM-TC on station with BK DK-toggle operated twice	 	L,	x	UiU	н	L	н	н	L	ائد	Н	_ <u>_</u>
FM-TC on and cassette station with BK, DK cassette switched on	L	L	<u></u>	×	Н	н	L	L	н	Н	н	Н
FM-TC on and cassette station with BK cassette switched on	L	L	ſ	X	н	L	н	н	L	н	Н	н
FM-TC on and cassette station with BK cassette switched off	L	L	~	x	Н	L	н	н	L	<i></i>	н	ſ
function and state	AM/FM HIGH/LOW	TC on on = LOW	cassette off = H→L	DK toggle active = LOW	BK on=HIGH	DK on = HIGH	min. volume on = LOW	motor stop stop = LOW	motor stop stop = HIGH	MUTE on = LOW	warning tone on = LOW	DK display on = LOW

^{*} After about 25 seconds.

Positive logic:

H = HIGH state (the more positive voltage)

L = LOW state (the less positive voltage)

X = state is immaterial

= positive-going transition= negative-going transition

Functions of the control inputs						
DK toggle operated chatter-proof by internal delay of 10 to 20 ms	pin 1	active	= LOW			
TC (traffic control) released	pin 2	on	= LOW			
Test condition	pin 11	on	= to grou	ınd		
clock rate 24 times faster	,	off	= to Vp	or open		
Start warning signal	pin 14	on	= LOW			
Reset of DK flip-flop by cassette player	pin 15	reset	= HIGH	to LOW tran	nsitio	n
Reset of DK flip-flop						
by tuning AM band	pin 16	reset	= HIGH			
BK input*	pin 17	on	= HIGH			
DK input*	pin 18	on	= HIGH			
Minimum volume	pin 7	on	= LOW			
Motor stop (30 mA)	pin 5	stop	= LOW			
Motor stop (2 mA)	pin 6	stop	= HIGH			
MUTE (volume off)	pin 8	on	= LOW			
Warning tone	pin 13	on	= LOW			
DK display	pin 3	on	= LOW			
RATINGS						
Limiting values in accordance with the Absolute	Maximum Sy	stem (IE	C 134)			
Supply voltage (pin 10)			V_P	max.	16	٧
Input voltages pins 1, 2, 11, 14, 15, 16, 17 and 18			v _i	0 to	o Vp	v
Output voltages pins 3, 5, 6, 8, 13			v _o	max.	23	V
Currents inputs 1, 2, 11, 14, 15, 16, 17 and 18 outputs 6, 8, 13 outputs 3 and 5			1 _i 1 _o	max. max. max.	10	mA mA
Storage temperature			T _{stg}	-55 to +	150	οС
Operating ambient temperature			T _{amb}	-30 to	+ 80	oC

^{*} Open collector output of TC-decoder.

Vp typ. 8,5 V; T _{amb} = 25 °C; unless otherwise specified (see Fig. 1) Supply voltage range Supply current Control inputs	Vp Ip	7,! typ. <	5 to 16 4,5	
Supply current	•	typ.	4,5	
	lp		•	mΑ
Control inputs			U	mΑ
Pins 1, 2, 11, 14, 15, 16, 17 and 18		251	/ += \/-	
Input voltage HIGH	V _{IH}	•	to Vp	٧
Input voltage LOW	V_{IL}	<	2	V
Input current HIGH V _i = 16 V	н	<	1	μΑ
Input current LOW V _i = 0 V	-I _{IL}	25	to 200	μΑ
Input resistance $V_j = 0 V$	R _i	<	10	kΩ
Control outputs				
DK-LED display and motor stop open collector outputs 3 and 5				
Output voltage LOW		typ.	1	٧
$I_{OL} = 20 \text{ mA}$	VOL	<	1,5	٧
Output current LOW	loL	typ.	30	mΑ
Output voltage HIGH (open collector)		_	00	.,
I _{OH} < 10 μA	v_{OH}	<	23	V
LF-MUTE, motor stop and warning signal start pins 8, 6 and 13				
Output voltage LOW			0.25	17
$I_{OL} = 1 \text{ mA}$	V _{OL}	<	0,35	
Output current LOW	lOL	=	2	mΑ
Output voltage HIGH (open collector) $I_{OH} < 1 \mu A$	V _{OH}	=	16	V
Minimum voltage (pin 7) (R _S = 800 Ω , R _L = ∞)	- 011		-	
Output voltage LOW				
(for volume HIGH)	V ₇₋₄	<	0,1	V
Output voltage HIGH (for volume LOW)	V ₇₋₄	typ.	5	v
not totalic Lotty	- /-4	-76-	-	
Warning signal (pin 12)				
f = 600 Hz; square-wave pulsed; R _S = 300 Ω			00	
Switching time on	ton	typ.		ms
Switching time off	^t ogf	typ.	560	ms
Output voltage during ton	V45.4	tun	4,3	V
at maximum peak (R _L = 1 kΩ) during T _{Off}	V ₁₂₋₄ V ₁₂₋₄	typ. typ.		v

Automatic level control			
Duration per level	t _p	typ.	5 s
Output level swing (in 5 steps)	ΔV ₁₂₋₄	-	-20 to 0 dB
Output current peak value	^{−l} 12M	typ.	6 mA
Oscillator (pin 9)			
Frequency	* f	typ.	2400 Hz
Filter resistance	$R_{\mathbf{o}}$	typ.	100 k Ω
Filter capacitance	Co	typ.	4,7 nF
Oscillator frequency tolerance	Δf/f	-10	to + 10 %