TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

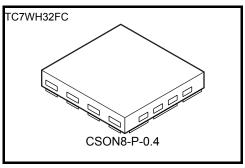
TC7WH32FC

Dual 2-Input OR Gate

Features

High-speed :t_{pd} = 3.8 ns (Typ.) at V_{CC} = 5 V
 Low power dissipation :I_{CC}=2μA(Max.) at Ta=25°C
 High noise immunity :V_{NIH}=V_{NIL}=28%V_{CC}(Min.)
 Operation voltage range :V_{CC}(opr.)=2~5.5V

• 5.5-V Tolerant inputs.



Weight: 0.002g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Ratingh	Unit
Power supply viltage	V _{CC}	-0.5~7.0	V
DC input voltage	V_{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note1)	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20 (Note2)	mA
DC output current	lout	±25	mA
DC V _{CC} /GND current	Icc	±50	mA
Power dissipation	PD	150 (Note3)	mW
Storage temperature	T _{stg}	-65~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: High or Low State.

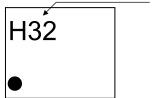
I_{OUT} absolute maximum rating must be observed.

Note2 : V_{OUT} < GND , V_{OUT} > V_{CC} Note3 : Mounted on an FR4 board.

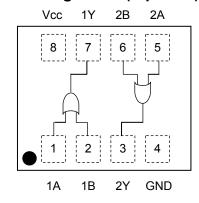
 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$

Marking

Product name



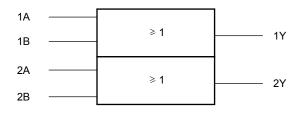
Pin Assignment (top view)



Truth Table

Inp	Outputs	
Α	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

IEC Logic Diagram





Operating Ranges

Characteristics	Symbol	Rathing	Unit	
Power supply voltage	V _{CC}	2.0~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	$0 \sim 100 \ (V_{CC} = 3.3 \ V \pm 0.3 \ V)$	ns/V	
imput rise and fail time	avav	0~20 (V _{CC} = 5 V ± 0.5 V)	113/V	

DC Electrical Characteristics

Characteristic Symbo		Test condition			Ta = 25°C			Ta = -40~85°C		unit
				V _{CC} (V)	Min.	Тур.	Max.	Min.	Max.	unit
				2.0	1.5	_	_	1.5	_	
High-level input voltage V _{IH}			_		V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
				2.0	_	_	0.5	_	0.5	V
Low-level input voltage	V _{IL}		_		_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	
		V _{IN} = V _{IL} or V _{IH}	I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9		V
				3.0	2.9	3.0	_	2.9	_	
high-level output voltage	V _{OH}			4.5	4.4	4.5	_	4.4	_	
			I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
			I _{OH} = -8 mA	4.5	3.94	_	_	3.80	_	
Low-level output voltage		VIN = VIL	I _{OL} = 50 μA	2.0	_	0.0	0.1	_	0.1	
				3.0	_	0.0	0.1	_	0.1	
	V _{OL}			4.5	_	0.0	0.1	_	0.1	
			I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5	V _{IN} = 5.5 V or GND		_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5			2.0	_	20.0	μА

AC Electrical Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristic	Symbol		Test condition		Ta = 25°C			Ta = -40~85°C		- Unit
	Syllibol		V _{CC} (V)	C _{L (} pF)	Min.	Тур.	Max.	Min.	Max.	Offic
Propagation delay time			3.3 ± 0.3	15	_	5.5	7.9	1.0	9.5	- ns
	t _{pLH}			50	_	8.0	11.4	1.0	13.0	
	t _{pHL}		5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
				50	_	5.3	7.5	1.0	8.5	
Input capacitance	C _{IN}		_		_	4	10	_	10	pF
Power dissipation capacitanse	C _{PD}		(Note 4)		_	14	_		_	pF

Note 4 : C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

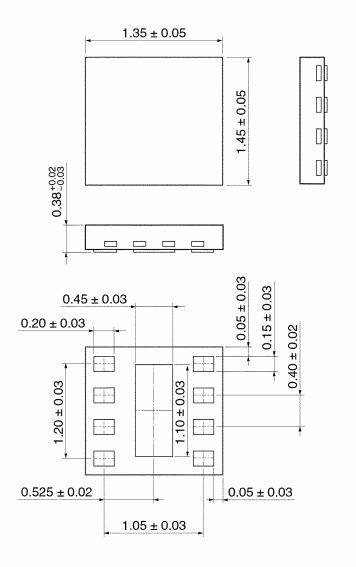
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

3 2007-11-01



Package Dimensions

CSON8-P-0.4 Unit: mm



Weight: 0.002 g (Typ.)

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20070701-EN GENERAL

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