TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG14FU,TC7WG14FK

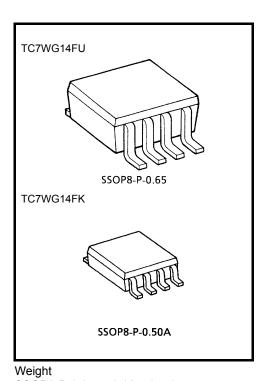
Triple Schmitt Inverter

Features

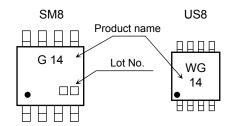
- High-level output current: I_{OH}/I_{OL} = ±8 mA (min) at V_{CC} = 3 V
- High-speed operation: t_{pd} = 4.0 ns (typ.)

at V_{CC} = 3.3 V,15pF

- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.5-V tolerant inputs
- 3.6-V power down protection outputs



Marking

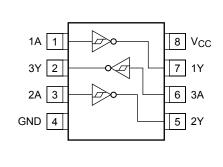


Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	VIN	-0.5~7.0	V	
DC output voltage	Vour	-0.5~ 4.6 (Note 1)	v	
DC output voltage	VOUT	-0.5~V _{CC} + 0.5 (Note 2)	v	
Input diode current	IIК	-20	mA	
Output diode current	IOK	-20 (Note 3)	mA	
DC output current	IOUT	±25	mA	
DC V _{CC} / ground current	Icc	±50	mA	
Power dissipation	PD	300 (SM8) 200 (US8)	mW	
Storage temperature	T _{stg}	-65~150	°C	

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

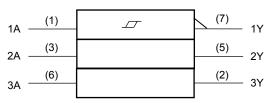
Note 1: $V_{CC} = 0V$

Note 2: High or Low State. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

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IEC Logic Symbol



А	Y	
L	Н	
н	1	

Truth Table

Operating Ranges

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	0.9~3.6	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	Vout	0~3.6 (Note 4)	V	
	VOUT	0~V _{CC} (Note 5)	v	
Output Current		±8.0 (Note 6)		
	I _{OH} /I _{OL}	±4.0 (Note 7)		
		±3.0 (Note 8)	mA	
		±1.7 (Note 9)	IIIA	
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T _{opr}	-40~85	°C	

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \sim 1.6 \text{ V}$

Note 10: V_{CC} = 1.1~1.3 V

Note 11: $V_{CC} = 0.9 V$

Electrical Characteristics

DC Electrical Characteristics

Characteristics S		Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit	
		Symbol	1630	Condition	$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit
					0.9	_	_	0.73		0.80	
High level					1.1			0.86		0.93	
	VP	_		1.4			1.07		1.12		
				1.65	_		1.23		1.25		
				2.3	_	_	1.66	_	1.68		
Threshold voltage					3.0	_	_	2.14	_	2.15	.,
Threshold voltage					0.9	0.18	_	_	0.07	_	V
					1.1	0.26	_	_	0.18	_	
					1.4	0.36	_	_	0.31	_	
	Low level	V _N		—	1.65	0.45	_	_	0.41	_	
					2.3	0.69		—	0.64		
					3.0	0.96			0.91		
					0.9	0.20		0.38	0.15	0.53	
					1.1	0.25	_	0.41	0.21	0.53	
Hysteresis voltage		Max			1.4	0.35		0.48	0.34	0.57	V
Hysteresis voltage		V _H			1.65	0.42	_	0.56	0.40	0.60	V
					2.3	0.60		0.74	0.61	0.76	
					3.0	0.79		0.93	0.80	0.94	
			VIN = VIL	I _{OH} =–0.02 mA	0.9	0.75			0.75	_	
				I _{OH} = -0.3 mA	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
	High level	evel V _{OH}		I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
				I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45		
				I _{OH} = -4.0 mA	2.3~2.7	2.0		_	2.0		
Output voltage				I _{OH} = -8.0 mA	3.0~3.6	2.48		_	2.48		V
Output voltage		.ow level V _{OL}	VIN = VIH	I _{OL} = 0.02 mA	0.9	_		0.1		0.1	
				I _{OL} = 0.3 mA	1.1~1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
Low leve	Low level			I _{OL} = 1.7 mA	1.4~1.6	—	—	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	—	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
				I _{OL} = 3.0 mA	1.65~ 1.95	—	_	0.45	_	0.45	
				I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage curre	Input leakage current I _{IN} V _{IN} = 0~5.5V		0~3.6	—	_	±0.1	—	±1.0	μA		
Power off leakage current I_{OFF} $V_{IN} = 0 \sim 5.5 V_{OUT} = 0 \sim 3.6 V$		-5V -3.6V	0	_	_	1.0	_	10.0	μΑ		
Quiescent supply currentI CCV INV CCOr GND		3.6	_		1.0		10.0	μA			

AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40~85°C		Unit
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		C _L = 10 pF,	0.9	_	41.3			_	ns
			1.1~1.3	_	18.0	25.4	1.0	40.8	
			1.4~1.6		9.5	12.2	1.0	13.5	
		$R_L = 1 M\Omega$	1.65~ 1.95		7.0	8.7	1.0	9.3	
			2.3~2.7		4.7	5.7	1.0	6.2	
Propagation delay time			3.0~3.6		3.7	4.5	1.0	4.7	
	^t рLН tрНL		0.9		44.4				
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.1~1.3		19.3	27.7	1.0	46.9	
			1.4~1.6		10.2	13.1	1.0	14.7	
			1.65~ 1.95		7.5	9.3	1.0	9.9	
			2.3~2.7		5.0	5.9	1.0	6.4	
			3.0~3.6		4.0	4.8	1.0	5.2	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		55.8		_	_	
			1.1~1.3	_	24.7	36.3	1.0	59.6	
			1.4~1.6	_	12.9	16.8	1.0	19.2	
			1.65~ 1.95	_	9.2	11.5	1.0	12.9	
			2.3~2.7		5.9	7.1	1.0	8.3	
			3.0~3.6		4.9	5.7	1.0	6.6	
Input capacitance	C _{IN}	—	3.6	_	3	_		_	pF
Power dissipation capacitance	C _{PD}	(Note 12)	0.9 ~ 3.6		11				pF

Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

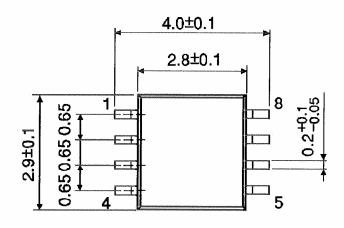
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

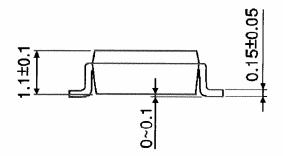
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Package Dimensions

SSOP8-P-0.65

Unit : mm





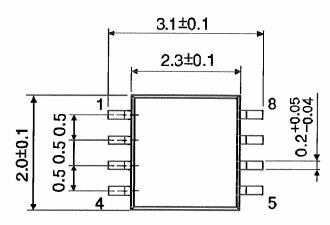
Weight: 0.02 g (typ.)

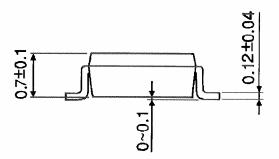
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Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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