TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SZ08AFE

2 Input AND Gate

#### Features

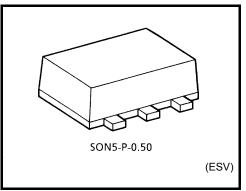
High output drive: ±24 mA (min)

at V<sub>CC</sub> = 3 V

Super high speed operation: t<sub>pd</sub> 2.7 ns (typ.)

at  $V_{CC} = 5 V, 50 pF$ 

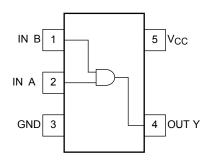
- Operation voltage range: V<sub>CC (opr.)</sub> = 1.8~5.5 V
- Supply voltage data retention: V<sub>CC</sub> = 1.5~5.5 V
- 5.5-V tolerant inputs
- Matches the performance of TC74LCX series when operated at 3.3 -V  $\mathsf{V}_{\mathsf{CC}}$



Weight: 0.003 g (typ.)

#### Marking

# R 2<sup>k</sup>



Pin Assignment (top view)

## Absolute Maximum Ratings (Ta = 25°C)

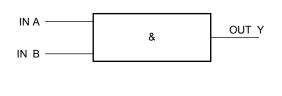
Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5~6	V
DC input voltage	V <sub>IN</sub>	-0.5~6	V
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5	V
Input diode current	IIК	-20	mA
Output diode current	I <sub>OK</sub>	±20	mA
DC output current	IOUT	±50	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C
Lead temperature (10 s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# <u>TOSHIBA</u>

# Logic Diagram



А	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Truth Table

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	1.8~5.5	V
Supply voltage	VCC	1.5~5.5 (Note 1)	
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
		0~20 (V_{CC} = 1.8 V, 2.5 V $\pm$ 0.2 V)	ns/V
Input rise and fall time	d <sub>t</sub> /d <sub>v</sub>	0~10 (V_{CC} = 3.3 V $\pm$ 0.3 V)	
		0~5 (V_{CC} = 5.5 V $\pm$ 0.5 V)	

Note 1: Data retention only

# **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40~85°C		Linit	
				V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
High-level input			_		0.75 × V <sub>CC</sub>	_	_	$0.75 \times V_{CC}$	_	V
voltage		$0.7 \times V_{CC}$				_	$0.7 \times V_{CC}$	_		
Low-level input voltage			1.8	_		$0.25 \times V_{CC}$		$\begin{array}{c} 0.25 \\ \times \ V_{CC} \end{array}$	V	
			2.3-5.5	_		$0.3 \\ \times V_{CC}$		$0.3 \\ \times V_{CC}$	V	
				1.8	1.7	1.8	—	1.7	—	V
			100 ··· A	2.3	2.2	2.3	_	2.2	_	
			I <sub>OH</sub> = −100 μA	3.0	2.9	3.0	_	2.9	_	
High-level	Mari			4.5	4.4	4.5	_	4.4	_	
output voltage	∨он	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OH</sub> = -8 mA	2.3	1.9	2.15	_	1.9	_	
			$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
		I <sub>OH</sub> = -24 mA	3.0	2.3	2.68	_	2.3	_		
			I <sub>OH</sub> = -32 mA	4.5	3.8	4.2	_	3.8	_	
			I <sub>OL</sub> = 100 μA	1.8	_	0	0.1		0.1	-
				2.3		0	0.1	_	0.1	
				3.0		0	0.1	_	0.1	
Low-level output voltage	V <sub>IN</sub> = V <sub>IH</sub>		4.5		0	0.1	_	0.1		
	VOL	or V <sub>IL</sub>	I <sub>OL</sub> = 8 mA	2.3		0.1	0.3	_	0.3	V
			I <sub>OL</sub> = 16 mA	3.0		0.15	0.4	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0		0.22	0.55	_	0.55	
			I <sub>OL</sub> = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I <sub>IN</sub>	$V_{IN} = 5.5 \text{ V or GND}$		0-5.5	_	_	±1	_	±10	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND		_		2	_	20	μA

# AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics Symbol		Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit	
Propagation delay <sup>t</sup> pLH time t <sub>pHL</sub>		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.8	2.0	5.2	9.5	2.0	10.5	ns
			$2.5\pm0.2$	0.8	3.4	7.0	0.8	7.5	
	t <sub>pLH</sub>		$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.6	4.7	0.5	5.0	
	tpHL		$5.0\pm0.5$	0.5	2.2	4.1	0.5	4.4	
		C <sub>L</sub> = 50 pF,	$\textbf{3.3}\pm\textbf{0.3}$	1.5	3.3	5.2	1.5	5.5	
		$R_{L} = 500 \Omega$	$5.0\pm0.5$	0.8	2.7	4.5	0.8	4.8	
Input capacitance	C <sub>IN</sub>	—	0-5.5	_	4			_	pF
Power dissipation C <sub>PD</sub>	C	(Note 2)	3.3		19			_	pF
	ФРD		5.5	_	26	_		_	

Note2: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

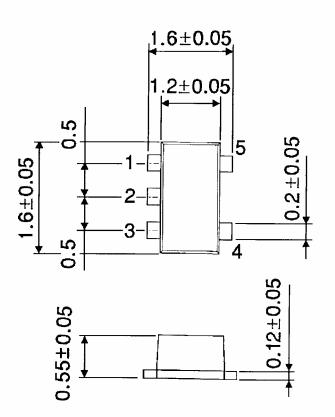
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# **TOSHIBA**

# Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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20070701-EN GENERAL

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