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TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX02F,TC74LCX02FN,TC74LCX02FT

Low-Voltage Quad 2-Input NOR Gate with 5-V Tolerant Inputs and Outputs

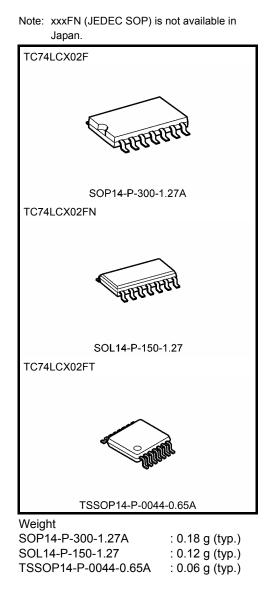
The TC74LCX02 is a high-performance CMOS 2-input NOR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

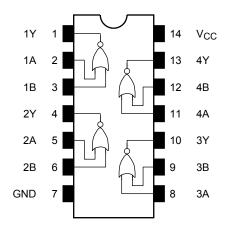
All inputs are equipped with protection circuits against static discharge.

Features

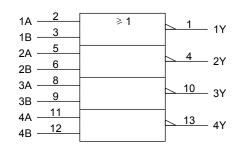
- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation: $t_{pd} = 5.2 \text{ ns} (\text{max}) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\text{min}) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 02 type



Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs	
А	В	Y	
L	L	Н	
L	Н	L	
Н	L	L	
Н	Н	L	

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5 to 7.0	V	
DC input voltage	V _{IN}	-0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)		
DC output voltage	Vout	-0.5 to V _{CC} + 0.5 (Note 3)	V	
Input diode current	Iк	-50	mA	
Output diode current	I _{OK}	±50 (Note 4)	mA	
DC output current	IOUT	±50	mA	
Power dissipation	PD	180	mW	
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA	
Storage temperature	T _{stg}	–65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 2: $V_{CC} = 0 V$
- Note 3: High or low state. $I_{\mbox{OUT}}$ absolute maximum rating must be observed.
- Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	2.0 to 3.6	V	
Power supply voltage	VCC	1.5 to 3.6 (Note 2)	v	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 3)	V	
Output voltage		0 to V_{CC} (Note 4)	v	
Output current	Іон/Іог	±24 (Note 5)	mA	
Output current	'OH/'OL	±12 (Note 6)	IIIA	
Operating temperature	T _{opr}	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

- Note 2: Data retention only
- Note 3: $V_{CC} = 0 V$
- Note 4: High or low state
- Note 5: $V_{CC} = 3.0$ to 3.6 V
- Note 6: $V_{CC} = 2.7$ to 3.0 V
- Note 7: $V_{IN}=0.8$ to 2.0 V, $V_{CC}=3.0$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol	Test Condition V _{CC} (V)		Min	Max	Unit	
		- ,			V _{CC} (V)		max	onit
H-level	VIH	—		2.7 to 3.6	2.0	—	V	
Input voltage	L-level	VIL			2.7 to 3.6	_	0.8	v
			$V_{IN} = V_{IL}$	I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_	V
	H-level	V _{OH}		I _{OH} = -12 mA	2.7	2.2	_	
		-		I _{OH} = -18 mA	3.0	2.4	_	
Output voltage				I _{OH} = -24 mA	3.0	2.2	_	
	L-level V _{OL}		I _{OL} = 100 μA	2.7 to 3.6	_	0.2		
		Ve	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 12 mA	2.7	_	0.4	
		VOL		I _{OL} = 16 mA	3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55		
Input leakage current		I _{IN}	$V_{IN} = 0$ to 5.5 V		2.7 to 3.6	_	±5.0	μA
Power-off leakage cur	rent	IOFF	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μA
Quieseent augusty aurrent		ICC	$V_{IN} = V_{CC} \text{ or } GND$		2.7 to 3.6		10.0	
Quiescent supply current	$V_{IN} = 3.6 \text{ to } 5.5 \text{ V}$			2.7 to 3.6	_	±10.0	μA	
Increase in Icc per input		Δlcc	$V_{IH} = V_{CC} - 0.6 \ V$		2.7 to 3.6		500	

AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7	_	6.0	ns
	t _{pHL}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	5.2	
Output to output skew	t _{osLH}	(Note)	2.7	_	_	
	t _{osHL}		$\textbf{3.3}\pm\textbf{0.3}$	_	1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5 \text{ ns}$, $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V_{OL}	V _{OLP}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_	3.3	7	pF
Output capacitance	C _{OUT}	_	0	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note) 3.3	25	pF

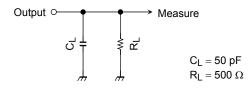
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 4$ (per gate)

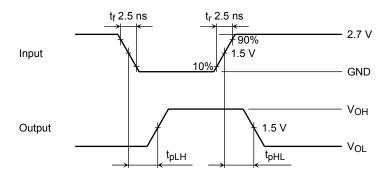
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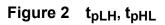
AC Test Circuit





AC Waveform



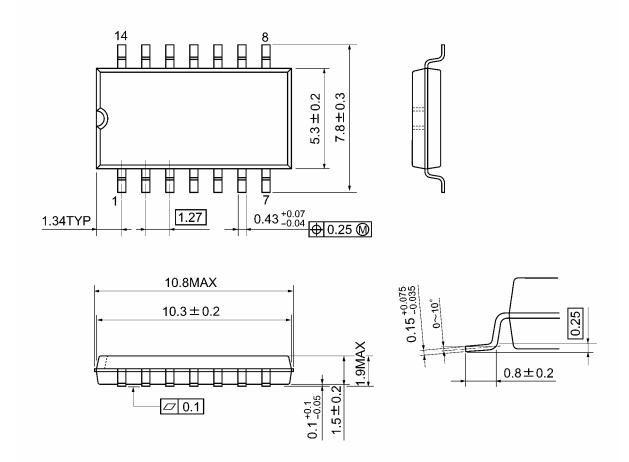




Package Dimensions

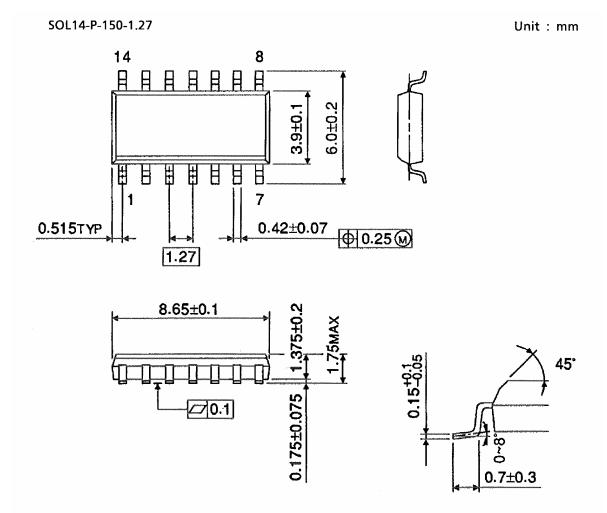
SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



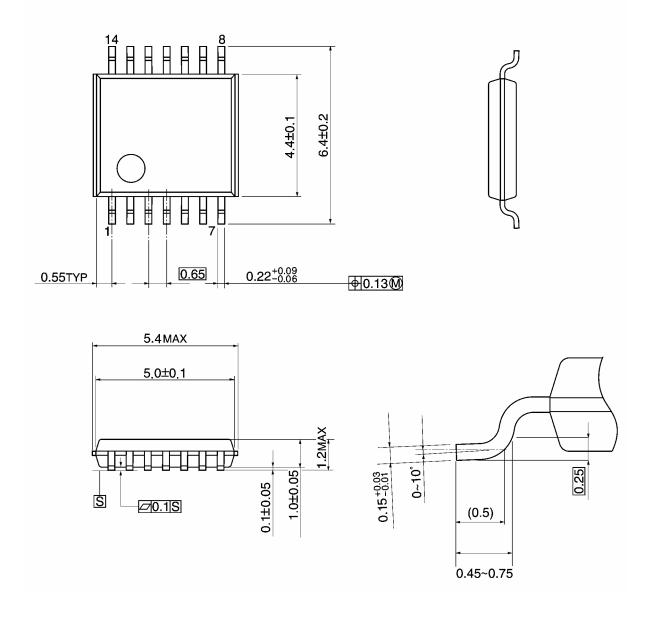
Note: This package is not available in japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

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20070701-EN GENERAL

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