

Accutek Microcircuit Corporation

AK63232Z
32,768 x 32 Bit CMOS / BiCMOS
Static Random Access Memory

DESCRIPTION

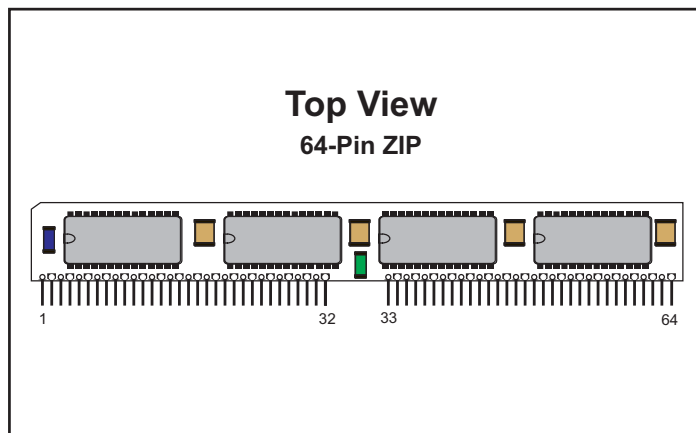
The Accutek AK63232Z SRAM Module consists of fast high performance SRAMs mounted on a low profile, 64 pin ZIP Board. The module utilizes four 28 pin 32K x 8 SRAMs in 300 mil SOJ packages and four decoupling capacitors mounted on the front side of a printed circuit board.

The SRAMs used have common I/O functions and single output enable functions. Also, four separate chip select (CE) connections are used to independently enable the four bytes. The modules can be supplied in a variety of access time values from 8 nSEC to 45 nSEC in CMOS or BiCMOS technology.

The Accutek module is designed to have a maximum seated height of 0.500 inch. The module conforms to JEDEC-standard sizes and pin-out configurations. This, along with use of two pins for module memory density identification, PD₀ and PD₁, minimizes interchangeability and design considerations when changing from one module size to the other in customer applications.

FEATURES

- 32,768 x 32 bit organization
- JEDEC standard 64 pin ZIP format
- Common I/O, single $\overline{OE}$ functions with four separate chip selects (CE)
- Low height, 0.500 inch maximum seated height



- Upward compatible with 64K x 32 (AK63264), 256K x 32 (AK632256), 512K x 32 (AK632512) and 1 Meg x 32 (AK6321024)
- Presence Detect, PD₀ and PD₁ for identifying module density
- Fast Access Times range from 8 nSEC BiCMOS to 45 nSEC CMOS
- TTL compatible inputs and outputs
- Single 5 volt power supply - AK63232Z
- Single 3.3 volt power supply - AK63232Z/3.3
- Operating temperature range in free air, 0°C to 70°C

PIN NOMENCLATURE

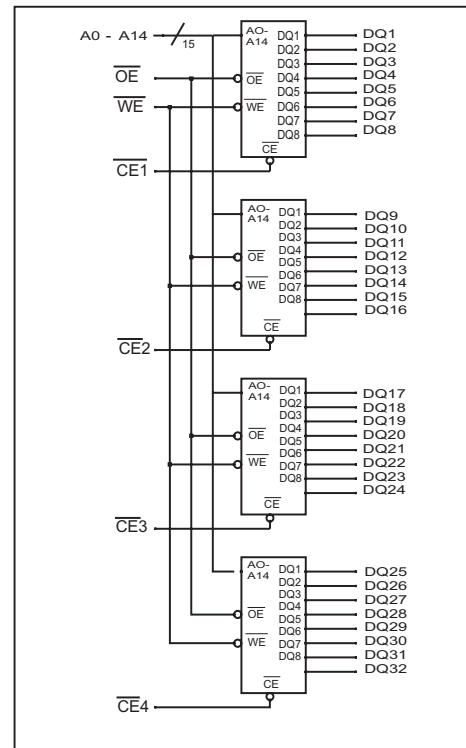
A ₀ - A ₁₄	Address Inputs
$\overline{CE}_1$ - $\overline{CE}_4$	Chip Enable
DQ ₁ - DQ ₃₂	Data In/Data Out
$\overline{OE}$	Output Enable
PD ₀ - PD ₁	Presence Detect
V _{cc}	Power Supply
V _{ss}	Ground
$\overline{WE}$	Write Enable

PIN ASSIGNMENT

PIN #	SYMBOL	PIN #	SYMBOL	PIN #	SYMBOL	PIN #	SYMBOL
1	V _{ss}	17	A ₂	33	CE ₄	49	A ₄
2	PD ₀	18	A ₉	34	CE ₃	50	A ₁₁
3	PD ₁	19	DQ ₁₃	35	NC	51	A ₅
4	DQ ₁	20	DQ ₅	36	NC	52	A ₁₂
5	DQ ₉	21	DQ ₁₄	37	OE	53	V _{cc}
6	DQ ₂	22	DQ ₆	38	V _{ss}	54	A ₁₃
7	DQ ₁₀	23	DQ ₁₅	39	DQ ₂₅	55	A ₆
8	DQ ₃	24	DQ ₇	40	DQ ₁₇	56	DQ ₂₁
9	DQ ₁₁	25	DQ ₁₆	41	DQ ₂₆	57	DQ ₂₉
10	DQ ₄	26	DQ ₈	42	DQ ₁₈	58	DQ ₂₂
11	DQ ₁₂	27	V _{ss}	43	DQ ₂₇	59	DQ ₃₀
12	V _{cc}	28	WE	44	DQ ₁₉	60	DQ ₂₃
13	A ₀	29	NC	45	DQ ₂₈	61	DQ ₃₁
14	A ₇	30	A ₁₄	46	DQ ₂₀	62	DQ ₂₄
15	A ₁	31	CE ₂	47	A ₃	63	DQ ₃₂
16	A ₈	32	CE ₁	48	A ₁₀	64	V _{ss}

PD₀ = Open
PD₁ = Open

FUNCTIONAL DIAGRAM



MODULE OPTIONS

Leaded ZIP: AK63232Z

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