New Jersey Semi-Conductor Products, Inc.

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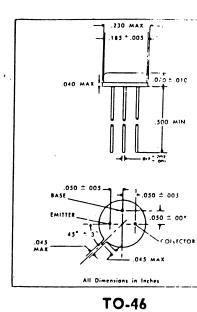
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The 2N 3218 is designed for low level chopper applications and embody the most advanced semiconductor technology, combining epitaxial junction growth with diffusion and oxide passivation techniques. This transistor exhibit the high voltage capabilities of alloy junction devices with the ruggedness, stability, and reliability of the planar process. The bed mounted construction utilizes a unique gold bonding technique which eliminates "purple plague". In addition, leakage current and offset voltage are exceptionally low. The inherent parameter stability of this passivated transistor allows standard matching of offset voltage to 50 microvolts over the range from —25 to +100° C.

ELECTRICAL DATA ABSOLUTE MAXIMUM RATINGS

	2N3218	
Collector To Emitter Voltage (BVceo)	—20	Volts
Collector To Base Voltage (BVcbo)	25	Volts
Emitter To Base Voltage (BVebo)	25	Volts
Collector Current (Ic)	100	mA
Total Power Dissipation (free air)	400	mW
Total Power Dissipation (infinite heat sink)	2	Watts
Storage Temp (max)	200	°C
Operation Temp (max)	200	°C
Lead Temp (@ 1/16 ± 1/32 from case)	240	10 sec
Derating Factor (D.)	2 3	mW/C°



ELECTRICAL CHARACTERISTICS TA 25°C (UNLESS OTHERWISE STATED)

Symbol Parame		Conditions	2N3218			
	Parameter		Min.	Тур.	Max.	Units
lcio	Collector Leakage	At Max Rated Voltage	_	0.1	1.0	пA
leso	Emitter Leakage	At Max Rated , Voltage		0.1	1.0	nA
Iceo	Collector Leakage at 100°C	At Max Rated Voltage	_	0.01	0.1	μА
, I ₁₁₀	Emitter Leakage at 100°C	At Max Rated Voltage		0.01	0.1	μΑ
۷o	Offset Voltage	I _θ = 200μΑ I _ε = 0		1.0	2.0	m۷
hr.	High Frequency Current Gain	f = IMC V _{CE} = -6V I _{CE} = ImA	_	5	-	
Faat	Inverted Dynamic Saturation Resistance	l₁ = 0.1mA l₃ = 1mA	_	25	50	ohms
Соь	Collector To Base Capacitance	V _{CB} = -6V Ic = ImA	_	_	14	pfd
Ceb	Emitter To Base Capacitance	$V_{\epsilon 0} = -6V$ $I_{\epsilon} = 0$	_		8	pfd

