

RL201 thru RL207

MINIATURE PLASTIC SILICON RECTIFIER



**CHENG-YI
ELECTRONIC**



FEATURE

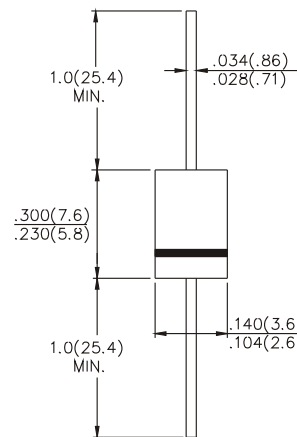
- Plastic package has underwriters laboratory Flammability classification 94V-O utilizing Flame retardant epoxy molding compound.
- 2.0 ampere operation at $T_A=55^{\circ}\text{C}$ with no thermal runaway
- Exceeds environmental standard of MIL-STD-19500/228

MECHANICAL DATA

- Case:Molded plastic ,DO-15
- Terminals:axial leads ,solderable per MIL-STD-202, Method 208
- Polarity:Color band denotes cathode
- Mounting position:Any
- Weight:0.015 ounce, 0.4 gram

VOLTAGE-50 TO 1000 Volts
CURRENT -2.0 Amperes

DO-15



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless specified. Resistive or inductive load, 60Hz.
For capacitance load derate current by 20%.

	RL201	RL202	RL203	RL204	RL205	RL206	RL207	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current .375", 9.5mm Lead Length at $T_A = 60^{\circ}\text{C}$	2.0							A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rted load (JEDEC method)	70							A
Maximum Forward Voltage at 2.0A	1.1							V
Maximum Reverse Current $T_A = 25^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_A = 100^{\circ}\text{C}$	5.0 500							μA μA
Typical Junction Capacitance See (Note 1)	40							pF
Typical Thermal Resistance (Note 2) R θ JA	25							$^{\circ}\text{C} / \text{W}$
Operating and Storage Temperature Range, T_i , T_{STG}	-55 to +150							$^{\circ}\text{C}$

Notes : 1. Measured at 1 MHz and applied reverse voltage of 4.0 VDC.

2. Thermal resistance from junction to ambient and from junction to lead at 0.375" (9.5mm) lead length P.C.B. mounted.

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RATING AND CHARACTERISTICS CURVES RL201 THRU RL207

Fig. 1 - TYPICAL FORWARD CHARACTERISTICS

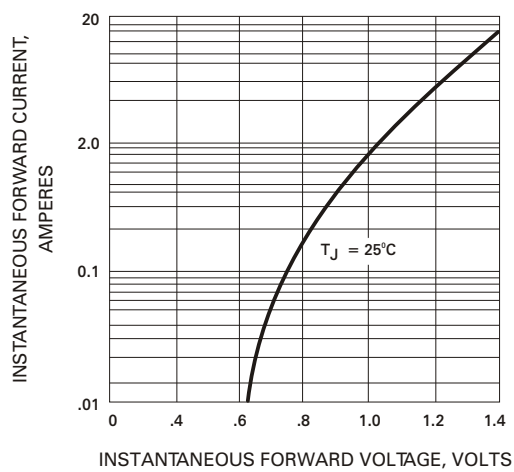


Fig. 2 - PEAK FORWARD SURGE CURRENT

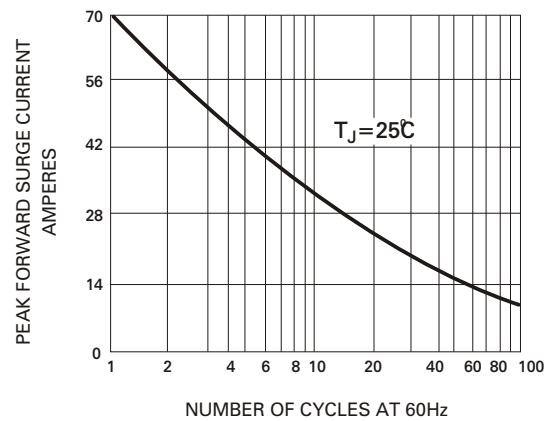


Fig. 3 - FORWARD CURRENT DERATING CURVE

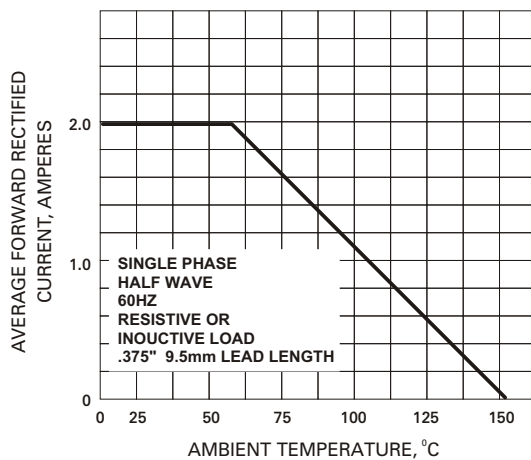


Fig. 4 - TYPICAL JUNCTION CAPACITANCE

