

Features

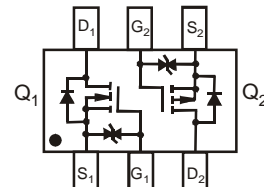
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- **Lead Free/RoHS Compliant (Note 2)**
- **ESD Protected Up to 2.5kV**
- **"Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe
(Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 7
- Ordering Information: See Page 7
- Weight: 0.006 grams (approximate)



TOP VIEW



TOP VIEW

Internal Schematic

Maximum Ratings N-CHANNEL – Q₁ @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±6	V
Drain Current (Note 1)	Steady State	T _A = 25°C T _A = 85°C	I _D	1066 690	mA

Maximum Ratings P-CHANNEL – Q₂ @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±6	V
Drain Current (Note 1)	Steady State	T _A = 25°C T _A = 85°C	I _D	845 548	mA

Thermal Characteristics – Total Device @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	P _D	330	mW
Thermal Resistance, Junction to Ambient (Note 1)	R _{θJA}	379	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

- Notes:
1. Device mounted on FR-4 PCB with minimum recommended pad layout.
 2. No purposefully added lead.
 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

Electrical Characteristics N-CHANNEL – Q₁ @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	100	nA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±1.0	μA	V _{GS} = ±4.5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V _{GS(th)}	0.5	-	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	0.3	0.45	Ω	V _{GS} = 4.5V, I _D = 600mA
			0.4	0.6		V _{GS} = 2.5V, I _D = 500mA
			0.5	0.75		V _{GS} = 1.8V, I _D = 350mA
Forward Transfer Admittance	Y _{fs}	-	1.4	-	S	V _{DS} = 10V, I _D = 400mA
Diode Forward Voltage (Note 4)	V _{SD}		0.7	1.2	V	V _{GS} = 0V, I _S = 150mA
DYNAMIC CHARACTERISTICS (Note 5)						
Input Capacitance	C _{iss}	-	60.67	-	pF	V _{DS} = 16V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	9.68	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	5.37	-	pF	
Total Gate Charge	Q _g	-	736.6	-	pC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	-	93.6	-	pC	
Gate-Drain Charge	Q _{gd}	-	116.6	-	pC	
Turn-On Delay Time	t _{D(on)}	-	5.1	-	ns	V _{DD} = 10V, V _{GS} = 4.5V, R _L = 47Ω, R _G = 10Ω
Turn-On Rise Time	t _r	-	7.4	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	26.7	-	ns	
Turn-Off Fall Time	t _f	-	12.3	-	ns	

Electrical Characteristics P-CHANNEL – Q₂ @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	-	-	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	-100	nA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±2.0	μA	V _{GS} = ±4.5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V _{GS(th)}	-0.5	-	-1.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	0.5	0.75	Ω	V _{GS} = -4.5V, I _D = -430mA
			0.7	1.05		V _{GS} = -2.5V, I _D = -300mA
			1.0	1.5		V _{GS} = -1.8V, I _D = -150mA
Forward Transfer Admittance	Y _{fs}	-	0.9	-	S	V _{DS} = -10V, I _D = -250mA
Diode Forward Voltage (Note 4)	V _{SD}		-0.8	-1.2	V	V _{GS} = 0V, I _S = -150mA
DYNAMIC CHARACTERISTICS (Note 5)						
Input Capacitance	C _{iss}	-	59.76	-	pF	V _{DS} = -16V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	12.07	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	6.36	-	pF	
Total Gate Charge	Q _g	-	622.4	-	pC	V _{GS} = -4.5V, V _{DS} = -10V, I _D = -250mA
Gate-Source Charge	Q _{gs}	-	100.3	-	pC	
Gate-Drain Charge	Q _{gd}	-	132.2	-	pC	
Turn-On Delay Time	t _{D(on)}	-	5.1	-	ns	V _{DD} = -10V, V _{GS} = -4.5V, R _L = 47Ω, R _G = 10Ω
Turn-On Rise Time	t _r	-	8.1	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	28.4	-	ns	
Turn-Off Fall Time	t _f	-	20.7	-	ns	

Notes: 4. Short duration pulse test used to minimize self-heating effect.
5. Guaranteed by design. Not subject to production testing.

N-CHANNEL – Q₁

NEW PRODUCT

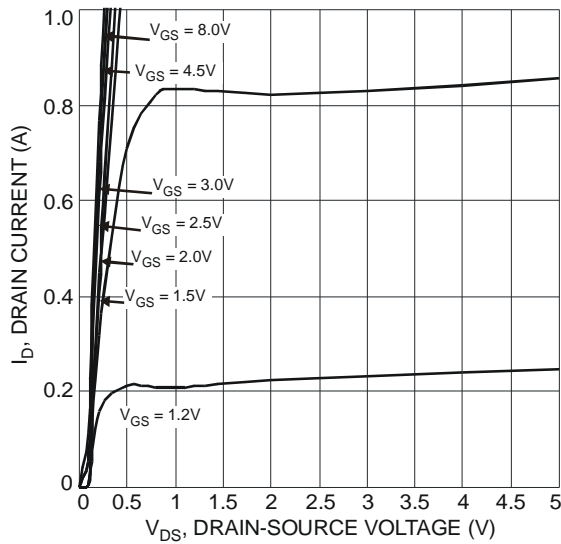


Fig. 1 Typical Output Characteristic

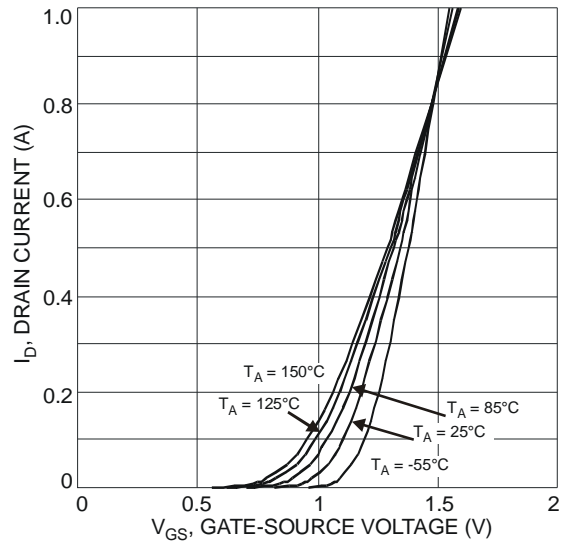


Fig. 2 Typical Transfer Characteristic

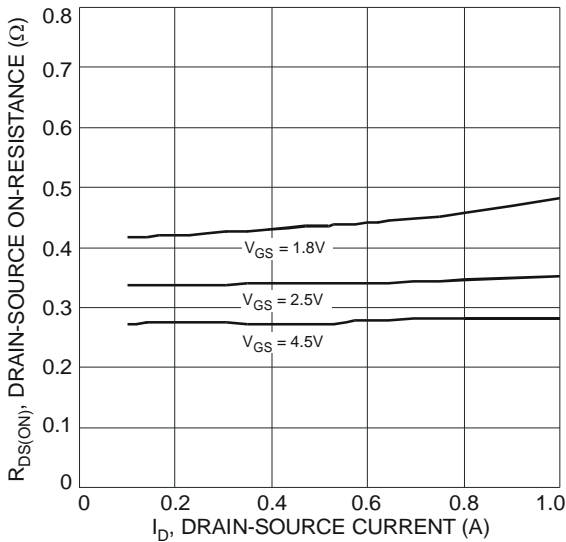


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

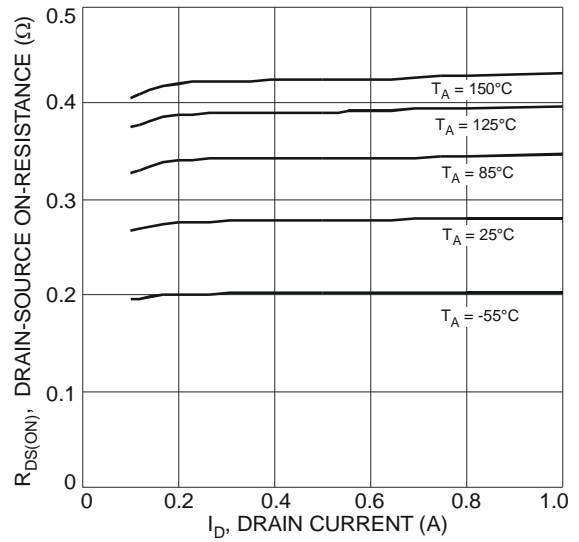


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

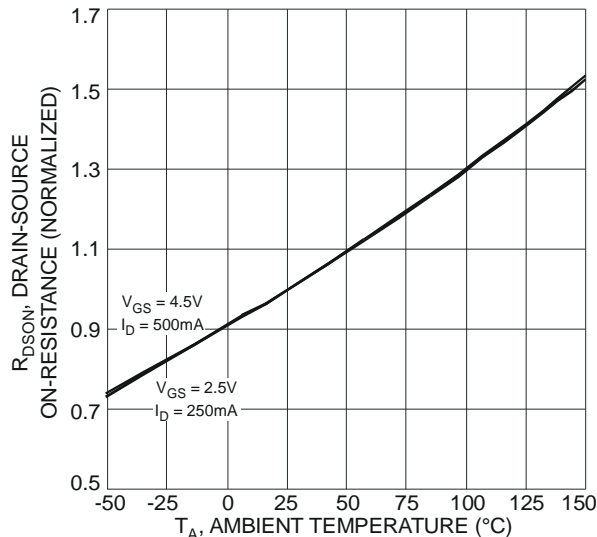


Fig. 5 On-Resistance Variation with Temperature

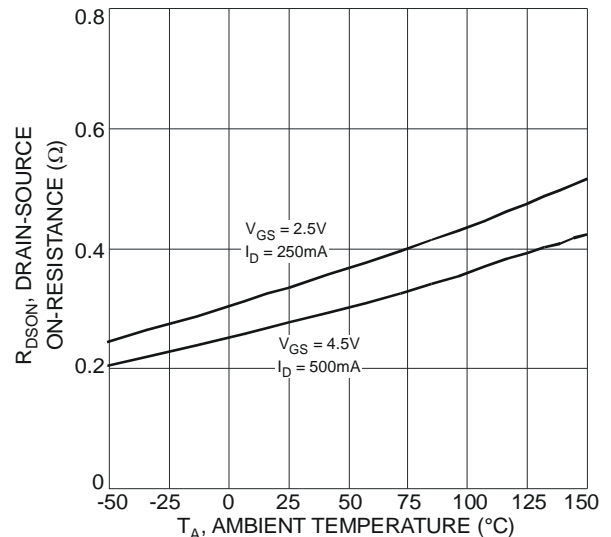


Fig. 6 On-Resistance Variation with Temperature

N-CHANNEL – Q₁ (continued)

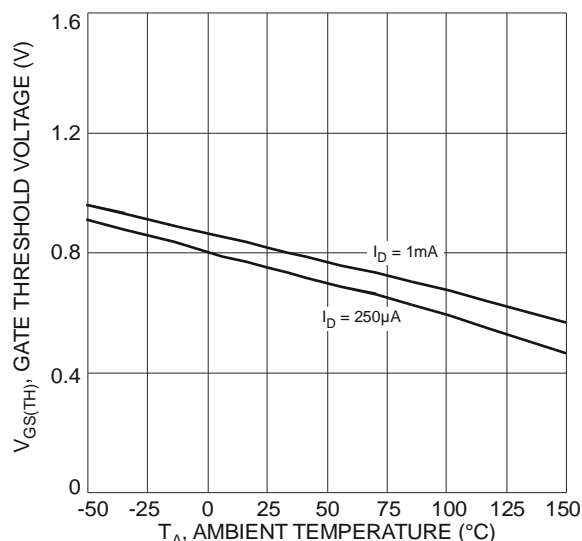


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

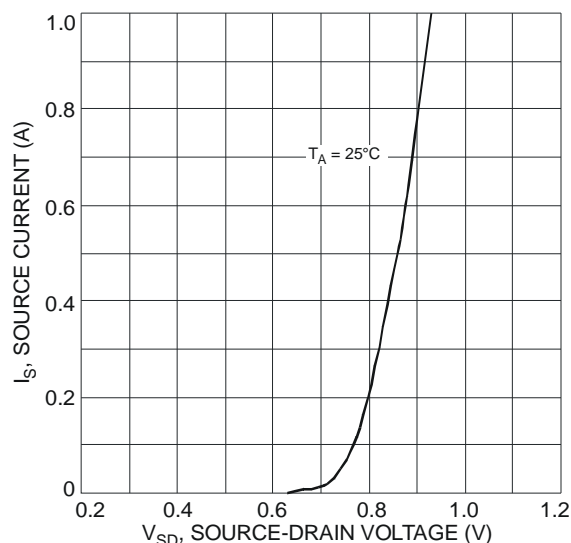


Fig. 8 Diode Forward Voltage vs. Current

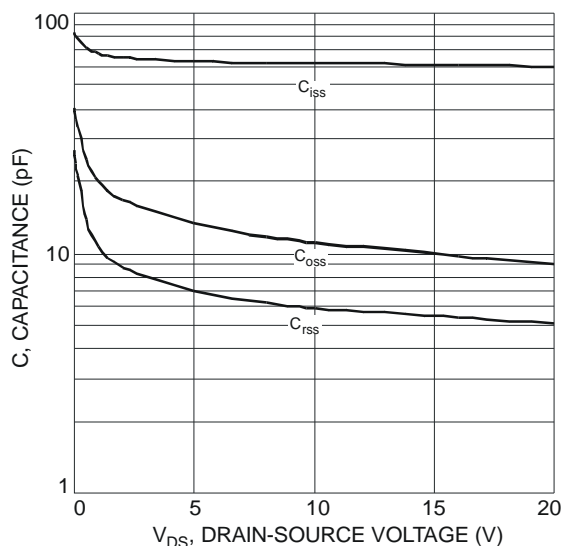


Fig. 9 Typical Total Capacitance

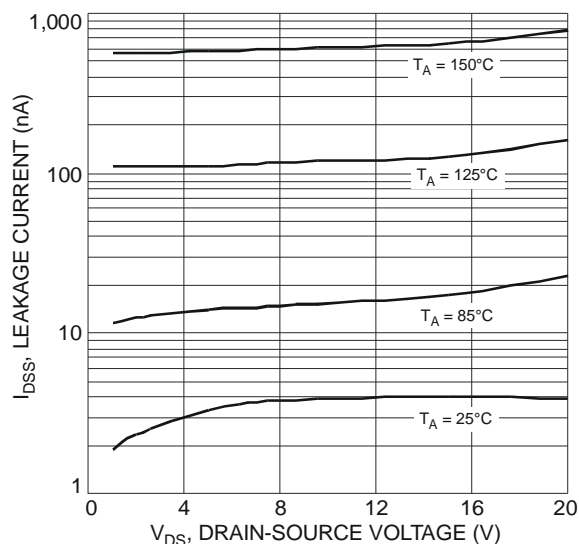


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

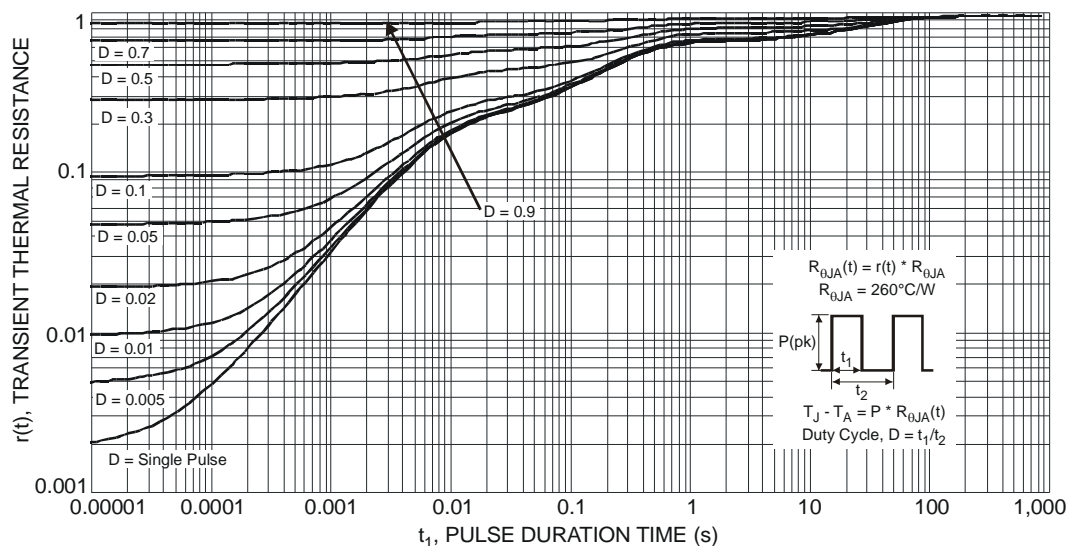
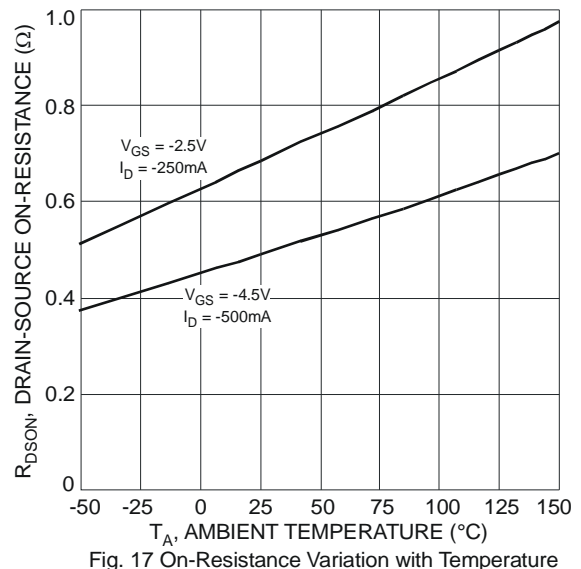
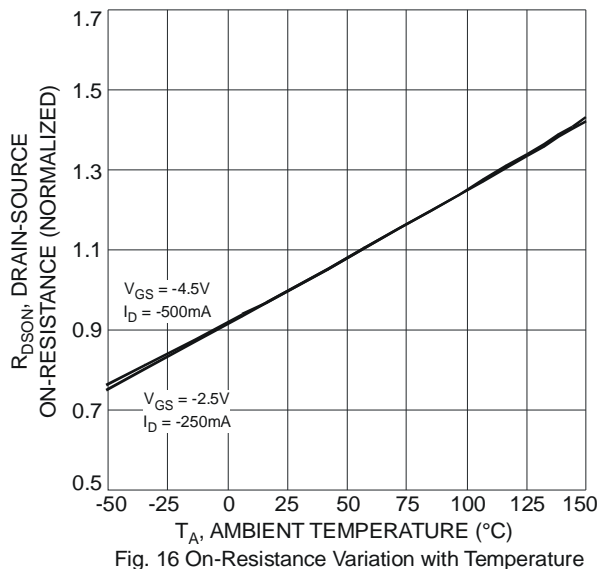
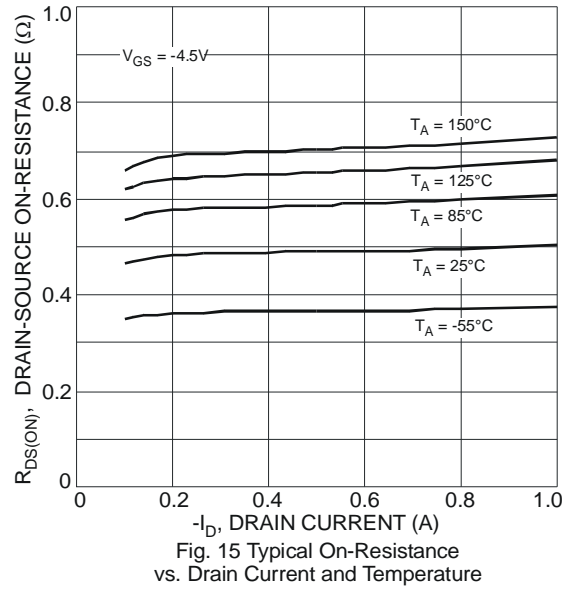
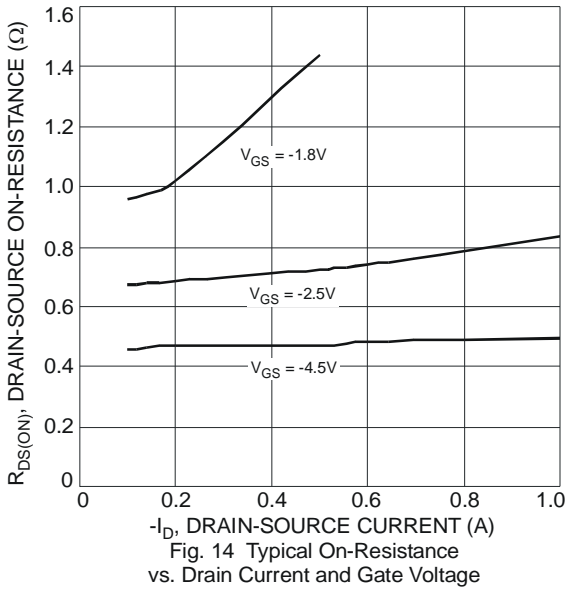
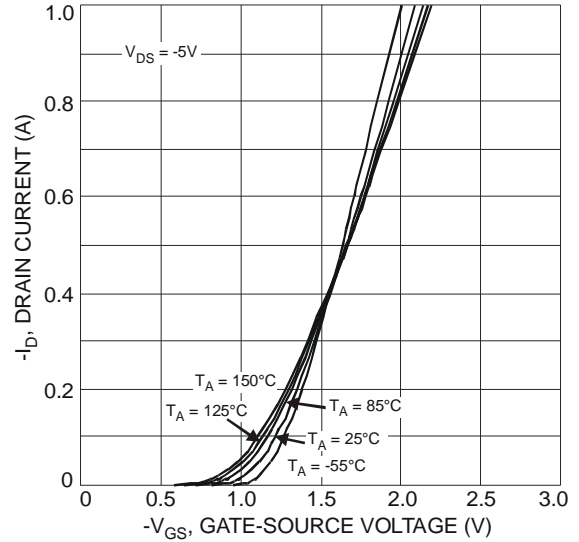
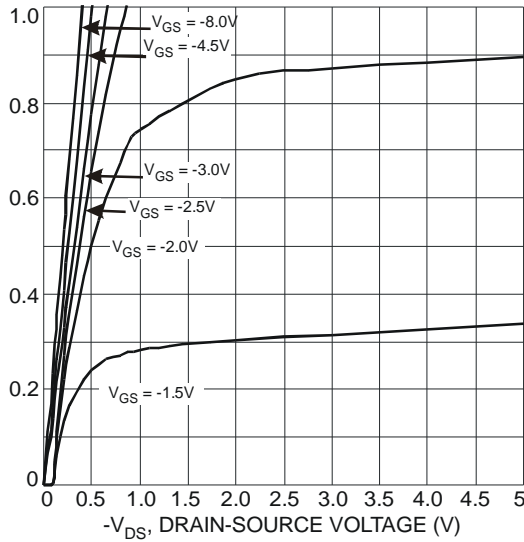


Fig. 11 Transient Thermal Response

P-CHANNEL – Q₂

NEW PRODUCT



P-CHANNEL – Q₂ (continued)

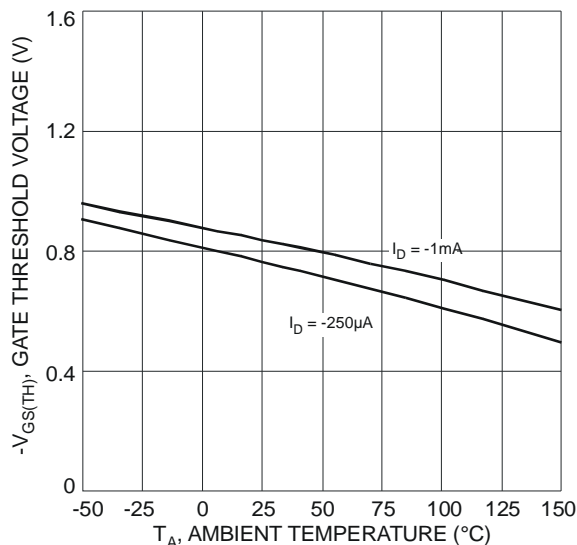


Fig. 18 Gate Threshold Variation vs. Ambient Temperature

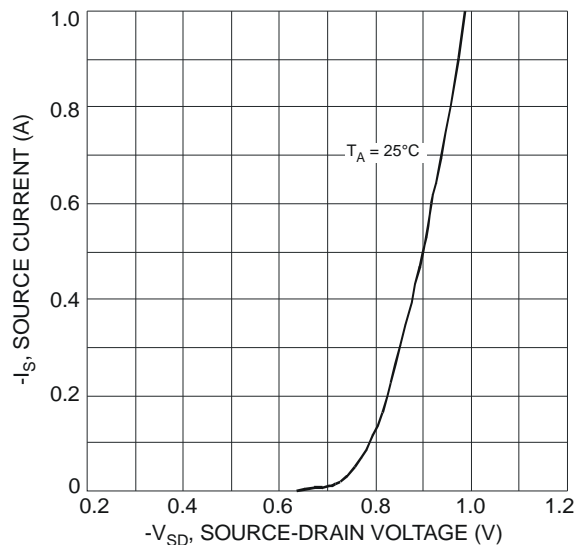


Fig. 19 Diode Forward Voltage vs. Current

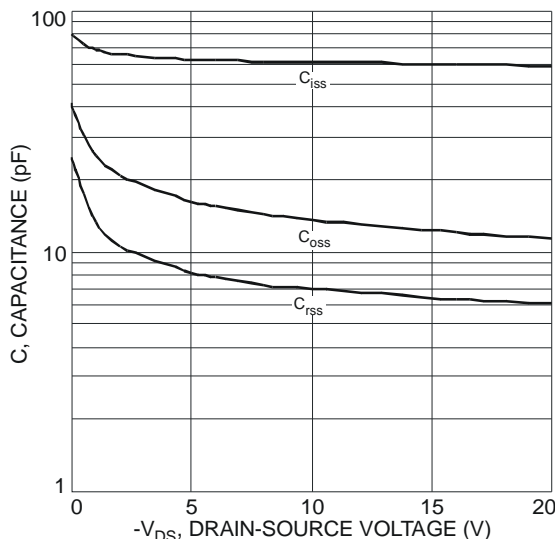


Fig. 20 Typical Total Capacitance

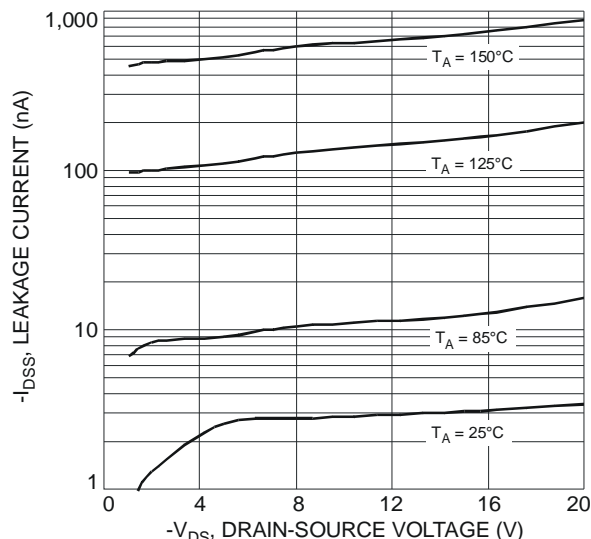


Fig. 21 Typical Leakage Current vs. Drain-Source Voltage

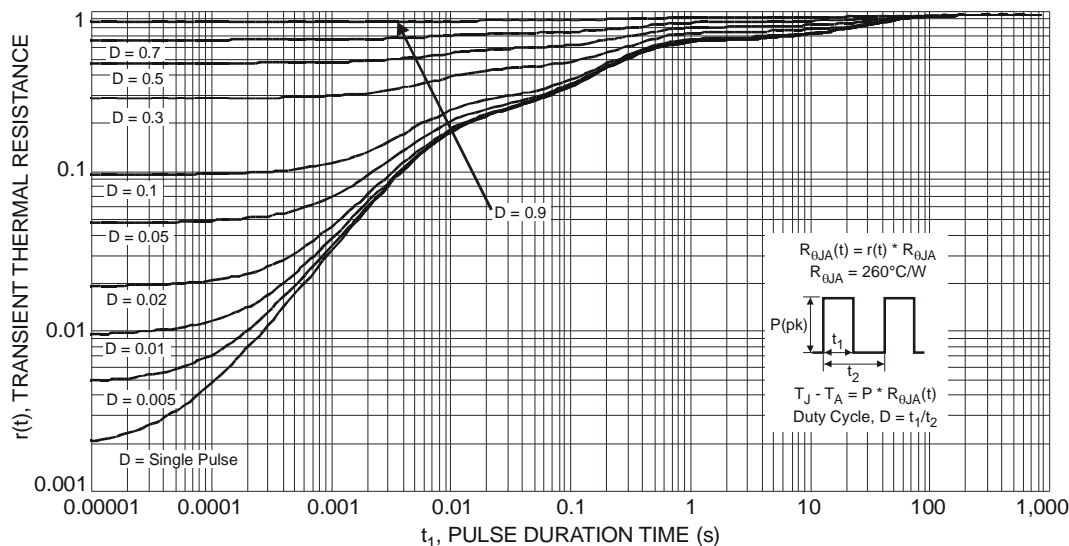


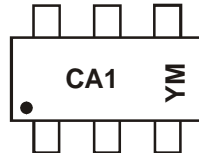
Fig. 22 Transient Thermal Response

Ordering Information (Note 6)

Part Number	Case	Packaging
DMG1016UDW-7	SOT-363	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



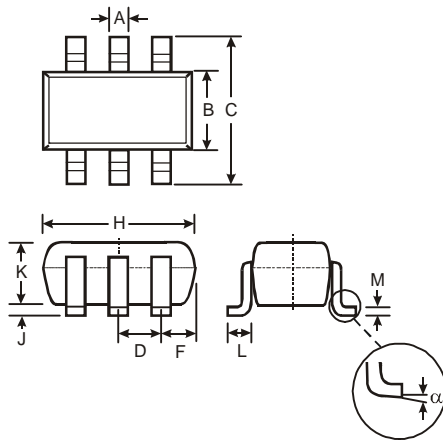
CA1 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: W = 2009)
M = Month (ex: 9 = September)

Date Code Key

Year	2009	2010	2011	2012	2013	2014	2015
Code	W	X	Y	Z	A	B	C

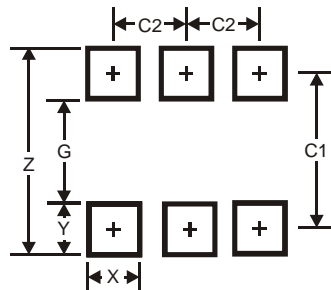
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-363		
Dim	Min	Max
A	0.10	0.30
B	1.15	1.35
C	2.00	2.20
D	0.65 Typ	
F	0.40	0.45
H	1.80	2.20
J	0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.22
α	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2009, Diodes Incorporated

www.diodes.com