





P-CHANNEL ENHANCEMENT MODE MOSFET

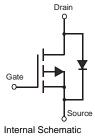
Features

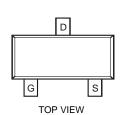
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)







Maximum Ratings @T_A = 25°C unless otherwise specified

Characte	eristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±8	V	
Continuous Drain Current (Note 3)			I _D	-2.5 -2.0	А
Pulsed Drain Current (Note 4)			I _{DM}	-27	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P_{D}	0.8	W
Thermal Resistance, Junction to Ambient @T _A = 25°C	$R_{ heta JA}$	157	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 3. Device mounted on FR-4 PCB with minimum recommended pad layout.
- 4. Repetitive rating, pulse width limited by junction temperature.

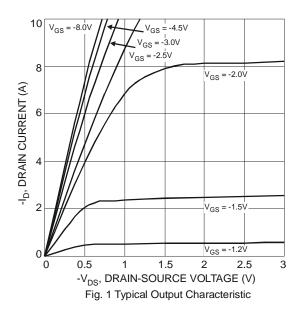


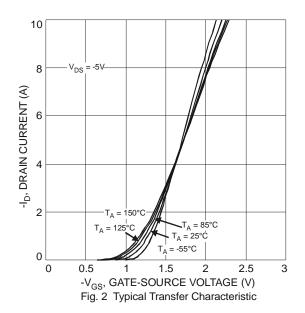
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_		V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	_		-1.0	μΑ	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage	I_{GSS}	_		±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	-0.45		-1.0	٧	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	P (-)			130	mΩ	$V_{GS} = -4.5V, I_D = -2.8A$	
Static Drain-Source On-Resistance	R _{DS} (ON)		_	190		$V_{GS} = -2.5V, I_D = -2.0A$	
Forward Transfer Admittance	$ Y_{fs} $	_	10	_	S	$V_{DS} = -5V, I_{D} = -2.8A$	
Diode Forward Voltage	V_{SD}	_	-0.75	-1.0	V	$V_{GS} = 0V$, $I_S = -1A$	
DYNAMIC CHARACTERISTICS (Note 6)							
Input Capacitance	C _{iss}	_	608	_	pF	V 6V V 6V	
Output Capacitance	Coss	_	82		pF	$V_{DS} = -6V, V_{GS} = 0V$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	72		pF	1 = 1.0WHZ	
Gate Resistance	R_{G}	_	44.9		Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$	
Total Gate Charge	Q_g	_	6.5		nC		
Gate-Source Charge	Q_{gs}	_	0.9		nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_{D} = -3A$	
Gate-Drain Charge	Q_{gd}	_	1.5		nC		
Turn-On Delay Time	t _{D(on)}	_	12.5		ns		
Turn-On Rise Time	t _r	_	10.3		ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(off)}	_	46.5	1	ns	$R_L = 10\Omega$, $R_G = 1.0\Omega$, $I_D = -1A$	
Turn-Off Fall Time	t _f		22.2	_	ns		

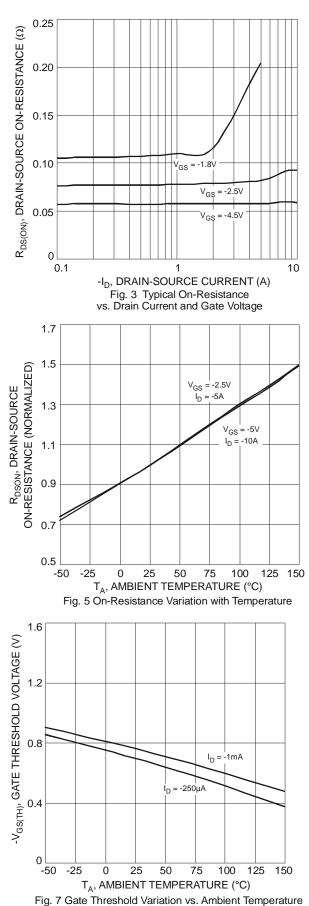
Notes:

- 5. Short duration pulse test used to minimize self-heating effect.
- 6. Guaranteed by design. Not subject to production testing.









0.16 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) -4.5V V_{GS} 0.12 T_A = 150°C 0.08 T_A = 125°C T_A = 85°C T_A = 25°C 0.04 T_A = -55°C 0 4 6 -I_D, DRAIN CURRENT (A) 0 10 Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

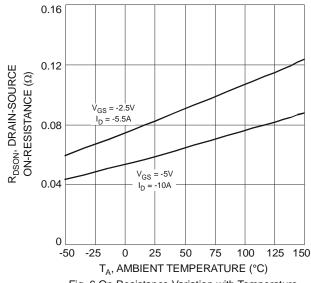
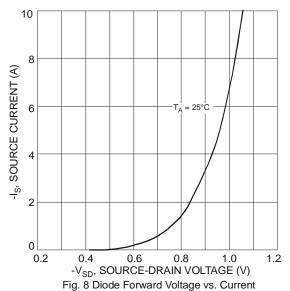
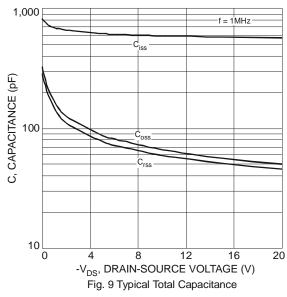


Fig. 6 On-Resistance Variation with Temperature







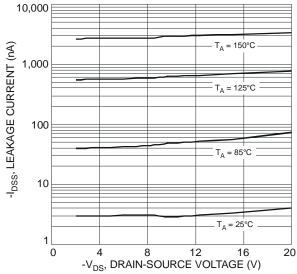


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

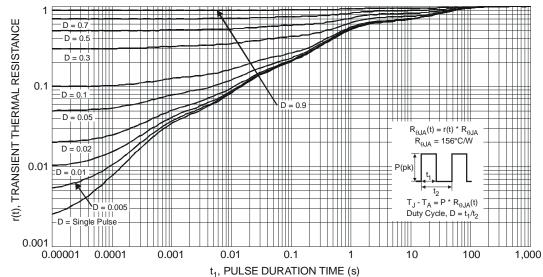


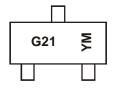
Fig. 11 Transient Thermal Response

Ordering Information (Note 7)

Part Number	Case	Packaging		
DMG2301U-7	SOT-23	3000/Tape & Reel		

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



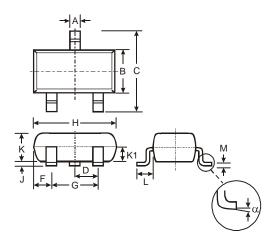
G21 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key

Year	200	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Χ		Υ		7	Α		В		С
								1			,	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

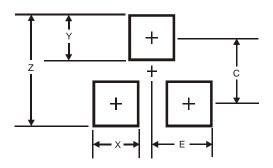


Package Outline Dimensions



SOT-23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.903	1.10	1.00			
K1	-	1	0.400			
L	0.45	0.61	0.55			
M	0.085	0.18	0.11			
α	0°	8°	-			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
П	1 35



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