



DMP2215L

P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

Low On-Resistance:

$$\begin{split} R_{DS(ON)} < 100 m\Omega &\ @\ V_{GS} = \text{-}4.5 \text{V},\ I_D = \text{-}2.7 \text{A} \\ R_{DS(ON)} < 215 m\Omega &\ @\ V_{GS} = \text{-}2.5 \text{V},\ I_D = \text{-}2.0 \text{A} \end{split}$$

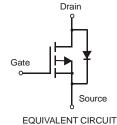
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

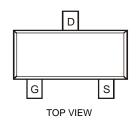
Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)

SOT-23







Maximum Ratings @T_A = 25°C unless otherwise specified

Chai	racteristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±12	V
Drain Current (Note 1)	Steady State	T _A = 25°C T _A = 70°C	I _D	-2.7 -2	А
Pulsed Drain Current (Note 3)			I _{DM}	8	Α

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	P_{D}	1.08	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 1)	$R_{ hetaJA}$	115	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

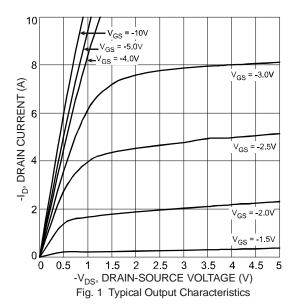
- 1. Device mounted on FR-4 PCB. t ≤5 sec.
- 2. No purposefully added lead.
- 3. Pulse width $\leq 10 \mu S$, Duty Cycle $\leq 1\%$.
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

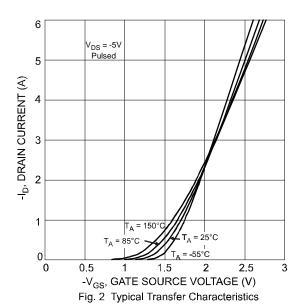


Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20		_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_		-800	nA	$V_{DS} = -20V, V_{GS} = 0V$	
On-State Drain Current	1	-6		_	Α	$V_{DS} \le -5V$, $V_{GS} = -4.5V$	
On-State Drain Current	I _{D(ON)}	-3		_	^	$V_{DS} \le -5V$, $V_{GS} = -2.5V$	
Gate-Source Leakage	I _{GSS}	_	_	±80	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	-0.45		-1.25	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_	80	100	mΩ	$V_{GS} = -4.5V$, $I_D = -2.7A$	
Static Dialit-Source Off-Resistance	R _{DS} (ON)		165	215	1115.2	$V_{GS} = -2.5V$, $I_D = -2.0A$	
Forward Transfer Admittance	Y _{fs}	_	4	_	S	$V_{DS} = -5V, I_{D} = -2.7A$	
Diode Forward Voltage (Note 5)	V_{SD}	_		-1.26	V	$V_{GS} = 0V, I_{S} = -2.7A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	250	_	pF	101/11/	
Output Capacitance	Coss	_	88	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	58	_	pF	I = 1.0WI IZ	
Gate Resistance	R_{g}	_	12	16	Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg		4.3	5.3		V 45V V 40V	
Gate-Source Charge	Q _{gs}	_	0.9	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -2.7A$	
Gate-Drain Charge	Q_{gd}	_	2.1	_		ID = -2.7 A	

Notes: 5. Short duration pulse test used to minimize self-heating effect.







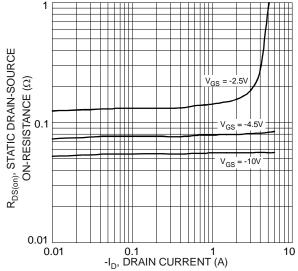


Fig. 3 On-Resistance vs. Drain Current and Gate Voltage

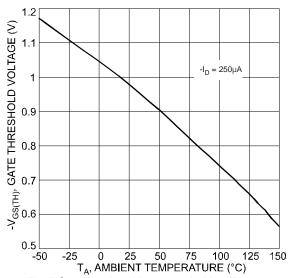


Fig. 5 Gate Threshold Voltage vs. Ambient Temperature

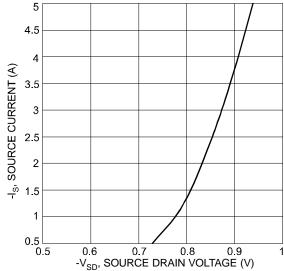
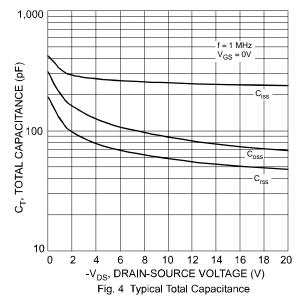


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage



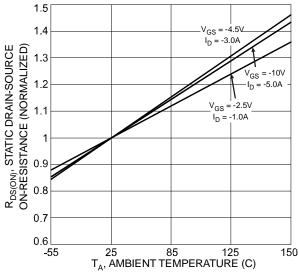


Fig. 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

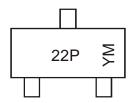


Ordering Information (Note 6)

Part Number	Case	Packaging
DMP2215L-7	SOT-23	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

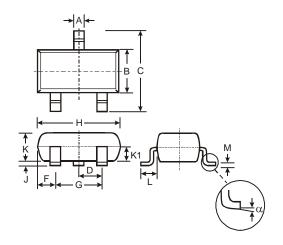


22P = Product Type Marking Code YM = Date Code Marking Y = Year (ex: U = 2007) M = Month (ex: 9 = September)

Date Code Key

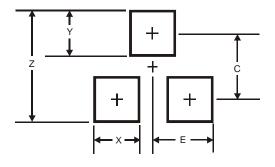
Year	20	07	20	08	20	09	20	10	20	11	20	12
Code	l	J	\	/	V	V	>	(١	1	Z	7
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Package Outline Dimensions



SOT-23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
7	0.013	0.10	0.05			
K	0.903	1.10	1.00			
K1	-	-	0.400			
L	0.45	0.61	0.55			
М	0.085	0.18	0.11			
α	0°	8°	-			
All Dimensions in mm						

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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