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features

- Controlled Baseline
 - One Assembly/Test Site, One Fabrication Site
- Extended Temperature Performance of Up to -55°C to 125°C
- Enhanced Diminishing Manufacturing Sources (DMS) Support
- Enhanced Product-Change Notification
- Qualification Pedigree[†]
- Single Voltage Detector (TPS3803): Adjustable/1.5 V

† Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.

description

The TPS3803 and TPS3805 families of supervisory circuits provide circuit initialization and timing supervision, primarily for DSPs and processor-based systems.

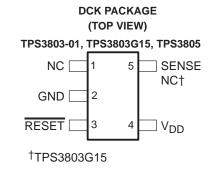
The TPS3803G15 device has a fixed-sense threshold voltage $V_{\rm IT}$ set by an internal voltage divider, whereas the TPS3803–01 has an adjustable SENSE input that can be configured by two external resistors. In addition to the fixed sense threshold monitored at $V_{\rm DD}$, the TPS3805 devices provide a second adjustable SENSE input. RESET is asserted in case any of the two voltages drops below $V_{\rm IT}$

During power on, $\overline{\text{RESET}}$ is asserted when supply voltage V_{DD} becomes higher than 0.8 V. Thereafter, the supervisory circuit monitors V_{DD} (and/or SENSE) and keeps $\overline{\text{RESET}}$ active as long as V_{DD} or SENSE remains below the threshold voltage V_{IT} . As soon as V_{DD} (SENSE) rises above the threshold voltage V_{IT} , $\overline{\text{RESET}}$ is deasserted again. The product spectrum is designed for 1.5 V, 3.3 V, and adjustable supply voltages. The devices are available in a five-pin SC-70 package.

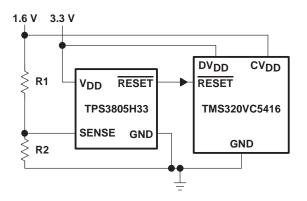
- Dual Voltage Detector (TPS3805): Adjustable/3.3 V
- High ±1.5% Threshold Voltage Accuracy
- Supply Current: 3 μA Typical at V_{DD} = 3.3 V
- Push/Pull Reset Output (TPS3805)
 Open-Drain Reset Output (TPS3803)
- Five-Pin SC-70 Package

typical applications

- Applications Using DSPs, Microcontrollers, or Microprocessors
- Wireless Communication Systems
- Portable/Battery-Powered Equipment
- Programmable Controls
- Intelligent Instruments
- Industrial Equipment
- Notebook/Desktop Computers
- Automotive Systems



typical operating circuit





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

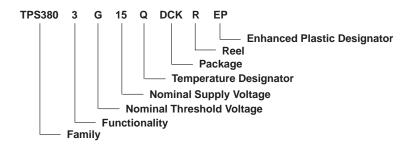


PACKAGE INFORMATION

_	DEVICE NAME	THRESHOL	MARKING	
TA	DEVICE NAME	V_{DD}	SENSE	MARKING
	TPS3803-01QDCKREPT	NA	1.226 V	AWH
-40°C to 125°C	TPS3803G15QDCKREP†	1.4 V	NA	AXT
	TPS3805H33QDCKREP†	3.05 V	1.226 V	AWY
	TPS3803-01MDCKREP [†]	NA	1.226 V	BAY
–55°C to 125°C	TPS3803G15MDCKREP [†]	1.40 V	NA	ARH
	TPS3805H33MDCKREP†	3.05 V	1.226 V	ARJ

[†] The DCKR passive indicates tape and reel containing 3000 parts.

ordering information

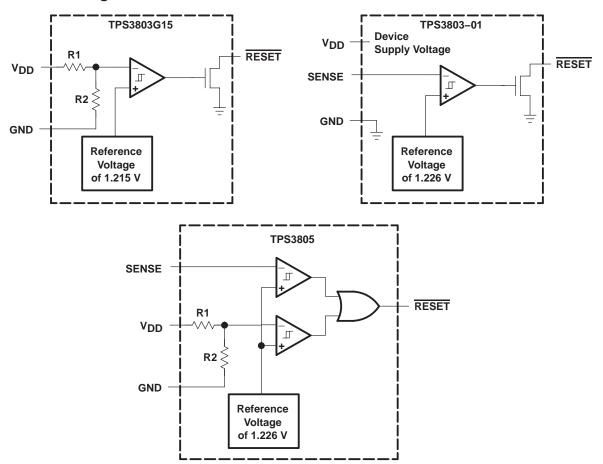


Function/Truth Tables

TPS3803-	01	TPS3803G15		
SENSE > VIT	RESET	$V_{DD} > V_{IT}$	RESET	
0	L	0	L	
1	Н	1	Н	

TPS3805H33						
V _{DD} > V _{IT}	SENSE > VIT	RESET				
0	0	L				
0	1	L				
1	0	L				
1	1	Н				

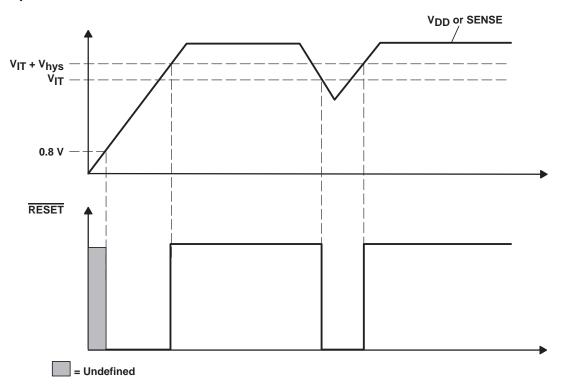
functional block diagram



TPS3803-01-EP, TPS3803G15-EP, TPS3805H33-EP VOLTAGE DETECTOR

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timing requirements



Terminal Functions

TERMINAL			DECORPTION
NAME	NO.	1/0	DESCRIPTION
GND	2	I	Ground
RESET	3	0	Active-low reset output (TPS3803—open drain, TPS3805—push/pull)
SENSE	5	- 1	Adjustable sense input
NC	1		No internal connection
NC (TPS3803G15)	5		No internal connection
V_{DD}	4	I	Input supply voltage, fixed sense input for TPS3803G15 and TPS3805

TPS3803-01-EP, TPS3803G15-EP, TPS3805H33-EP VOLTAGE DETECTOR

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absolute maximum ratings over operating free-air temperature (unless otherwise noted)†

Supply voltage, V _{DD} (see Note1)	7 V
All other pins (see Note 1)	0.3 V to 7 V
Maximum low-output current, I _{OL}	5 mA
Maximum high-output current, I _{OH}	–5 mA
Input clamp current, I _{IK} (V _I <0 or V _I >V _{DD})	±10 mA
Output clamp current, I _{OK} (V _O <0 or V _O >V _{DD})	±10 mA
Continuous total power dissipation	. See Dissipation Rating Table
Operating free-air temperature range, T _A	–55°C to 125°C
Storage temperature range, T _{stg} , (See Note 2)	–65°C to 150°C
Soldering temperature	

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values are with respect to GND. For reliable operation the device should not be continuously operated at 7 V for more than t=1000 h.
 - 2. Long term high-temperature storage and/or extended use at maximum recommended operating conditions may result in a reduction of overall device life. See www.ti.com/ep_quality for additional information on enhanced plastic packaging.

DISSIPATION RATING TABLE

PACKAGE	T _A <25°C	DERATING FACTOR	T _A = 70°C	T _A = 85°C
	POWER RATING	ABOVE $T_A = 25^{\circ}C$	POWER RATING	POWER RATING
DCK	321 mW	2.6 mW/°C	206 mW	167 mW

recommended operating conditions

		MIN	MAX	UNIT
Supply voltage, V _{DD}		1.3	6	V
Input voltage, V _I		0	V _{DD} +0.3	V
Operating free cir temperature range. To	Q suffix devices	-40	125	°C
Operating free-air temperature range, T _A	M suffix devices	-55	125	-0



TPS3803-01-EP, TPS3803G15-EP, TPS3805H33-EP VOLTAGE DETECTOR

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER		TEST C	ONDITIONS	MIN	TYP	MAX	UNIT
Vон	High-level output voltage (TPS3805 on	$V_{DD} = 1.5 \text{ V},$ $V_{DD} = 3.3 \text{ V},$ $V_{DD} = 6 \text{ V},$	$I_{OH} = -0.5 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1.5 \text{ mA}$	0.8xV _{DD}			V	
V _{OL} Low-level output voltage			$V_{DD} = 1.5 \text{ V},$ $V_{DD} = 3.3 \text{ V},$ $V_{DD} = 6 \text{ V},$	$I_{OL} = 1 \text{ mA}$ $I_{OL} = 2 \text{ mA}$ $I_{OL} = 3 \text{ mA}$			0.3	V
	Dower up react voltage (acc Note 2)	VIT > 1.5 V, TA =	25°C		0.8			V
	Power-up reset voltage (see Note 2)	VIT ≤ 1.5 V, TA = 1	25°C		1			V
		SENSE]		1.2	1.226	1.244	
\vee_{IT}	Negative-going input threshold voltage (see Note 3)	TPS3803G15			1.379	1.4	1.421	V
	voltage (see Note o)	TPS3805H33			3.004	3.05	3.096	
.,	I best and the		1.2 V < V _{IT} < 2	.5 V		15		
V _{hys}	Hysteresis		2.5 V < V _{IT} < 3	.5 V		30		mV
lį	Input current	SENSE			-25		25	nA
loh	High-level output current at RESET	Open drain only	$V_{DD} = V_{IT} + 0.$.2V, V _{OH} = V _{DD}			300	nA
		TPS3803-01				2	4	
		TPS3805, TPS3803G15	$V_{DD} = 3.3 \text{ V}$, output unconnected			3	5	
IDD	Supply current	TPS3803-01				2	4	μΑ
		TPS3805, TPS3803G15	V _{DD} = 6 V, output unconnected			4	6	
Cl	Input capacitance		$V_I = 0 V \text{ to } V_{DI}$)		1		pF

NOTES: 3. The lowest supply voltage at which $\overline{\text{RESET}}$ (VOL(max) = 0.2 V, IOL = 50 μ A) becomes active. $t_{r(VDD)} \ge 15 \mu s/V$

timing requirements at R $_L$ = 1 M $\!\Omega_{\!_1}$ C $_L$ = 50 pF, over recommended operating free-air temperature range

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Γ.	Dollar width	At V _{DD}	V 4.05 V 0.05 V				_
tν	Pulse width	At SENSE	V _{IH} = 1.05 x V _{IT} , V _{IL} = 0.95 x V _{IT}	5.5			μs

switching characteristics at R $_L$ = 1 M $\!\Omega_{\!_1}$ C $\!_L$ = 50 pF, over recommended operating free-air temperature range

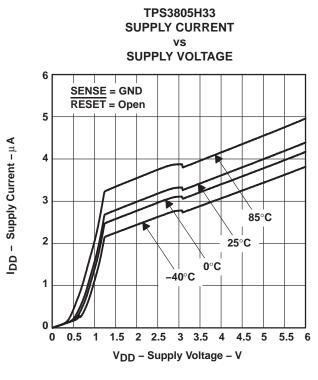
	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
tPHL	Propagation (delay) time, high-to-low-level output		V _{IH} = 1.05 x V _{IT} ,		5	100	_
tPLH	Propagation (delay) time, low-to-high-level output	V _{DD} to RESET delay SENSE to RESET delay	V _{IL} = 0.95 x V _{IT}		5	100	μs



^{4.} To ensure the best stability of the threshold voltage, place a bypass capacitor (ceramic, 0.1 μF) near the supply terminals.

TPS3803-01

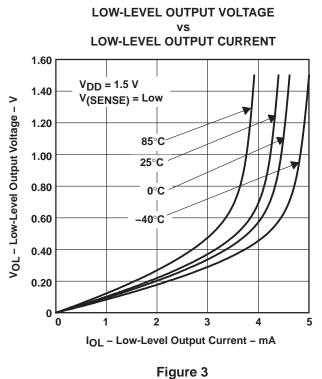
TYPICAL CHARACTERISTICS



SUPPLY CURRENT SUPPLY VOLTAGE 2.5 IDD - Supply Current - µA 2 1.5 85°C 25°C 0°C -40°C 0.5 SENSE = GND RESET = Open 0.5 2 2.5 3 3.5 4.5 5 5.5 6 V_{DD} - Supply Voltage - V

Figure 1

Figure 2



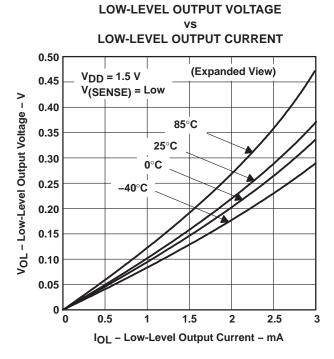


Figure 4

TYPICAL CHARACTERISTICS

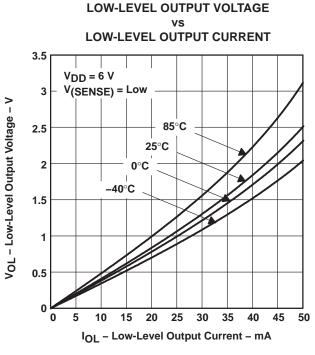


Figure 5

TPS3805H33

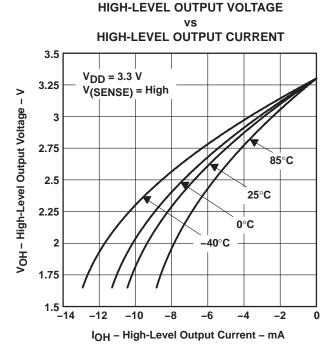


Figure 7

LOW-LEVEL OUTPUT VOLTAGE
vs
LOW-LEVEL OUTPUT CURRENT

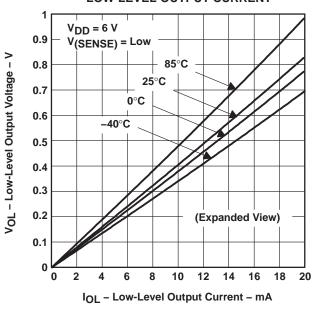


Figure 6

TPS3805H33
HIGH-LEVEL OUTPUT VOLTAGE
vs
HIGH-LEVEL OUTPUT CURRENT

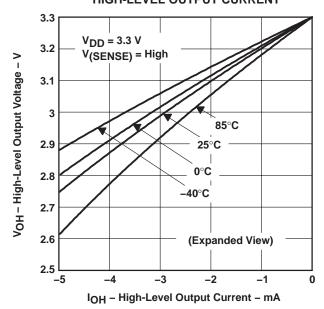
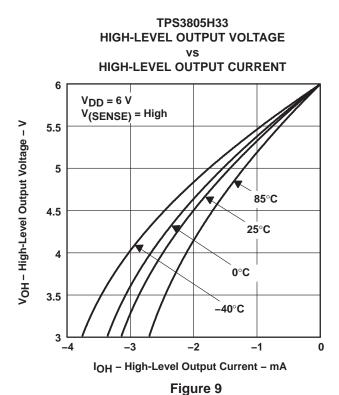


Figure 8

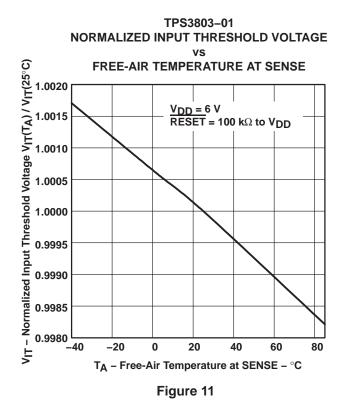


TPS3805H33

TYPICAL CHARACTERISTICS



HIGH-LEVEL OUTPUT VOLTAGE **HIGH-LEVEL OUTPUT CURRENT** 6 $V_{DD} = 6 V$ 5.8 V(SENSE) = High VOH - High-Level Output Voltage - V 5.6 5.4 25°C 5.2 0°C -40°C 5 4.8 (Expanded View) -10 IOH - High-Level Output Current - mA Figure 10



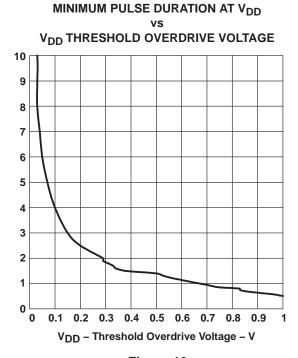


Figure 12

t_w – Minimum Pulse Duration at V_{DD} – μs

TYPICAL CHARACTERISTICS

MINIMUM PULSE DURATION AT SENSE vs SENSE THRESHOLD OVERDRIVE VOLTAGE

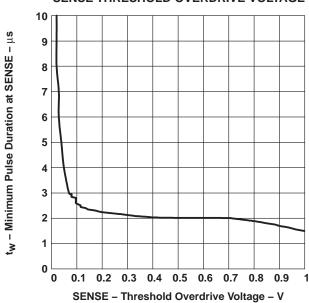


Figure 13





.com 7-Mar-2005

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TPS3803-01MDCKREP	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
TPS3803-01QDCKREP	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
TPS3803G15MDCKREP	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
TPS3803G15QDCKREP	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
TPS3805H33MDCKREP	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
V62/04648-01XE	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
V62/04648-02XE	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
V62/04648-04XE	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
V62/04648-05XE	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM
V62/04648-06XE	ACTIVE	SC70	DCK	5	3000	None	CU SNPB	Level-1-220C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

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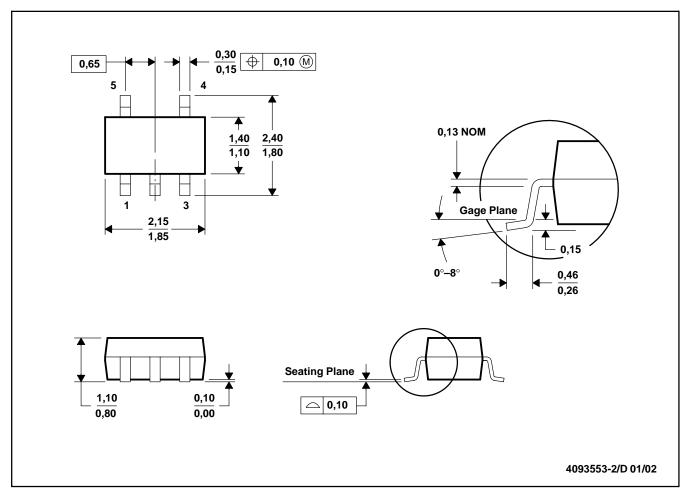
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

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DCK (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion.

D. Falls within JEDEC MO-203

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