TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC153P,TC74AC153F,TC74AC153FN

Dual 4-Channel Multiplexer

The TC74AC153 is an advanced high speed CMOS DUAL 4-CHANNEL MULTIPLEXER fabricated with silicon gate and double-layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Each of these data (1C0-1C3, 2C0-2C3) is selected by the two address inputs A and B.

Separate strobe inputs (1 \overline{G} , $\ 2\overline{G}$) are provided for each of the two four-line sections.

The strobe input can be used to inhibit the data output; the output is fixed in low level unconditionally.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

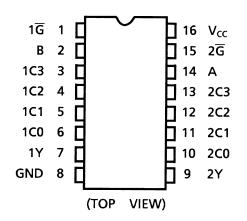
Features

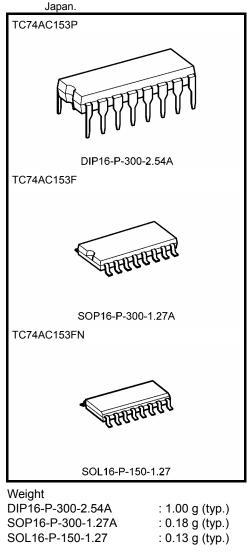
- High speed: $t_{pd} = 3.9$ ns (typ.) at $V_{CC} = 5$ V
- Low power dissipation: $I_{CC} = 8 \mu A \pmod{at Ta} = 25^{\circ}C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Symmetrical output impedance: |IOH| = IOL = 24 mA (min)

Capability of driving 50 Ω transmission lines.

- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Pin and function compatible with 74F153

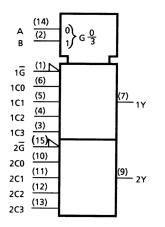
Pin Assignment





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IEC Logic Symbol



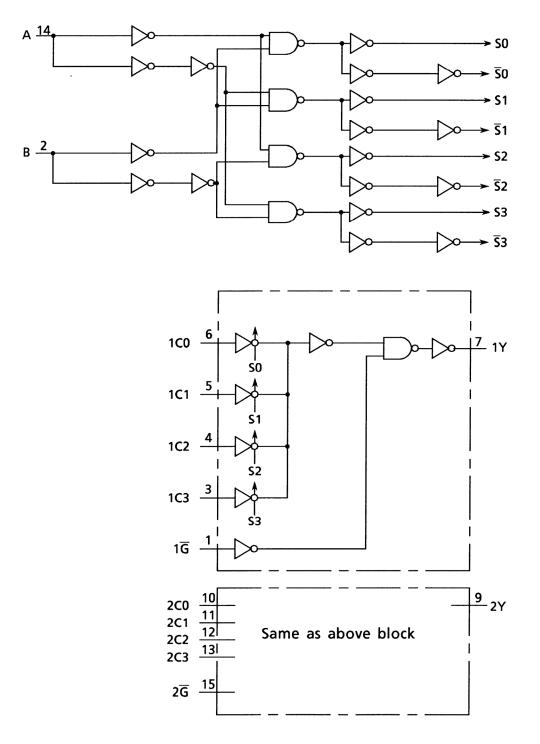
Truth Table

| Select Inputs | | | Data | Inputs | Strobe | Output | |
|---------------|---|----|------|--------|--------|--------|---|
| В | А | C0 | C1 | C2 | C3 | IJ | Y |
| Х | Х | Х | Х | Х | Х | Н | L |
| L | L | L | Х | Х | Х | L | L |
| L | L | Н | Х | Х | Х | L | Н |
| L | Н | Х | L | Х | Х | L | L |
| L | Н | Х | Н | Х | Х | L | Н |
| Н | L | Х | Х | L | Х | L | L |
| Н | L | Х | Х | Н | Х | L | Н |
| Н | Н | Х | Х | Х | L | L | L |
| Н | Н | Х | Х | Х | Н | L | Н |

X: Don't care

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System Diagram



Absolute Maximum Ratings (Note 1)

| Characteristics | Symbol | Rating | Unit |
|------------------------------------|------------------|-------------------------------|------|
| Supply voltage range | V _{CC} | –0.5 to 7.0 | V |
| DC input voltage | V _{IN} | -0.5 to V _{CC} + 0.5 | V |
| DC output voltage | V _{OUT} | -0.5 to V _{CC} + 0.5 | V |
| Input diode current | I _{IK} | ±20 | mA |
| Output diode current | IOK | ±50 | mA |
| DC output current | IOUT | ±50 | mA |
| DC V _{CC} /ground current | ICC | ±100 | mA |
| Power dissipation | PD | 500 (DIP) (Note 2)/180 (SOP) | mW |
| Storage temperature | T _{stg} | –65 to 150 | °C |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65° C. From Ta = 65 to 85° C a derating factor of -10 mW/°C should be applied up to 300 mW.

| Characteristics | Symbol | Rating | Unit | |
|--------------------------|------------------|-------------------------------------|--------|--|
| Supply voltage | V _{CC} | 2.0 to 5.5 | V | |
| Input voltage | V _{IN} | 0 to V _{CC} | V | |
| Output voltage | V _{OUT} | 0 to V _{CC} | V | |
| Operating temperature | T _{opr} | -40 to 85 | °C | |
| Input rise and fall time | dt/dV | 0 to 100 (V_{CC} = 3.3 \pm 0.3 V) | ns/V | |
| | uvuv | 0 to 20 (V_{CC} = 5 \pm 0.5 V) | 115/ V | |

Operating Ranges (Note)

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

| Characteristics | Symbol | Test Condition V _{CC} (V) | | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit | |
|-----------------------------|-----------------|--|--------------------------|-----------------------|-----------|------|------|---------------------|------|------|------|
| Characteristics | Cymbol | | | | | Min | Тур. | Max | Min | Max | Unit |
| | | _ | | | 2.0 | 1.50 | | | 1.50 | | |
| High-level input voltage | V _{IH} | | | | 3.0 | 2.10 | — | — | 2.10 | — | V |
| | | | | | 5.5 | 3.85 | — | — | 3.85 | — | |
| | | | | | 2.0 | _ | — | 0.50 | _ | 0.50 | |
| Low-level input voltage | VIL | — | | 3.0 | — | — | 0.90 | — | 0.90 | V | |
| | | | | 5.5 | — | — | 1.65 | — | 1.65 | | |
| | V _{OH} | | | | 2.0 | 1.9 | 2.0 | _ | 1.9 | _ | |
| | | V _{IN} = V _{IH} or V _{IL} | I _{OH} = -50 μA | | 3.0 | 2.9 | 3.0 | _ | 2.9 | — | - V |
| High-level output | | | | | 4.5 | 4.4 | 4.5 | — | 4.4 | — | |
| voltage | | | I _{OH} = -4 mA | | 3.0 | 2.58 | _ | _ | 2.48 | _ | |
| | | | I _{OH} = -24 mA | | 4.5 | 3.94 | — | _ | 3.80 | — | |
| | | | I _{OH} = -75 mA | (Note) | 5.5 | _ | _ | — | 3.85 | — | |
| | V _{OL} | VIN = VIH or VIL | $I_{OL} = 50 \ \mu A$ | | 2.0 | | 0.0 | 0.1 | | 0.1 | |
| | | | | | 3.0 | — | 0.0 | 0.1 | — | 0.1 | v |
| Low-level output | | | | | 4.5 | | 0.0 | 0.1 | _ | 0.1 | |
| voltage | | | $I_{OL} = 12 \text{ mA}$ | _{DL} = 12 mA | | | — | 0.36 | | 0.44 | v |
| | | | $I_{OL} = 24 \text{ mA}$ | | 4.5 | _ | — | 0.36 | — | 0.44 | |
| | | | I _{OL} = 75 mA | (Note) | 5.5 | | _ | _ | _ | 1.65 | |
| Input leakage current | I _{IN} | $V_{IN} = V_{CC}$ or GND | | 5.5 | _ | — | ±0.1 | | ±1.0 | μΑ | |
| Quiescent supply current | ICC | $V_{IN} = V_{CC}$ or GND | | 5.5 | | _ | 8.0 | | 80.0 | μΑ | |

Note: This spec indicates the capability of driving 50 Ω transmission lines. One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

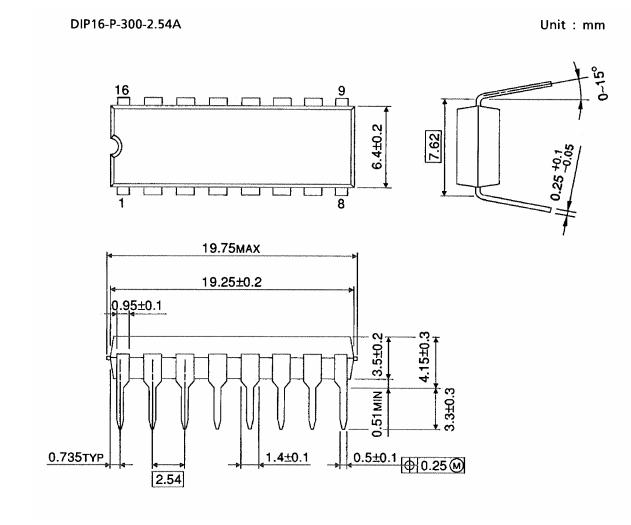
| Characteristics | Symbol | Test Condition | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit |
|---------------------------------------|--------------------------------------|----------------|---|-----------|-------------|--------------|---------------------|--------------|------|
| | , | | V _{CC} (V) | Min | Тур. | Max | Min | Max | |
| Propagation delay time (Cn-Y) | t _{pLH} t _{pHL} | _ | $\begin{array}{c} 3.3\pm0.3\\ 5.0\pm0.5\end{array}$ | | 7.6 5.0 | 14.5 9.0 | 1.0 1.0 | 16.5 10.3 | ns |
| Propagation delay time (A, B-Y) | t _{pLH} t _{pHL} | _ | $\begin{array}{c} 3.3\pm0.3\\ 5.0\pm0.5\end{array}$ | | 10.5 6.6 | 20.5 10.5 | 1.0 1.0 | 23.4 12.0 | ns |
| Propagation delay time (G -Y) | t _{pLH} t _{pHL} | _ | $\begin{array}{c} 3.3\pm0.3\\ 5.0\pm0.5\end{array}$ | | 6.8 4.4 | 13.3 8.0 | 1.0 1.0 | 15.2 9.1 | ns |
| Input capacitance | C _{IN} | _ | | _ | 5 | 10 | | 10 | pF |
| Power dissipation capacitance | C _{PD} (Note) | _ | | _ | 54 | _ | _ | _ | pF |

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions



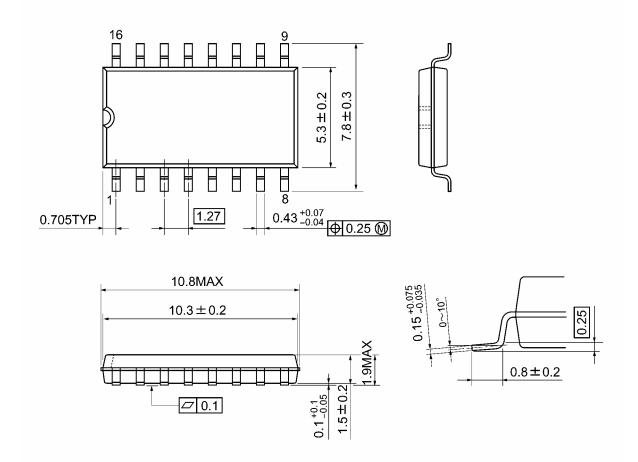
Weight: 1.00 g (typ.)



Package Dimensions

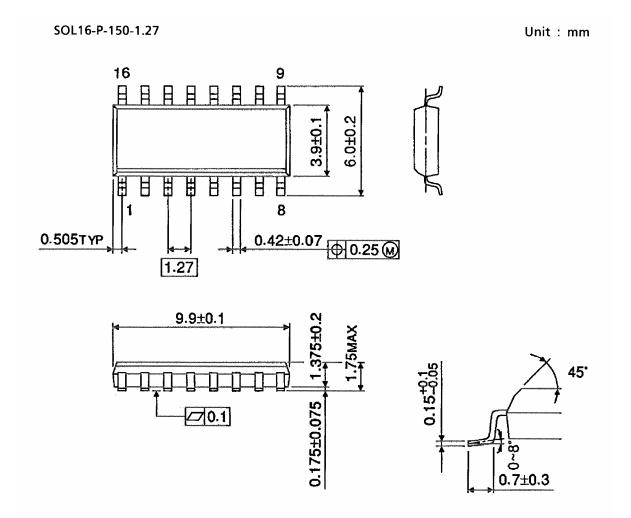
SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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