

HD74LV2G126A

Dual Bus Buffer with 3-state Output

REJ03D0100-0500Z
(Previous ADE-205-348D (Z))
Rev.5.00
Sep.30.2003

Description

The HD74LV2G126A has dual bus buffer with 3-state output in a 8 pin package. Output is disabled when the associated output enable (OE) input is low. To ensure the high impedance state during power up or power down, OE should be connected to V_{CC} through a pull-down resistor; the minimum value of the resistor is determined by the current sourcing capability of the driver. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

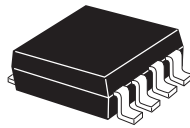
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV126A
Supply voltage range : 1.65 to 5.5 V
Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
All outputs V_O (Max.) = 5.5 V (@ V_{CC} = 0 V, Output : Z)
- Output current ± 6 mA (@ V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@ V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

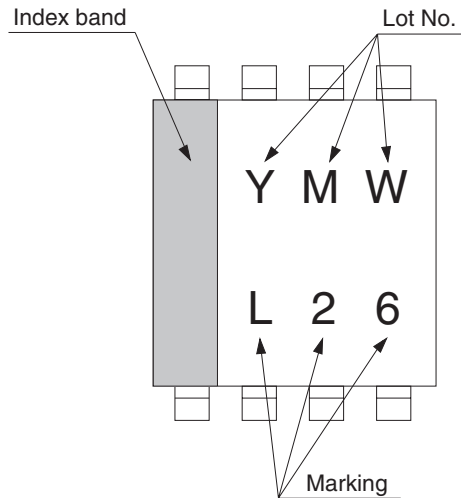
Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV2G126AUSE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)

Outline and Article Indication

- HD74LV2G126A



SSOP-8



Y : Year code
(the last digit of year)
M : Month code
W : Week code

Function Table

Inputs		Output Y
OE	A	
H	H	H
H	L	L
L	X	Z

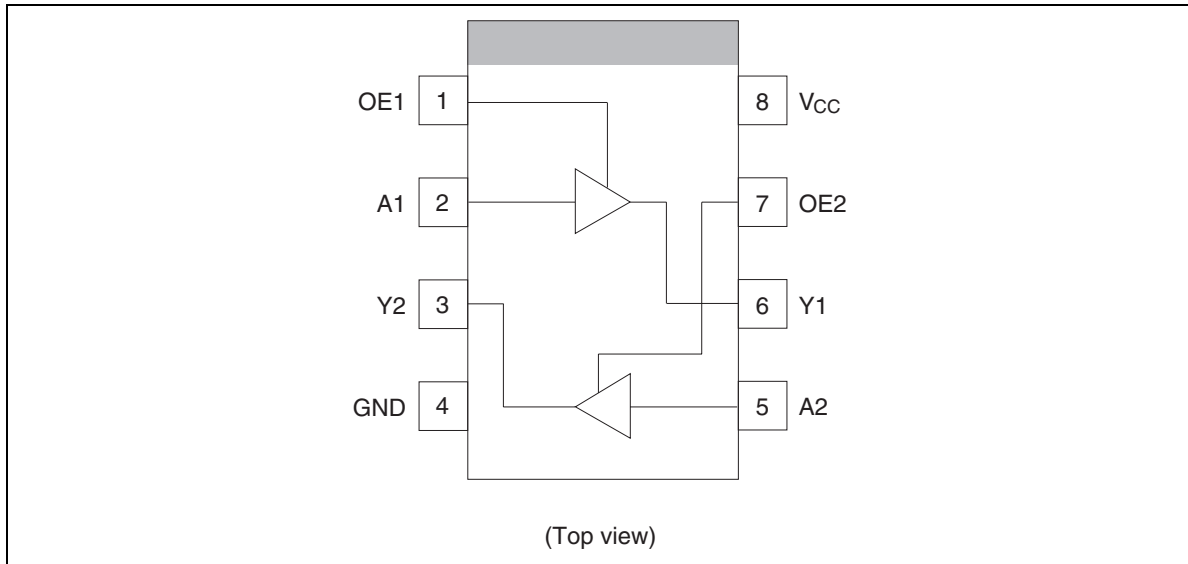
H : High level

L : Low level

X : Immaterial

Z : High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	-0.5 to 7.0	V	
Input voltage range ^{*1}	V_I	-0.5 to 7.0	V	
Output voltage range ^{*1, 2}	V_O	-0.5 to $V_{CC} + 0.5$ -0.5 to 7.0	V	Output : H or L V_{CC} : OFF or output : Z
Input clamp current	I_{IK}	-20	mA	$V_I < 0$
Output clamp current	I_{OK}	± 50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	± 25	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 50	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air) ^{*3}	P_T	200	mW	
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150°C .

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
		0	5.5		Output : Z
Output current	I_{OL}	—	1	mA	$V_{CC} = 1.65$ to 1.95 V
		—	2		$V_{CC} = 2.3$ to 2.7 V
		—	6		$V_{CC} = 3.0$ to 3.6 V
		—	12		$V_{CC} = 4.5$ to 5.5 V
	I_{OH}	—	–1		$V_{CC} = 1.65$ to 1.95 V
		—	–2		$V_{CC} = 2.3$ to 2.7 V
		—	–6		$V_{CC} = 3.0$ to 3.6 V
		—	–12		$V_{CC} = 4.5$ to 5.5 V
Input transition rise or fall rate	$\Delta t / \Delta v$	0	300	ns / V	$V_{CC} = 1.65$ to 1.95 V
		0	200		$V_{CC} = 2.3$ to 2.7 V
		0	100		$V_{CC} = 3.0$ to 3.6 V
		0	20		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	T_a	–40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

- $T_a = -40$ to 85°C

Item	Symbol	V _{CC} (V) *	Min	Typ	Max	Unit	Test condition
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.75	—	—	V	
		2.3 to 2.7	V _{CC} ×0.7	—	—		
		3.0 to 3.6	V _{CC} ×0.7	—	—		
		4.5 to 5.5	V _{CC} ×0.7	—	—		
	V _{IL}	1.65 to 1.95	—	—	V _{CC} ×0.25		
		2.3 to 2.7	—	—	V _{CC} ×0.3		
		3.0 to 3.6	—	—	V _{CC} ×0.3		
		4.5 to 5.5	—	—	V _{CC} ×0.3		
Hysteresis voltage	V _H	1.8	—	0.25	—	V	V _T ⁺ – V _T [–]
		2.5	—	0.30	—		
		3.3	—	0.35	—		
		5.0	—	0.45	—		
Output voltage	V _{OH}	Min to Max	V _{CC} –0.1	—	—	V	I _{OH} = –50 μA
		1.65	1.4	—	—		I _{OH} = –1 mA
		2.3	2.0	—	—		I _{OH} = –2 mA
		3.0	2.48	—	—		I _{OH} = –6 mA
		4.5	3.8	—	—		I _{OH} = –12 mA
	V _{OL}	Min to Max	—	—	0.1	I _{OL} = 50 μA	
		1.65	—	—	0.3	I _{OL} = 1 mA	
		2.3	—	—	0.4	I _{OL} = 2 mA	
		3.0	—	—	0.44	I _{OL} = 6 mA	
		4.5	—	—	0.55	I _{OL} = 12 mA	
Input current	I _{IN}	0 to 5.5	—	—	±1	μA	V _{IN} = 5.5 V or GND
Off state output current	I _{OZ}	Min to Max	—	—	±5	μA	V _O = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	—	—	10	μA	V _{IN} = V _{CC} or GND, I _O = 0
Output leakage current	I _{OFF}	0	—	—	5	μA	V _{IN} or V _O = 0 to 5.5 V
Input capacitance	C _{IN}	3.3	—	3.0	—	pF	V _{IN} = V _{CC} or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

- $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	13.5	23.5	1.0	26.0	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	19.0	33.0	1.0	36.0		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	13.7	26.5	1.0	29.0	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{ZL}	—	20.5	36.0	1.0	38.0		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	8.3	20.0	1.0	22.5	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{LZ}	—	13.0	29.5	1.0	32.0		$C_L = 50 \text{ pF}$		

- $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	7.1	13.0	1.0	15.5	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	9.2	16.5	1.0	18.5		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	7.4	13.0	1.0	15.5	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{ZL}	—	9.5	16.5	1.0	18.5		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	5.7	14.7	1.0	17.0	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{LZ}	—	8.1	18.2	1.0	20.5		$C_L = 50 \text{ pF}$		

- $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	5.0	8.0	1.0	9.5	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	6.4	11.5	1.0	13.0		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	5.1	8.0	1.0	9.5	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{ZL}	—	6.6	11.5	1.0	13.0		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	4.4	9.7	1.0	11.5	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{LZ}	—	6.1	13.2	1.0	15.0		$C_L = 50 \text{ pF}$		

HD74LV2G126A

Switching Characteristics (cont)

- $V_{CC} = 5.0 \pm 0.5 \text{ V}$

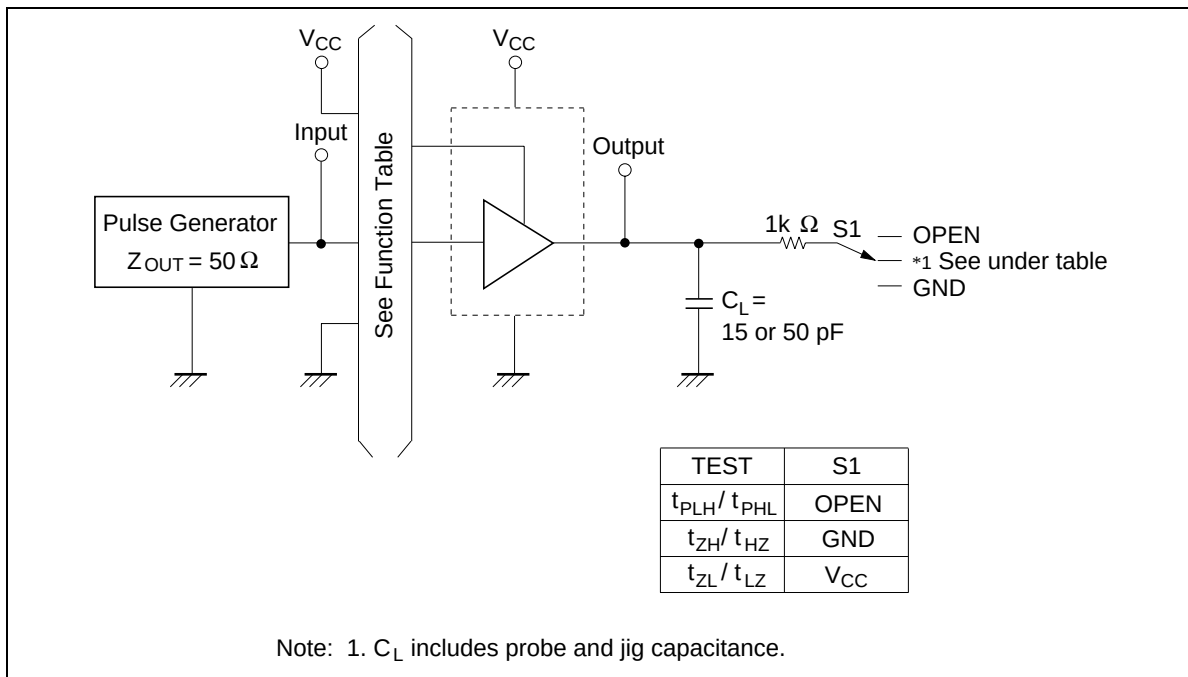
Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	t_{PLH}	—	3.5	5.5	1.0	6.5	ns	$C_L = 15 \text{ pF}$	A	Y
	t_{PHL}	—	4.6	7.5	1.0	8.5		$C_L = 50 \text{ pF}$		
Enable time	t_{ZH}	—	3.6	5.1	1.0	6.0	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{ZL}	—	4.6	7.1	1.0	8.0		$C_L = 50 \text{ pF}$		
Disable time	t_{HZ}	—	3.3	6.8	1.0	8.0	ns	$C_L = 15 \text{ pF}$	OE	Y
	t_{LZ}	—	4.3	8.8	1.0	10.0		$C_L = 50 \text{ pF}$		

Operating Characteristics

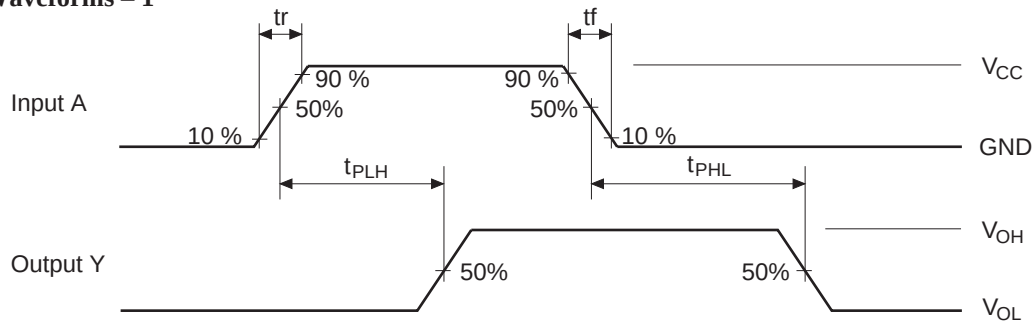
- $C_L = 50 \text{ pF}$

Item	Symbol	$V_{CC} \text{ (V)}$	$T_a = 25^\circ\text{C}$			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	C_{PD}	3.3	—	10.5	—	pF	$f = 10 \text{ MHz}$
		5.0	—	11.5	—		

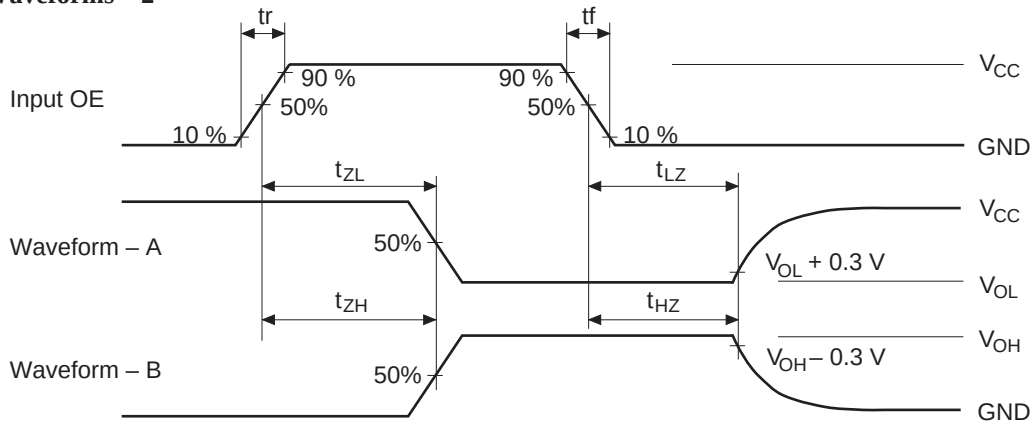
Test Circuit



• Waveforms – 1

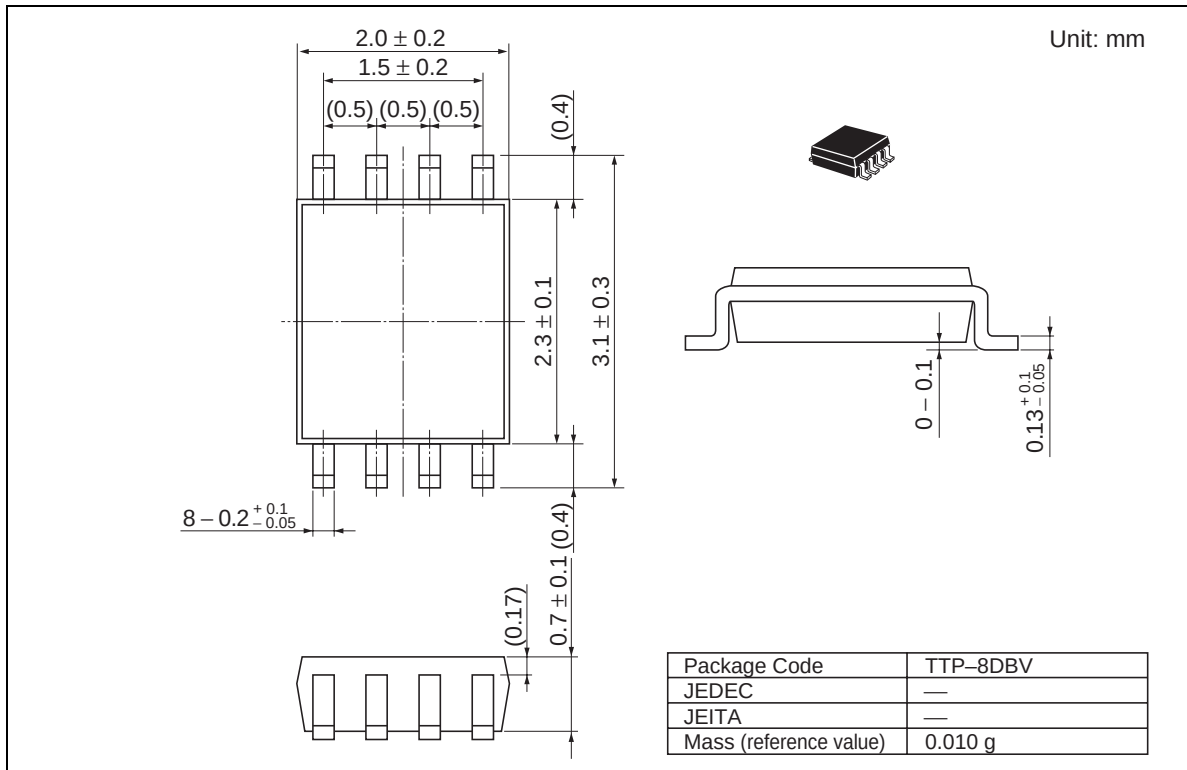


• Waveforms – 2



- Notes:
1. Input waveform : $PRR \leq 1 \text{ MHz}$, $Z_o = 50 \Omega$, $t_r \leq 3 \text{ ns}$, $t_f \leq 3 \text{ ns}$.
 2. Waveform – A is for an output with internal conditions such that the output is low except when disabled by the output control.
 3. Waveform – B is for an output with internal conditions such that the output is high except when disabled by the output control.
 4. The output are measured one at a time with one transition per measurement.

Package Dimensions



Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.
Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
 2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
 3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
The information described here may contain technical inaccuracies or typographical errors.
Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (<http://www.renesas.com>).
 4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
 5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.
-



RENESAS SALES OFFICES

<http://www.renesas.com>

Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom
Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbH
Dornacher Str. 3, D-85622 Feldkirchen, Germany
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd.
7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2375-6836

Renesas Technology Taiwan Co., Ltd.
FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001