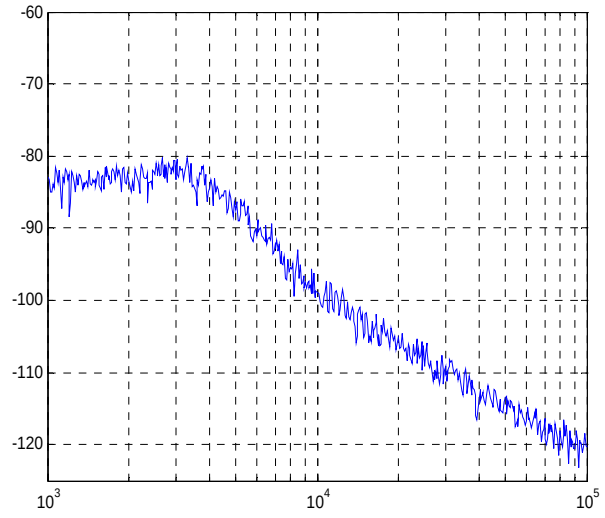


PHASE NOISE (1 Hz BW, typical)

$\mathcal{L}(f)$ (dBc/Hz)



OFFSET (Hz)

FEATURES

- Frequency Range: 2685 - 2715 MHz
- Step Size: 1000 KHz
- PLL - Style Package

APPLICATIONS

- Telecommunications
- Satellite
- Telemetry

PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	2685 - 2715	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-100	dBc/Hz
Harmonic Suppression (2nd, typ.)	-25	dBc
Sideband Spurs (typ.)	-75	dBc
Power Output	1±2	dBm
Load Impedance	50	Ω
Step Size	1000	kHz
Charge Pump Output Current	1250	μ A
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	$^{\circ}$ C
Package Style	PLL	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	33	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-202 : PLL Functional Description

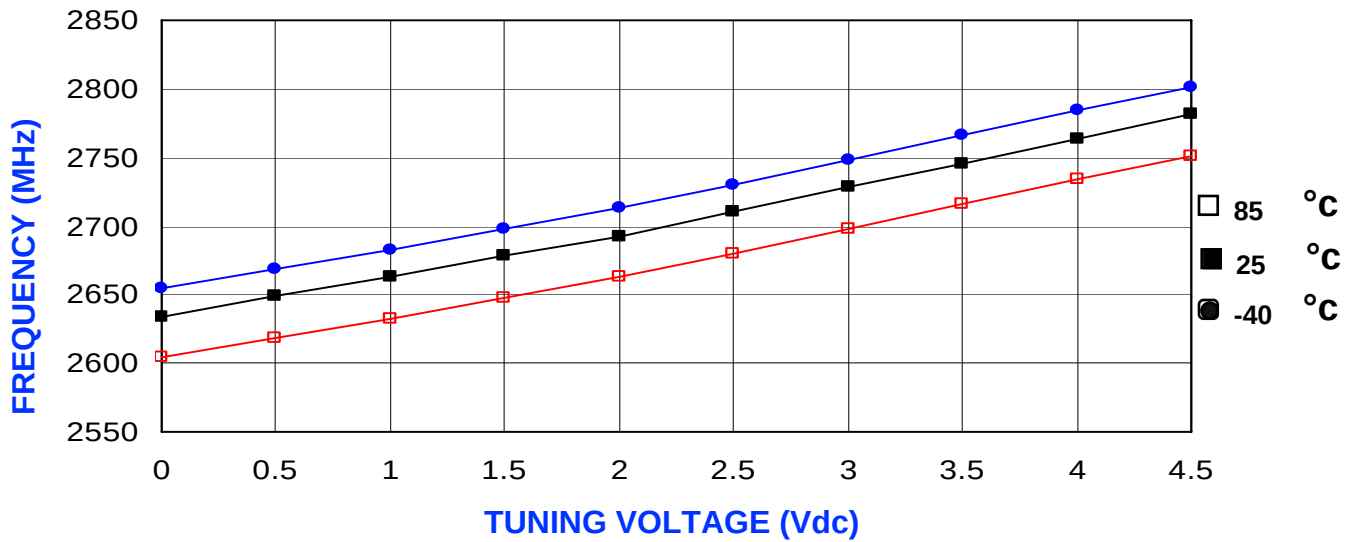
NOTES:

Reference Oscillator Signal: 5 MHz < f_{osc} < 100 MHz
Frequency Synthesizer: Analog Devices - ADF4106

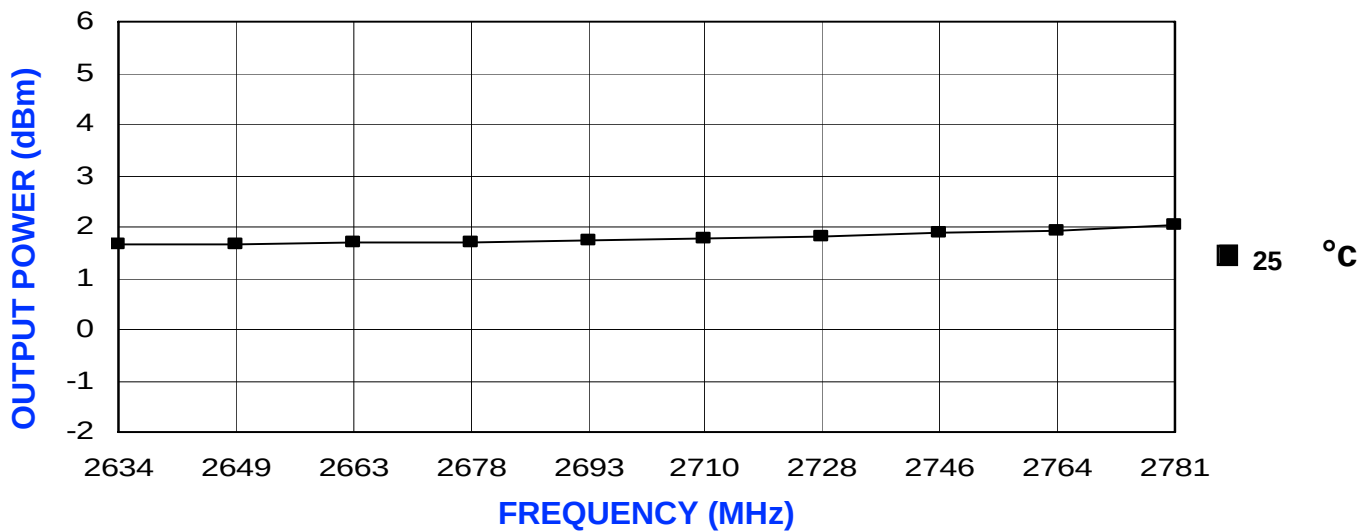
Prescaler: 32

PHASE LOCKED LOOP

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

