

PMEG2015EH; PMEG2015EJ

20 V, 1.5 A very low V_F MEGA Schottky barrier rectifiers

Rev. 03 — 15 January 2010

Product data sheet

1. Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifiers with an integrated guard ring for stress protection encapsulated in small and flat SMD plastic packages.

Table 1. Product overview

Type number	Package		Configuration
	NXP	JEITA	
PMEG2015EH	SOD123F	-	single diode
PMEG2015EJ	SOD323F	SC-90	single diode

1.2 Features

- Forward current: ≤ 1.5 A
- Reverse voltage: ≤ 20 V
- Very low forward voltage
- Small and flat lead SMD plastic packages

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low and medium power general applications

1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_F	forward current	$T_{sp} \leq 55$ °C	-	-	1.5	A
V_R	reverse voltage		-	-	20	V
V_F	forward voltage	$I_F = 1.5$ A	[1] -	560	660	mV

[1] Pulse test: $t_p \leq 300$ μ s; $\delta \leq 0.02$.

2. Pinning information

Table 3. Pinning

Pin	Description	Simplified outline	Symbol
1	cathode	 001aab540	 sym001
2	anode		

[1] The marking bar indicates the cathode.

3. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2015EH	-	plastic surface mounted package; 2 leads	SOD123F
PMEG2015EJ	SC-90	plastic surface mounted package; 2 leads	SOD323F

4. Marking

Table 5. Marking codes

Type number	Marking code
PMEG2015EH	AD
PMEG2015EJ	EL

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _R	reverse voltage		-	20	V
I _F	forward current	T _{sp} ≤ 55 °C	-	1.5	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25	-	5.5	A
I _{FSM}	non-repetitive peak forward current	square wave; t _p = 8 ms	[1] -	9	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
	PMEG2015EH		[1] -	375	mW
			[2] -	830	mW
	PMEG2015EJ		[1] -	360	mW
			[2] -	830	mW
T _j	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Table 6. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
T_{stg}	storage temperature		-65	+150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

6. Thermal characteristics

Table 7. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air				
		PMEG2015EH	[1][2] -	-	330	K/W
			[2][3] -	-	150	K/W
	PMEG2015EJ		[1][2] -	-	350	K/W
			[2][3] -	-	150	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point					
		PMEG2015EH	-	-	60	K/W
		PMEG2015EJ	-	-	55	K/W

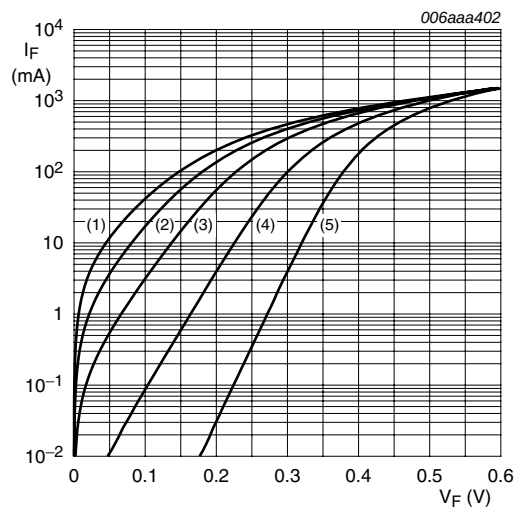
- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] For Schottky barrier diodes thermal run-away has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and $I_{F(AV)}$ rating are available on request.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

7. Characteristics

Table 8. Characteristics $T_{amb} = 25\text{ °C}$ unless otherwise specified.

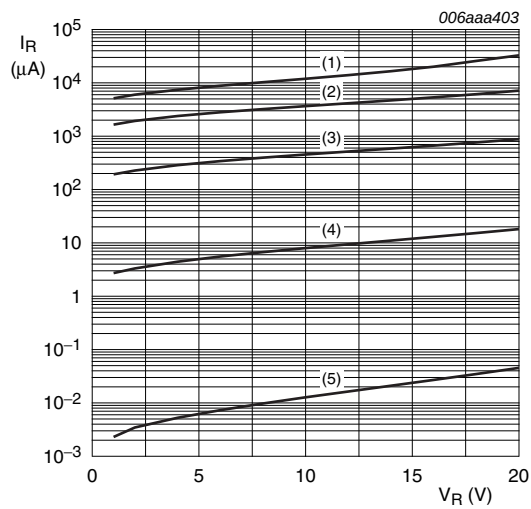
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 10\text{ mA}$	[1] -	240	270	mV
		$I_F = 100\text{ mA}$	[1] -	300	350	mV
		$I_F = 500\text{ mA}$	[1] -	400	460	mV
		$I_F = 1\text{ A}$	[1] -	480	550	mV
		$I_F = 1.5\text{ A}$	[1] -	560	660	mV
I_R	reverse current	$V_R = 5\text{ V}$	-	5	10	μA
		$V_R = 8\text{ V}$	-	7	20	μA
		$V_R = 10\text{ V}$	-	8	30	μA
		$V_R = 15\text{ V}$	-	10	50	μA
		$V_R = 20\text{ V}$	-	15	70	μA
C_d	diode capacitance	$V_R = 1\text{ V}; f = 1\text{ MHz}$	-	40	50	pF

- [1] Pulse test: $t_p \leq 300\text{ μs}$; $\delta \leq 0.02$.



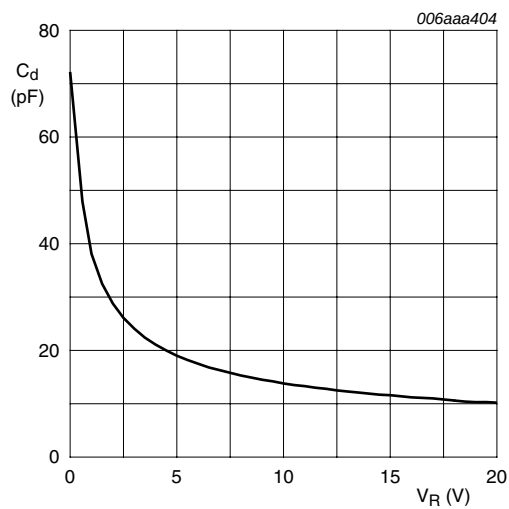
- (1) $T_{amb} = 150\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (5) $T_{amb} = -40\text{ }^{\circ}\text{C}$

Fig 1. Forward current as a function of forward voltage; typical values



- (1) $T_{amb} = 150\text{ }^{\circ}\text{C}$
- (2) $T_{amb} = 125\text{ }^{\circ}\text{C}$
- (3) $T_{amb} = 85\text{ }^{\circ}\text{C}$
- (4) $T_{amb} = 25\text{ }^{\circ}\text{C}$
- (5) $T_{amb} = -40\text{ }^{\circ}\text{C}$

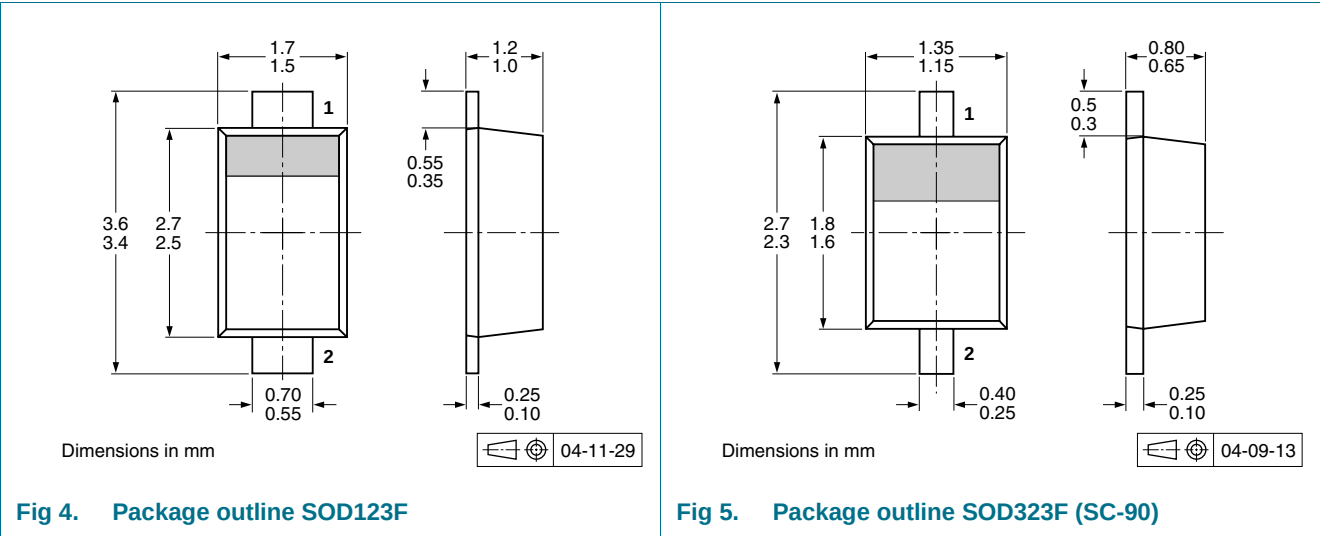
Fig 2. Reverse current as a function of reverse voltage; typical values



$T_{amb} = 25\text{ }^{\circ}\text{C}$; $f = 1\text{ MHz}$

Fig 3. Diode capacitance as a function of reverse voltage; typical values

8. Package outline



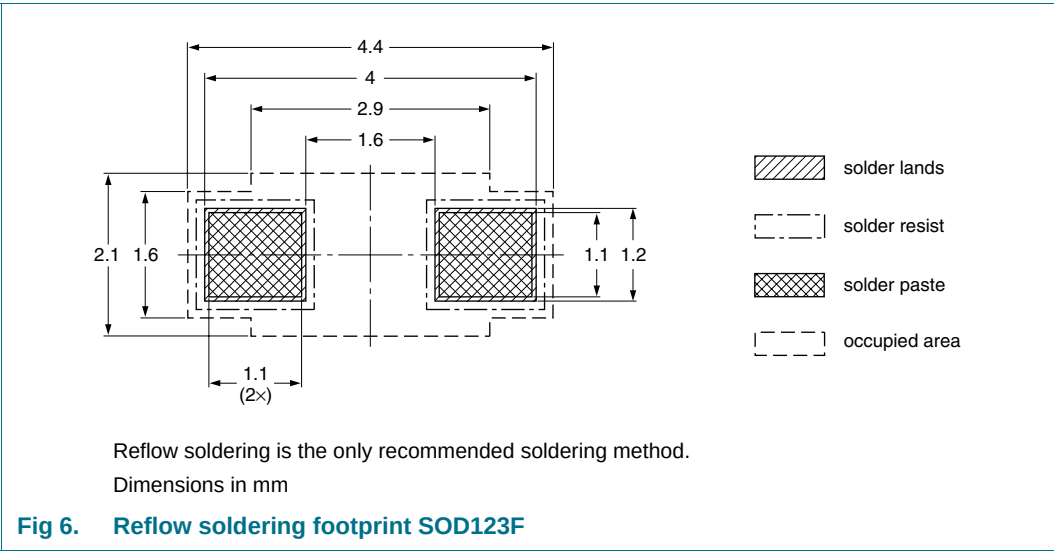
9. Packing information

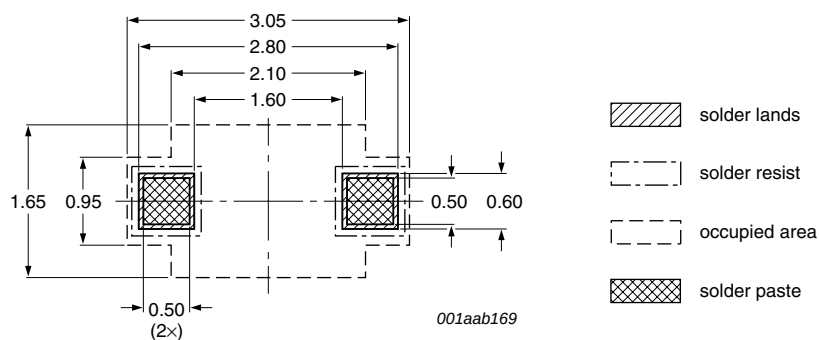
Table 9. Packing methods
The -xxx numbers are the last three digits of the 12NC ordering code.^[1]

Type number	Package	Description	Packing quantity	
			3000	10000
PMEG2015EH	SOD123F	4 mm pitch, 8 mm tape and reel	-115	-135
PMEG2015EJ	SOD323F			

[1] For further information and the availability of packing methods, see [Section 13](#).

10. Soldering





Reflow soldering is the only recommended soldering method.

Dimensions in mm

Fig 7. Reflow soldering footprint SOD323F (SC-90)

11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2015EH_EJ_3	20100115	Product data sheet	-	PMEG2015EH_EJ_2
Modifications:	This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content.			
PMEG2015EH_EJ_2	20050407	Product data sheet	-	PMEG2015EJ_1
PMEG2015EJ_1	20050302	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

12.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

12.3 Disclaimers

General — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental

damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

13. Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

14. Contents

1	Product profile	1
1.1	General description	1
1.2	Features	1
1.3	Applications	1
1.4	Quick reference data	1
2	Pinning information	2
3	Ordering information	2
4	Marking	2
5	Limiting values	2
6	Thermal characteristics	3
7	Characteristics	3
8	Package outline	5
9	Packing information	5
10	Soldering	5
11	Revision history	7
12	Legal information	8
12.1	Data sheet status	8
12.2	Definitions	8
12.3	Disclaimers	8
12.4	Trademarks	8
13	Contact information	8
14	Contents	9

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

founded by

PHILIPS

© NXP B.V. 2010.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 15 January 2010

Document identifier: PMEG2015EH_EJ_3